

Direct and Cross-Coupling Audio-Susceptibilities of the Peak Current-Mode Controlled Independent-Input Series-Output Boost Converter

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This work studies the small-signal direct and cross-coupling audio-susceptibilities of the independent-input series-output boost converter with peak current-mode control. The converter functions in the continuous-conduction mode and comprises *n*-connected identical boost modules whose inputs are fed from separate voltage sources and outputs connected in series. Expressions for the module direct (self) and cross-coupling audio-susceptibilities are derived in symbolic form. The expressions explicitly show (*n*) as a variable and take into account the sampling action of the current loops. In addition, audio-susceptibility frequency responses following the closure of the voltage feedback loops are generated and the influence of increasing *n* is discussed. Detailed simulations using PSIM are performed to support the analysis.

1. INTRODUCTION

Over the past few decades, the modular connection of dc-dc converters has shown effectiveness in reducing current and voltage stresses on the participating semiconductor devices and also in increasing the conversion system reliability. A modular converter allows power to be processed by a number of converter modules connected in different combinations to fulfil certain input/output requirements. The four well-known conventional arrangements of modular converters are: the parallel-input/parallel-output (PIPO), the parallel-input/seriesoutput (PISO), the series-input/parallel-output (SIPO), and the series-input/series-output (SISO); the works [1, 2] are comprehensive references on the subject. Other arrangements, however, have appeared in the literature like the seriesparallel-input/series-output [3], the series-input/seriesparallel-output [4], and the independent-input arrangements, namely the independent-input/series-output (IISO) [5-15], and the independent-input/parallel-output (IIPO) [16-19].

Several control methodologies have been proposed for these modular converters, among them is the peak current-mode control (PCMC) technique due to its renowned merits such as fast response, stable current sharing and precise output voltage regulation [1]. PCMC has been used for the control of PIPO [20-23], PISO [24-26], SIPO [27-29], SISO [30, 31], IISO [14], and IIPO [16] converters. Initial examination of the dynamics of these systems is dominantly done with the help of linearised small-signal (SS) models. Better insight into the converter performance can be obtained if SS modeling is supported by transfer function expressions showing how the system behaves as the number of modules is varied.

The SS line-to-output voltage transfer function (audiosusceptibility) of a dc-dc converter cell is an important dynamic performance parameter. It describes the converter's ability to attenuate SS input voltage disturbances. The audiosusceptibilities of several PCMC single-stage dc-dc converters have been formally studied [32-36]. Also many papers addressed the audio-susceptibilities of the conventional PCMC single-input modular converters [20, 24, 26, 28]. Nevertheless, susceptibilities of PCMC modular converters supplied from *n*-independent sources and how these susceptibilities react to varying the number of modules under open and closed-loop conditions have never been treated in the literature. In such converters like the IISO and the IIPO there are two audio-susceptibilities to consider: 1) The direct (self) audio-susceptibility which defines the output voltage response of a certain module due to a disturbance in its input voltage; and 2) the cross-coupling audio- susceptibility which gives the output voltage response of one module due to a disturbance in the source voltage of another.

The objective of this work is to study analytically the SS direct and cross-coupling audio-susceptibilities of an IISO PCMC boost converter intended for dc power supplies applications. Converters using the IISO structure have the distinct feature of being supplied from independent voltage sources; with higher output voltages obtained by the series connection of the outputs of a number of single-cell converters. The pioneer work on IISO dc-dc converters dates back to the 1990's [5] where a two module IISO boost configuration was used to interface a small-size wind-photovoltaic system to the utility circuit. Since then many articles have been written on IISO converters for different applications related to renewable energy systems, distributed power systems and dedicated dc power supplies, but none of them have employed PCMC for the control of the constituent modules until recently [14] where the ramp-compensated PCMC is used to control an IISO boost converter. The work [14], however, has only focused on the control-to-output voltage SS responses produced numerically using Simulink software. Other performance parameters like audio-susceptibility and cross-coupling effect have not been considered.

The contributions of this paper are: 1) the SS direct and cross-coupling audio-susceptibilities of the PCMC IISO boost converter are analytically studied. Symbolic expressions for these transfers are derived after establishing a small-signal model based on state-space equations. The expressions explicitly show the number of used cells (n) and take into account the sampling action of each of the converter's current loops. These expressions help the user to readily plot SS responses for different n without needing a circuit simulator; 2) the study also addresses the effect of closing the voltage feedback loops on the audio-susceptibilities when classical controllers are employed. To validate the analysis, two, three, and four-module converters are implemented using PSIM and frequency responses are obtained using the "ac sweep" tool which allows the user to obtain the frequency response while the converter is in its switched-mode form.

The IISO converter considered in this study is depicted in Figure 1. Several identical PCMC boost modules are operated in the continuous-current conduction mode and joined in series at the output side to supply a common load. A separate voltage source feeds each of the independently controlled modules. The PCMC of each module in Figure 1 can be briefly reviewed as follows: Converter switching cycle is started by a constantfrequency clock. Transistor's duty ratio (D) is set when the inductor current, after being sensed by (R_i) , becomes equal to a value that depends on control voltage (V_c) , produced by the compensated error amplifier (EA) circuit, and an external ramp slope (S_e) . This ramp is necessary to stabilize the current loop if D is greater than 0.5 [32]. Also in Figure 1, the attenuation factor of the module output voltage is denoted by K_v while F_v represents the module voltage-loop compensator transfer function.

The following sections of this paper are: Section 2 presents the converter SS model. Section 3 studies the direct and crosscoupling audio-susceptibilities with closed current loops and voltage loops left open. Section 4 addresses the effect of closing the voltage feedback loops on the audiosusceptibilities. Finally, Section 5 contains the conclusion.



Figure 1. IISO converter schematic

2. SMALL-SIGNAL MODELING

2.1 Modeling the power stage

To develop the SS model of the multi-module PCMC converter, a two module converter is considered first. Referring to Figure 1, the input and output voltages of each module is denoted by V_g and V_o respectively; the module inductor current is indicated by I_L , and the transistor duty ratio of each module is symbolised by D. Using state-space averaging and linearization [37], the power-stage SS model of a two-module IISO boost converter assuming ideal components can be characterized as in Ref. [14] by:

$$\dot{x} = \frac{d}{dt} \begin{bmatrix} \hat{i}_{L1} \\ \hat{v}_{o1} \\ \hat{i}_{L2} \\ \hat{v}_{o2} \end{bmatrix} = [A] \begin{bmatrix} \hat{i}_{L1} \\ \hat{v}_{o1} \\ \hat{i}_{L2} \\ \hat{v}_{o2} \end{bmatrix} + [B] \begin{bmatrix} \dot{d}_{1} \\ \dot{d}_{2} \\ \hat{v}_{g1} \\ \hat{v}_{g2} \\ \hat{i}_{o} \end{bmatrix}$$
(1a)

where

$$[A] = \begin{bmatrix} 0 & \frac{-D_1'}{L_1} & 0 & 0 \\ \frac{D_1'}{C_1} & \frac{-1}{RC_1} & 0 & \frac{-1}{RC_1} \\ 0 & 0 & 0 & \frac{-D_2'}{L_2} \\ 0 & \frac{-1}{RC_2} & \frac{D_2'}{C_2} & \frac{-1}{RC_2} \end{bmatrix}$$
(1b)
$$B] = \begin{bmatrix} \frac{V_{g1}}{D_1'L_1} & 0 & \frac{1}{L_1} & 0 & 0 \\ \frac{-2V_{g1}}{D_1'^2RC_1} & 0 & 0 & 0 & \frac{1}{C_1} \\ 0 & \frac{V_{g2}}{D_2'L_2} & 0 & \frac{1}{L_2} & 0 \\ 0 & \frac{-2V_{g2}}{D_2'^2RC_2} & 0 & 0 & \frac{1}{C_2} \end{bmatrix}$$
(1c)

where the hat symbol (^) is used for SS changes; and D' = 1 - D.

2.2 Modeling the PCMC stage

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The PCMC stage of each module is modeled in a similar fashion to that of the single boost dc-dc converter [32, 33]. The SS model for a 2-module converter is illustrated in Figure 2(a). Each module is comprised of: The current sensing resistance R_i ; the PWM modulator gain F_m ; the current loop sampling gain H_e ; and feedforward gains K_f and K_r formed as a result of closing the current loop of each module. Variations in the inductor voltage during the ON and OFF intervals of the transistor are respectively denoted as \hat{v}_{on} and \hat{v}_{off} . Model parameters are given in Table 1. The module duty ratio with only the current loop closed is:

$$\hat{d} = F_m(\hat{v}_c - R_i H_e \hat{\iota}_L + K_f \hat{v}_{on} + K_r \hat{v}_{off})$$
(2)

After the application of Laplace transforms to (1) and the substitution for duty ratio (\hat{d}) from (2), we get the following when the modules are identical:

$$\begin{bmatrix} \hat{s}\hat{\iota}_{L1} \\ \hat{s}\hat{\upsilon}_{01} \\ \hat{s}\hat{\iota}_{L2} \\ \hat{s}\hat{\upsilon}_{02} \end{bmatrix} = \begin{bmatrix} A_1 & A_2 & 0 & 0 \\ A_3 & A_4 & 0 & A_5 \\ 0 & 0 & A_1 & A_2 \\ 0 & A_5 & A_3 & A_4 \end{bmatrix} \begin{bmatrix} \hat{\iota}_{L1} \\ \hat{\upsilon}_{01} \\ \hat{\iota}_{L2} \\ \hat{\upsilon}_{02} \end{bmatrix} + \begin{bmatrix} B_1 & 0 & B_2 & 0 & 0 \\ B_3 & 0 & B_4 & 0 & B_5 \\ 0 & B_1 & 0 & B_2 & 0 \\ 0 & B_3 & 0 & B_4 & B_5 \end{bmatrix} \begin{bmatrix} \hat{\upsilon}_{c1} \\ \hat{\upsilon}_{c2} \\ \hat{\upsilon}_{g1} \\ \hat{\upsilon}_{g2} \\ \hat{\iota}_{0} \end{bmatrix}$$
(3)

The A and B elements are tabulated in Table 2, and parameters F_m , H_e , K_r and K_f are as given in Table 1.

The set of Eq. (3) represents the converter SS model with only the current loops closed.



(b) Structure of Type II compensator

Figure 2. Two-module small-signal model

Table 1. Parameters of figure 2(a)

$F_m = \frac{1}{(S_n + S_e)T} = \frac{L}{M_c R_i V_g T} \text{where } M_c = 1 + \frac{S_e}{S_n}$			
and $S_n = V_g R_i / L$			
$H_e \cong \left(1 + \frac{s}{\omega_n Q_z} + \frac{s^2}{\omega_n^2}\right)$ where $Q_z = -2/\pi$ and $\omega_n = \pi/T$			
$K_f = -\frac{DTR_i(1-0.5D)}{L} + \frac{D^2T^2R_i(3-2D)}{12L}s$ and $K_r = \frac{D'^2TR_i}{2L}$			
$\hat{v}_{on} = \hat{v}_{g}$; $\hat{v}_{off} = \hat{v}_o - \hat{v}_g$			
$F_{\nu} = \frac{k_i(1+s/\omega_z)}{s(1+s/\omega_p)}$			

Table 2. Summary of the expressions of Eq. (3)

A_1	$\frac{-V_{o,\text{module}}R_iH_eF_m}{L}$	B_1	$\frac{V_{o,\text{module}}F_m}{L}$
<i>A</i> ₂	$\frac{-D' + V_{o,\text{module}}F_m K_r}{L}$	B_2	$\frac{1+V_{o,\mathrm{module}}F_m(K_f-K_r)}{L}$
A_3	$\frac{D' + I_{L,\text{module}} R_i H_e F_m}{C}$	B_3	$\frac{-I_{L,\text{module}}F_m}{C}$
A_4	$\frac{-1}{C} \left(\frac{1}{R} + I_{L,\text{module}} F_m K_r \right)$	B_4	$\frac{-I_{L,\text{module}}F_m(K_f - K_r)}{C}$
A_5	$\frac{-1}{RC}$	B_5	$\frac{1}{C}$

2.3 Modeling the voltage feedback loop stage

Referring to Figure 2(a), the voltage feedback loop of each module consists of an attenuator K_v and a classical (type II) compensator represented by F_v . The compensator structure is depicted in Figure 2(b). With voltage and current feedback loops closed, Eq. (3) is updated by substituting for the control voltages v_{C1} and v_{C2} which can be expressed as:

$$\hat{v}_{c1} = \left(\hat{v}_{ref1} - K_{v1}\hat{v}_{o1}\right)F_{v1}$$
(4a)

$$\hat{v}_{c2} = \left(\hat{v}_{ref2} - K_{\nu 2}\hat{v}_{o2}\right)F_{\nu 2} \tag{4b}$$

The SS model for the 2-module converter is represented as:

$$= \begin{bmatrix} A_{1} & A_{2} - B_{1}K_{v1}F_{v1} & 0 & 0 \\ A_{3} & A_{4} - B_{3}K_{v1}F_{v1} & 0 & A_{5} \\ 0 & 0 & A_{1} & A_{2} - B_{1}K_{v2}F_{v2} \\ 0 & A_{5} & A_{3} & A_{4} - B_{3}K_{v2}F_{v2} \end{bmatrix} \begin{bmatrix} \hat{\imath}_{L1} \\ \hat{\vartheta}_{o1} \\ \hat{\imath}_{L2} \\ \hat{\vartheta}_{o2} \end{bmatrix}$$
(5)
$$+ \begin{bmatrix} B_{1}F_{v1} & 0 & B_{2} & 0 & 0 \\ B_{3}F_{v1} & 0 & B_{4} & 0 & B_{5} \\ 0 & B_{1}F_{v2} & 0 & B_{2} & 0 \\ 0 & B_{3}F_{v2} & 0 & B_{4} & B_{5} \end{bmatrix} \begin{bmatrix} \hat{\imath}_{ref1} \\ \hat{\imath}_{g2} \\ \hat{\imath}_{g1} \\ \hat{\imath}_{g2} \\ \hat{\imath}_{a} \end{bmatrix}$$

3. AUDIO-SUSCEPTIBILITIES WITH ONLY THE CURRENT LOOPS CLOSED

3.1 Module direct audio-susceptibility

The direct (self) audio-susceptibility of a certain module is the output voltage response of that module due to a disturbance in its input voltage. The case of a two-module converter (n = 2) will be used as a starting point to reach a general SS expression for the audio-susceptibilities with *n*-connected modules. Referring to (3), the module direct audiosusceptibility when n = 2 can be expressed as:

$$\begin{aligned} &\frac{\hat{v}_{o1}}{\hat{v}_{g1}} = \frac{\hat{v}_{o2}}{\hat{v}_{g2}} \\ &= \frac{\Delta_1 [s^2 - (A_1 + A_4)s + A_1A_4 - A_2A_3]}{\Delta_2 [s^2 - (A_1 + A_4 + A_5)s + A_1(A_4 + A_5) - A_2A_3]} \end{aligned}$$
(6)

$$\Delta_1 = B_4(s - A_1 + A_3 B_2 / B_4) \tag{7a}$$

$$\Delta_2 = s^2 - (A_1 + A_4 - A_5)s + A_1(A_4 - A_5) - A_2A_3 \quad (7b)$$

A general expression for the direct audio-susceptibility of a converter with *n*-connected modules can be concluded when we find the susceptibilities of the three and four-module converters which can be reached by following the steps used above with the two-module case.

When n = 3, the direct audio-susceptibility can be derived as:

$$\frac{\hat{v}_{o1}}{\hat{v}_{g1}} = \frac{\hat{v}_{o2}}{\hat{v}_{g2}} = \frac{\hat{v}_{o3}}{\hat{v}_{g3}}$$
$$= \frac{\Delta_1 [s^2 - (A_1 + A_4 + A_5)s + A_1(A_4 + A_5) - A_2A_3]}{\Delta_2 [s^2 - (A_1 + A_4 + 2A_5)s + A_1(A_4 + 2A_5) - A_2A_3]}$$
(8)

And for n = 4

$$\frac{\hat{v}_{o1}}{\hat{v}_{g1}} = \frac{\hat{v}_{o2}}{\hat{v}_{g2}} = \frac{\hat{v}_{o3}}{\hat{v}_{g3}} = \frac{\hat{v}_{o4}}{\hat{v}_{g4}}$$

$$= \frac{\Delta_1 [s^2 - (A_1 + A_4 + 2A_5)s + A_1(A_4 + 2A_5) - A_2A_3]}{\Delta_2 [s^2 - (A_1 + A_4 + 3A_5)s + A_1(A_4 + 3A_5) - A_2A_3]}$$
(9)

In general, for the *n*-connected modules shown in Figure 1, the module direct audio-susceptibility is:

$$\frac{\hat{v}_{on}}{\hat{v}_{gn}} = \frac{\Delta_1}{\Delta_2} \times$$

$$\frac{[s^2 - (A_1 + A_4 + (n-2)A_5)s + A_1(A_4 + (n-2)A_5) - A_2A_3]}{[s^2 - (A_1 + A_4 + (n-1)A_5)s + A_1(A_4 + (n-1)A_5) - A_2A_3]}$$
(10)

The expression is 5^{th} order for the numerator and 6^{th} for the denominator when we substitute for the *A* and *B* terms given in Table 2. After performing these substitutions and rearranging terms the resultant expression is programmed into Matlab with the following parameters for each module:

$$V_g = (48$$
/number of modules) V; $D = 0.6$; $R = 30 \Omega$;
 $T = 10 \mu$ s; $L = 115 \mu$ H; $C = 40 \mu$ F; $R_i = 0.1 \Omega$

The direct audio-susceptibility can be evaluated for different slope ratio (M_c) values by using Eq. (10).

Figure 3 shows the direct audio-susceptibility responses as we vary *n* with slope ratio $M_c = 1.5$ while Figure 4 depicts the responses when $M_c = 2.9$. The slope-ratio values are selected to study the underdamped and critically-damped behaviour. Figures 3 and 4 also present PSIM "ac sweep" results to validate the analytical model predictions. Matlab pole-zero locations corresponding to these cases are given in Table 3. In the module audio-susceptibility response, at low frequencies, one can notice that there are two real left-half s-plane poles sandwiching a real zero. At $\frac{1}{2}$ the switching frequency ($f_s/2$), the response is influenced by a complex pair of poles in the left-half plane as a result of the sample-and-hold (S/H) effect of PCMC, and hence the peaking in Figure 3 when $M_c = 1.5$. This double pole however splits up into two single real poles with $M_c = 2.9$. The S/H effect also creates a complex pair of left-half-plane zeros in the module audio-susceptibility response at frequencies higher than $f_s/2$. Figures 3 and 4 show that increasing the number of modules increases the direct audio-susceptibility, but does not have a noticeable effect on the peaking at $f_s/2$; and critical damping of the responses is achieved for all cases with the same slope ratio.



(b) PSIM simulation results

Figure 3. Direct audio-susceptibility with current loops closed and variable number of modules with ($M_c = 1.5$)



Figure 4. Direct audio-susceptibility with current loops closed and variable number of modules with ($M_c = 2.9$)

Table 3. Pole-zero locations in (rad/sec) of direct audio-susceptibility with variable n

		$M_{C} = 1.5$			$M_C = 2.9$	
	n = 2	<i>n</i> = 3	n = 4	n = 2	<i>n</i> = 3	n = 4
Zeros	1.0e+05 *					
	-0.1189 + 4.6336i	-0.0792 + 4.6218i	-0.0594 + 4.6157i	-0.1189 + 4.8924i	-0.0792 + 4.7963i	-0.0594 + 4.7474i
	-0.1189 - 4.6336i	-0.0792 - 4.6218i	-0.0594 - 4.6157i	-0.1189 - 4.8924i	-0.0792 - 4.7963i	-0.0594 - 4.7474i
	-0.3940 + 3.1066i	-0.3940 + 3.1015i	-0.3938 + 3.0965i	-3.6775 + 0.0000i	-3.7480 + 0.0000i	-3.8112 + 0.0000i
	-0.3940 - 3.1066i	-0.3940 - 3.1015i	-0.3938 - 3.0965i	-2.6353 + 0.0000i	-2.5643 + 0.0000i	-2.5005 + 0.0000i
	-0.0265 + 0.0000i	-0.0350 + 0.0000i	-0.0435 + 0.0000i	-0.0288 + 0.0000i	-0.0376 + 0.0000i	-0.0465 + 0.0000i
Poles	1.0e+05 *					
	-0.3941 + 3.1066i	-0.3940 + 3.1015i	-0.3939 + 3.0965i	-3.6779	-3.7485	-3.8117
	-0.3941 - 3.1066i	-0.3940 - 3.1015i	-0.3939 - 3.0965i	-3.6771	-3.7475	-3.8106
	-0.3940 + 3.1066i	-0.3939 + 3.1015i	-0.3938 + 3.0965i	-2.6358	-2.5650	-2.5013
	-0.3940 - 3.1066i	-0.3939 - 3.1015i	-0.3938 - 3.0965i	-2.6348	-2.5636	-2.4996
	-0.0349 + 0.0000i	-0.0434 + 0.0000i	-0.0520 + 0.0000i	-0.0373	-0.0461	-0.0552
	-0.0181 + 0.0000i	-0.0266 + 0.0000i	-0.0351 + 0.0000i	-0.0203	-0.0290	-0.0379

3.2 Module cross-coupling audio-susceptibility

The cross-coupling audio-susceptibility is the output voltage response of one module due to a disturbance in the source voltage of another. Following the same procedure used above for deriving the direct audio-susceptibility, the crosscoupling audio susceptibility can be derived as:

$$\frac{\left(\frac{\hat{v}_{on}}{\hat{v}_{gn}}\right)_{cross} = \frac{\Delta_1}{\Delta_2} \times \\ \frac{A_5(s - A_1)}{[s^2 - (A_1 + A_4 + (n - 1)A_5)s + A_1(A_4 + (n - 1)A_5) - A_2A_2]}$$
(11)

where, Δ_1 and Δ_2 are as given by Eq. (7); and A_1 -to- A_5 are defined in Table 2.



(b) PSIM simulation results

Figure 5. Cross-coupling audio-susceptibility with current loops closed and variable number of modules ($M_c = 1.5$)

Figure 5 shows the predicted cross-coupling audiosusceptibility responses as we vary *n* with slope ratio $M_c = 1.5$ while Figure 6 depicts the responses when $M_c = 2.9$. Corresponding Matlab pole-zero locations appear in Table 4; the denominator of this transfer function is of course the same as that of the direct audio-susceptibility, but the numerator is one order lower. From Figures 5 and 6, it can be noticed that: 1) the cross-coupling audio-susceptibility is less than the direct audio-susceptibility; 2) At low frequencies the audiosusceptibility is reduced with the addition of modules, but as perturbation frequency rises, the susceptibility will increase when modules are added.



(b) PSIM simulation results

Figure 6. Cross-coupling audio-susceptibility with current loops closed and variable number of modules ($M_c = 2.9$)

Table 4. Pole-zero locations in (rad/sec) for cross-coupling audio-susceptibility with variable n

		$M_{C} = 1.5$			$M_{C} = 2.9$	
	n = 2	<i>n</i> = 3	n = 4	n = 2	<i>n</i> = 3	n = 4
Zeros	1.0e+05 *					
	-0.1189 + 4.6336i	-0.0792 + 4.6218i	-0.0594 + 4.6157i	-0.1189 + 4.8924i	-0.0792 + 4.7963i	-0.0594 + 4.7474i
	-0.1189 – 4.6336i	-0.0792 - 4.6218i	-0.0594 – 4.6157i	-0.1189 – 4.8924i	-0.0792 - 4.7963i	-0.0594 - 4.7474i
	-0.3948 + 3.1167i	-0.3948 + 3.1167i	-0.3948 + 3.1167i	-3.4824 + 0.0000i	-3.4824 + 0.0000i	-3.4824 + 0.0000i
	-0.3948 - 3.1167i	-0.3948 - 3.1167i	-0.3948 - 3.1167i	-2.8341 + 0.0000i	-2.8341 + 0.0000i	-2.8341 + 0.0000i
Poles	1.0e+05 *					
	-0.3941 + 3.1066i	-0.3940 + 3.1015i	-0.3939 + 3.0965i	-3.6779	-3.7485	-3.8117
	-0.3941 - 3.1066i	-0.3940 - 3.1015i	-0.3939 - 3.0965i	-3.6771	-3.7475	-3.8106
	-0.3940 + 3.1066i	-0.3939 + 3.1015i	-0.3938 + 3.0965i	-2.6358	-2.5650	-2.5013
	-0.3940 - 3.1066i	-0.3939 – 3.1015i	-0.3938 - 3.0965i	-2.6348	-2.5636	-2.4996
	-0.0349 + 0.0000i	-0.0434 + 0.0000i	-0.0520 + 0.0000i	-0.0373	-0.0461	-0.0552
	-0.0181 + 0.0000i	-0.0266 + 0.0000i	-0.0351 + 0.0000i	-0.0203	-0.0290	-0.0379

4. AUDIO-SUSCEPTIBILITIES WITH ALL FEEDBACK LOOPS CLOSED

In order to find the module direct and cross-coupling audiosusceptibilities when the current and voltage feedback loops are closed, the general expressions given by Eqns. (10) and (11) can be used but after updating four terms according to Eq. (5). These terms are A_2 , A_4 , B_1 and B_3 which include the output voltage attenuation factor K_{ν} and the compensated error amplifier transfer function F_{ν} .

Designing the module voltage feedback loop compensator is based on its control-to-output voltage response when no peaking is present (i.e. when $M_c = 2.9$). The control-to-output voltage transfer function is obtained following the same approach used for deriving the audio-susceptibilities and can be expressed as:

$$\frac{\hat{v}_{on}}{\hat{v}_{cn}} = \frac{\Delta_3}{\Delta_2} \times \frac{[s^2 - (A_1 + A_4 + (n-2)A_5)s + A_1(A_4 + (n-2)A_5) - A_2A_3]}{[s^2 - (A_1 + A_4 + (n-1)A_5)s + A_1(A_4 + (n-1)A_5) - A_2A_3]}$$
(12)

where, Δ_2 is given by (7b) and

$$\Delta_3 = B_3(s - A_1 + A_3 B_1 / B_3) \tag{13}$$

where the A and B terms are in Table 2.

The compensator should be designed for the case when *n* is maximum (i.e. n = 4) to ensure that the system is stable when lower number of modules is employed [14]. Usual compensator design procedure used with single-cell converters [38, 39] can be applied for each module. The chosen voltage-loop crossover frequency and phase margin is 800 Hz and 60° respectively.



(a) Plots from analytical model



(b) PSIM simulation results

Figure 7. Direct (self) audio-susceptibility with current and voltage feedback loops closed, variable *n*, and $F_v = [6974 (1+s/1919) / s(1+s/13170)]$



Figure 8. Cross-coupling audio-susceptibility with current and voltage feedback loops closed, variable *n*, and $F_v = [6974 (1+s/1919) / s(1+s/13170)]$

Figures 7 and 8 show the direct and cross-coupling audiosusceptibilities with current and voltage feedback loops closed and variable number of modules. It can be seen that closing the voltage feedback loops benefit the region below the crossover frequency only. Lower audio-susceptibilities can be obtained if the module voltage loop has a higher crossover frequency, but in the IISO boost converter this crossover frequency is limited by the *n*-dependent right-half-plane zero which appears in the module \hat{v}_o/\hat{v}_c transfer function at the angular frequency $\omega = R(1 - D)^2/nL$.

Figure 7 also shows that increasing *n* will slightly reduce the direct audio-susceptibility at frequencies below 500 Hz; otherwise, any increase in the number of modules will increase the direct audio-susceptibility. As for the cross-coupling audio-susceptibility, Figure 8 shows that adding modules reduces the susceptibility at frequencies below 900 Hz (by \approx 7 dB when *n* is changed from 2 to 4); however, at higher perturbation frequencies this susceptibility increases when modules are added. Due to system's complexity, the relationship between audio-susceptibilities and the number of modules cannot be quantified by a simple formula, but with a computer program based on the expressions proposed in this work the designer can straightforwardly find the effect of different parameters on the converter audio-susceptibilities.

5. CONCLUSIONS

Symbolic small-signal expressions are presented for both the direct and cross-coupling audio-susceptibilities of the peak current-mode controlled independent-input series-output (IISO) boost dc-dc converter. The derived expressions contain the sample-and-hold effect of the current loops. Using these expressions, the frequency responses under closed current and voltage feedback loop conditions are generated for different number of modules (*n*). The analytical model results correlate well with the ones obtained from PSIM simulations up to half the switching frequency region.

With the inner (current) and outer (voltage) feedback loops closed, the following can be stated about the module audio-susceptibility: 1) for a certain number of modules the cross-coupling susceptibility is always less than the direct susceptibility; 2) direct susceptibility is slightly reduced when n is increased but only over a small frequency range below the voltage-loop crossover frequency; else, this susceptibility increases with the addition of modules; and 3) cross-coupling susceptibility decreases as n is increased, but only at frequencies below voltage-loop crossover frequency, otherwise, this audio-susceptibility will increase when modules are added.

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