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Reliability Improvement in Serial Multicellular Converters Based on STATCOM Control

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ABSTRACT

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Keywords:

flexible AC transmission system (FACTS), STATCOM, multilevel converter, serial multicellular converters, mathematical modeling, control, PS-PWM FACTS devices installation is required to guarantee high energy quality and power system stability. The present paper tackles the problem of voltage control, reactive power and power factor improvement in an electric power network by one of the AC transmission devices (FACTS) of the static synchronous parallel compensator type (STATCOM), using serial multicellular converters with different levels (three to six levels). Such converters with special attention paid to the active balancing of capacitor voltages in order to generate good quality waveforms and improve the harmonic content of the output voltage. The controller's ability and functionality are evidenced through a detailed theoretical analysis. We elaborate the compensator's operating model by stating its equivalent electrical diagram representation. This is followed by a mathematical modeling and a control model based on phase shifted pulse width modulation (PS-PWM) technique to monitor reactive power flow inside the system and maintain the voltage at the common connection point (PCC) at a steady value. The proposed system simulation is carried out using Matlab Simulink environment. Finally, a presentation and a discussion of the obtained results are provided.

1. INTRODUCTION

Over the last couple of years' electricity demand has considerably increased through industrialization and expansion. In addition to political, economic and environmental factors, new equipment such as lines, transformers, power plants... are needed. New transmission lines often fail to relieve overloaded transmission lines and provide a sufficient stability margin to diminish cascading outages and blackouts risks [1, 2]. Addressing those problems entails further enhancement of the existing electrical system control. Flexible AC transmission devices (FACTS) are suggested to allow that improved system control.

These facilities operate based upon power electronics, and they provide new solutions to address these constraints or allow for better power flow control and management. They are intended to increase the power transit, regulate the voltage, and ensure a stable dynamic power transmission network [3, 4]. Static synchronous compensators "STATCOM" are connected in parallel at critical grid points as part of FACTS compensators featuring this capability. STATCOM is the first FACTS employing voltage source converter (VSC) [4]. This device uses high-power transistors type GTO (gate-turn-offoff) as well as IGBT (isolated-gate bipolar). This scheme has that characteristic to support voltage robustly under high disturbances, balancing asymmetric and fluctuating loads, and therefore; damping power oscillations [4, 5].

Typically, DSTATCOM deploys two-level VSCs [6]. Nevertheless, multilevel converters are increasingly implemented across STATCOM platforms owing to their high performance when compared to two-level converters [7].

Their numerous advantages include improving output

waveform quality, ability to run at high voltage/power conditions, and minimizing voltage stresses incurred by power switches. Additionally, they have a low dv/dt and the pulse frequency of each of the switches is lower than the apparent frequency of the voltage applied to the load [8, 9]. The multilevel converters which are frequently demonstrated in STATCOM applications are: flying capacitor multilevel converters (FCMC), diode multilevel converters (CHMC) [10].

Multi-level cascaded H-bridge converters (CHB) are the common topologies used in STATCOM implementations. CHB converters' advantages involve low switching loss, modularity, and absence of clamping diode or clamping capacitance. Alternatively, the necessity of several isolated DC sources is noted as its major drawback [11].

In fact, for 3-phase applications and a smaller number of levels, DCMCs are advantageous, as the capacitors are shared by the different branches [12]. Relative to DCMC, FCMC topology has the benefit of creating multiple voltage levels with a comparatively easy work and absence of looping diodes. Compared to DCMC, FCMC further eliminates an intermediate capacitor point to each switching pair, which results in a simple converter structure [12].

Out of the wide range of potential PWM implementations, multilevel sine-triangle PWM is the optimized one for a flying capacitor inverter [13].

Nonetheless, research studies on the optimal type of PWM schemes for an MMCC using a 5L-FC as the basic sub-module have been very limited.

The key specification required for a proper PWM approach would be the ability to keep the voltages across the submodule clamping capacitors balanced during all operating



states.

A presentation of the detailed power loss assessment of submodules, built by hybrid FC and NPC converters, regardless of the modulation method, was offered by [14, 15].

The authors provided a comparative analysis of three different sine-triangle PWM schemes for an FC-based MMCC in terms of waveform performance and power losses. Based on their attained findings, the researchers [16] found that the PD-PWM yields worse waveform performance in terms of THD values and higher losses due to equivalent series resistance (ESR) losses of the flying capacitors resulting from high voltage fluctuation. Simultaneously, the PS-PWM yields higher switching losses owing to its high equivalent switching frequency [16].

Referring to the presented approach, this paper's main aim is to implement a complex power electronics structure which consists of a multicellular converter with different levels (three, four, five and six) together with a PS-PWM control technique for managing voltage and compensating reactive energy during a grid point.

In Section II, the major component of the system will be discussed. Section III is devoted to analyzing the simulations results. The conclusion is reported in Section IV.

2. DESCRIPTION AND MODELLING

In this study, we use a conventional three-phase network consisting of a three-phase power source variable in amplitude, phase, and frequency feeding the two-phase electrical loads (fixed load and dynamic load) as shown in Figure 1.



Figure 1. Basic structure of a STATCOM coupled to the network



Figure 2. STATCOM equivalent diagram

The major component of the system is the STATCOM compensator to regulate the voltage at the common connection point (PCC) between the grid and the loads.

As Figure 2 reveals, the STATCOM is presented with a block diagram. It consists of a three-phase multicellular converter with a capacitor on the direct current (DC) side and a coupling inductor on the alternating current (AC) side, and it is connected to the power grid. The STATCOM regulates the voltage at the PCC by controlling the amount of reactive power injected or absorbed into the grid.

The operating principle of STATCOM is simple. By varying the magnitude of the output voltages produced, the exchange of reactive power between the inverter and the AC system is controlled by the magnitude of the voltage produced at the inverter output. There are three possibilities in Figure 3. First, if the amplitude of the voltage produced by the inverter (V_{can}) is higher than that of the AC system (V_{san}) , the reactive current (I_{ca}) will be injected into the system and the inverter will behave as a source of reactive power. If (V_{can}) is equal to the amplitude of the AC system voltage (V_{san}) , there will be no exchange of reactive power. Finally, if (V_{can}) is lower than that of the AC system (V_{san}) , the reactive that that of the AC system (V_{san}) , the reactive that the the AC system (V_{san}) is lower than that of the AC system (V_{san}) , the reactive current (I_{ca}) will be absorbed by the inverter.

As it is displayed in Figure 3 (b), when the current (I_{ca}) lags behind the voltage (V_{san}) by $\pi/2$, the device acts like a reactor, absorbing reactive power. When (I_{ca}) leads the voltage (V_{san}) by $\pi/2$, it acts like a capacitor, supplying reactive energy (Figure 3 (c)). If (V_{san}) = (V_{can}) the current goes through the reactance x is zero and there is no energy exchange (Figure 3 (a)) [17].



Figure 3. STATCOM operating modes [18]

2.1 Converter model

In order to develop a model of the multicellular series converter, the following simplifying assumptions: perfect switches, perfect sources and neglected dead times are taken into consideration. The three-phase multicellular series converter fed by a DC voltage source is demonstrated.

Perfect switches: The switching of the switches is instantaneous (zero closing and opening time) and without losses. Finally, the voltage drop in the switches is considered zero in conduction.

Perfect sources: The voltage at the terminals of the DC bus is constant and does not vary with the power exchanged.

Neglected dead time: The aim of our study is to reduce the switching frequency, so the dead times will have little influence.

The difference between the real and simplified model (Figure 4) is to linearize the studied model.



Figure 4. Multicellular series converter

Table 1. Characteristic quantities of the combination of P switching cells

Number of associated cells	Number of associated capacitors	Number of output voltage levels	Possible output levels
2	1	3	0, Vdc/2 et Vdc
3	2	4	0, Vdc/3, 2Vdc/3 et Vdc
4	3	5	0, Vdc/4, 2Vdc/4, 3Vdc/4 et Vdc
Р	P-1	P+1	0, Vdc/P, 2Vdc/P,, Vdc

It is based on the series connection of p nested cells separated by (p -1) floating capacitors. Each cell contains two power switches controlled in a complementary way [19, 20].

The modeling objective is to find a relation between both of the control and electrical quantities for the converter's AC and DC parts.

The multicellular converter is controlled by switching functions S_{ik} (K=a, b, c) denotes the phase and (i=1, 2, ...p denotes the cell concerned) whose value is between 0 and 1. If a switch is blocked, $S_{ik} = 0$ and if it is conducting, then $S_{ik} = 1$. Furthermore, when $S_{ik} = 1$, then its complement $\overline{S}_{ik} = 1$, so as not to short-circuit an arm of the converter.

The cell voltages can be expressed in terms of the capacitor voltages:

$$V_{celik} = V_{cik} - V_{c(i-1)k} \tag{1}$$

With $V_{c0k} = 0$ and $V_{cpk} = V_{dc}$.

Under steady state and equilibrium conditions, all cells have the same voltage level which is equal to:

$$V_{celik} = V_{dc}/p \tag{2}$$

Under these conditions the voltages across the capacitors take the values:

$$V_{cik} = i \times \frac{V_{dc}}{p} \tag{3}$$

The output voltages V_{ckO} of the converter with respect to point O are therefore given by:

$$V_{ck0} = \frac{V_{dc}}{p} \times \sum_{i=1}^{p} S_{ik} \tag{4}$$

where:

V_{ck0}: voltages between point O (-) and converter output points

of a, b, c phases; S: control switch; K = a, b, c: phase; V_{dc}: direct voltage; i = 1, 2, 3, ...p: cell concerned; p: cells number in each phase;

The characteristic quantities of the combination of P switching cells are defined in the following Table 1.

The voltages $V_{ckn} = V_{ckO} - V_{nO}$, (k=a, b, c), at the terminals of the load, with respect to the neutral point of potential $V_{nO} = (V_{aO} + V_{bO} + V_{cO})/3$, are written [21, 22]:

$$\begin{bmatrix} V_{can} \\ V_{cbn} \\ V_{ccn} \end{bmatrix} = \frac{1}{3} \begin{bmatrix} 2 & -1 & -1 \\ -1 & 2 & -1 \\ -1 & -1 & 2 \end{bmatrix} \times \begin{bmatrix} V_{a0} \\ V_{b0} \\ V_{c0} \end{bmatrix}$$
(5)

It can be inferred from the analysis of Figure 2 that the STATCOM state space Eq. (6) are obtained, where V_{sabc} represents the grid voltages at the PCC, V_{cabc} is the three-phase output voltage from the multicellular converter, and I_{cabc} is the three-phase balanced current flowing from the STATCOM [4, 23].

By applying Kirchhoff's second law (Law of meshes). We obtain the Eq. (6):

$$\begin{cases} V_{san} = rI_{ca} + L \frac{dI_{ca}}{dt} + V_{can} \\ V_{sbn} = rI_{cb} + L \frac{dI_{cb}}{dt} + V_{cbn} \\ V_{scn} = rI_{cc} + L \frac{dI_{cc}}{dt} + V_{ccn} \end{cases}$$
(6)

Or:

$$\begin{cases} V_{san} = V_M \cos(\omega t) \\ V_{sbn} = V_M \cos(\omega t - (2\pi/3)) \\ V_{scn} = V_M \cos(\omega t - (4\pi/3)) \end{cases}$$
(7)

Thus, the following equation defines the expression of the currents exchanged with the network:

$$\frac{d}{dt} \begin{bmatrix} I_{ca} \\ I_{cb} \\ I_{cc} \end{bmatrix} = \begin{bmatrix} -\frac{r}{L} & 0 & 0 \\ 0 & -\frac{r}{L} & 0 \\ 0 & 0 & -\frac{r}{L} \end{bmatrix} \times \begin{bmatrix} I_{ca} \\ I_{cb} \\ I_{cc} \end{bmatrix} + \frac{1}{L} \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix} \times \begin{bmatrix} V_{san} - V_{can} \\ V_{sbn} - V_{cbn} \\ V_{scn} - V_{ccn} \end{bmatrix}$$
(8)

The application of the Park transforms on the STATCOM model equations reveals a coupling term between the d-axis and q-axis component of the currents.

$$\frac{d}{dt} \begin{bmatrix} I_d \\ I_q \end{bmatrix} = \begin{bmatrix} -\frac{r}{L} & 0 \\ 0 & -\frac{r}{L} \end{bmatrix} \begin{bmatrix} I_d \\ I_q \end{bmatrix} + \begin{bmatrix} 0 & \omega \\ -\omega & 0 \end{bmatrix} \begin{bmatrix} I_d \\ I_q \end{bmatrix}$$
(9)
$$+ \frac{1}{L} \begin{bmatrix} V_{sd} & V_{cd} \\ V_{sq} & V_{cq} \end{bmatrix}$$

The two equations provide the expressions for the active power P and reactive power Q exchanged between the network and the STATCOM [24].

$$P = \frac{3}{2} \left(V_{sd} I_d + V_{sq} I_q \right) \tag{10}$$

$$Q = \frac{3}{2} \left(V_{sd} I_q - V_{sq} I_d \right) \tag{11}$$

Since $V_{sq} = 0$, is based on the instantaneous active and reactive power definition, the two equations can be obtained as follows:

$$P = \frac{3}{2} V_{sd} I_d \tag{12}$$

$$Q = \frac{3}{2} V_{sd} I_q \tag{13}$$

2.2 STATCOM control

The detailed control system of STATCOM is shown in Figure 5. In this system, the error signal between the rms measured and the rms AC voltage reference values is given to a PI regulator, which produces a reference current I_{qref} . Similarly, the error between the measured and the DC voltage reference values is given to a PI regulator that produces a reference current I_{dref} . The three-phase grid currents at the PCC are transformed into dq reference frame to create, I_d and I_q . These currents are then compared to the corresponding reference values to create error signals (ΔI_d and ΔI_q) which are transmitted to the PI controller in order to create the vectors of the reference voltages (V_{sdref} and V_{sqref}). Due to Park's inverse transformation, the voltages V_{sdref} and V_{sqref} are converted to V_{saref} , V_{sbref} and V_{scref} that are required by the SPWM generator [25-27].

To generate PWM control pulses for an N-level voltage converter, N-1 triangular carriers are required. These carriers have the same frequency fc and the same amplitude Ac as illustrated in Figure 6.



Figure 5. Block diagram of the STATCOM control strategy



Figure 6. Simplified diagram of the open loop PWM control of a multicellular converter

The pulse width modulation methods are classified according to the arrangement of the triangular carriers and the frequently utilized are [28, 29]:

- PS-PWM strategy (Phase Shifted Pulse Width Modulation):

In the case of a general n-level FCC, the carriers are phase shifted by $360^{\circ}/(n-1)$ in order to provide optimal stepped multilevel output voltage waveforms as indicated in Figure 7.



Figure 7. PS-PWM modulation strategy

- CD-PWM strategy (Carrier Disposition Pulse Width Modulation):

There are three types of CD-PWM method. A distinction can be made between the strategies of:

- PD-PWM (Phase Disposition Pulse Width Modulation).
- POD-PWM (Phase Opposition Disposition Pulse Width Modulation).
- APOD-PWM (Alternative Phase Opposition Disposition Pulse Width Modulation).

In order to decide on the most appropriate control strategy in cascaded inverters, a number of studies have been conducted on different PWM strategies, applied to distinct types of multilevel inverters.

A summary Table 2 is given for the different modulation strategies:

 Table 2. Simulation parameters [30]

Architecture	Modulation	
NPC	CD (PD, POD, APOD)	
FC	PS	
Н	PS	

As it has the advantage of decreasing the switch frequency as well as allowing uniform discharge at the power sources, PS-PWM has been viewed as the most beneficial method for cascaded inverters. The choice of the technique PS-PWM is used in our work to control multicellular converters because it is:

- Simple to implement than other techniques;
- THD is low for all values of the modulation index;
- Also has a better THD than other techniques.

3. SIMULATION RESULTS

We started by investigating the STATCOM's efficiency in terms of reactive energy compensation and voltage regulation in a power grid. To this end, simulations have been carried out using the serial multi-cellular converters (three levels, four levels, five levels and six levels) based on phase shifted pulse width modulation (PS-PWM) control.

Table 3. Simulation parameters [31]

Component	Parameters	Value
AC Source	Line-to-line rms voltage	381V
	Frequency	50Hz
	Source Resistance	7.3 m Ohm
	Source Inductance	0.23mH
STATCOM	DC-link Voltage	750V
	L filter	0.7mH
Load	Fixed load	50 kVA / +30kvar
	Dynamic load	10 kVA/ -50kvar

Two types of tests were applied in our model; in the first one, the fixed load is connected and in the second one, we connected the dynamic load. The entire system was simulated in MATLAB/Simulink with the parameters (Table 3).

In order to verify the validity of the proposed model, we carried out two experiments: one with STATCOM, and the other one without it.

3.1 Without STATCOM

The RMS voltage per phase, the three-phase voltages and currents at the common connection point (PCC) without

STATCOM are displayed in Figure 8 and Figure 9 respectively. The inductive load during the interval [0s 1s] causes the voltage drop. Moreover, an overvoltage that is caused by the capacitive load during the interval [1s 1.3s]. [1.3s 1.5s] return to the initial state. As it is illustrated in Figure 10, inductive and capacitive loads destabilize the evolution of reactive power in the network resulting from the phase shift between the voltage and the current of the network.



Figure 8. RMS voltage between phases without STATCOM



Figure 9. Three-phase voltage (*abc*) of the source without STATCOM



Figure 10. Source voltage and current for phase (*a*) at PCC without STATCOM

3.2 With STATCOM

These tests are repeated in the presence of STATCOM at the common connection point (PCC) with the same loads. Figure 11 and Figure 12 reveal that the positive reaction of STATCOM manages to maintain the constant voltage at the common point of connection PCC by controlling the amount of reactive power injected or absorbed at the common point of connection (PCC).

During the interval of 0s to 1s the STATCOM injects a quantity of reactive power to maintain voltage levels at the common connection point (PCC). In that case, the compensator operates in capacitive mode. On the other hand, STATCOM, in the interval 1s to 1.3s, absorbs a quantity of

reactive power that maintains the voltage levels in the line, and therefore; the compensator works in the inductive mode as shown in Figure 13.



Figure 11. RMS voltage between phases at PCC with STATCOM



Figure 12. Three-phase voltage (*abc*) of the source with STATCOM

We use two load types to examine STATCOM's behavior in the inductive and capacitive modes and to observe its behavior when the modes change (positive feedback).



Figure 13. Reactive power observed/supplied by STATCOM

Figure 14 displays that this reactive power exchange is realized through the AC filter leakage inductance which smoothing the currents in advance or in delay of the angle ($\pi/2$) with the converter output voltages imposed to grid.





Figure 14. Source voltage and current waveform of STATCOM output

The curve in Figure 15 clearly demonstrates the effectiveness of the block control of DC voltage in maintaining a constant voltage across the capacitor. This can be done by the good tunning of K_p and K_i (Figure 5).



Figure 15. Response curves of DC voltage





Figure 16. Source voltage and current with STATCOM

Figure 16 exhibits phase (a) voltage and current waveforms after compensation. This reflects a tight correspondence between voltage and current phases. The power factor jumps from 0.875 to 0.99.

Figure 17 shows the voltage curves with floating capacities and the DC-bus voltages of the three, four, five and six levels multicellular converters.

It can be noticed from the figures how the floating arm voltages follow their references with a small oscillation observed on the curves.





Figure 17. Voltage curves with floating capacities and DC voltage

Figure 18 and Figure 19 depict the voltages between phase (a) and neutral point O (Vcao), as well as the voltages between phase (Vcab) at the multicellular converters output with levels three, four, five and six.

This section suggests a comparative analysis with simulation results obtained in four studied STATCOM models.

The overall simulation results are summarized in Table 4, which presents the performance of multicellular STATCOMs on THD reduction.











Figure 19. Output voltage between phases of the STATCOM converter (Vcab)



Figure 20. THD for levels PS-PWM current at PCC

Line current Isabc Line voltage Vcab Phase Voltage Van THD% FD THD% FD THD% FD 309.6 3N 5.21 60.54 64.14 536.4 64.40 4N 4.13 61.10 41.35 535.8 41.21 309.6 5N 3.64 61.02 34.05 536.1 34.58 309.6 22.23 536 309.6

Table 4. Comparative analysis

6N 3.02 60.41 22.24 Vcab 70 60 50 40 30 20 10 0 1 2 5 7 3 4 6 8 Levels

(%)

THD (

Figure 21. THD for levels PS-PWM output voltage between phases

The previous table showed that the four types of compensators, controlled in the same way, provide almost

identical values of current load and phase-to-phase voltages at the converter output. Therefore, as the number of levels increases, the harmonics are lower.

Figure 20 and Figure 21 provides a global perspective on THD reduction performance of multilevel inverters.

A decrease of the THD from level three to six is noticed. It can be indicated that as the number of levels increases, the harmonics become weaker, which guarantee a low THD below the IEEE-519 standard (5%). Here multilevel converters implementation gains momentum.

4. CONCLUSIONS

This study demonstrated the modeling and performance analysis of multicellular converters (three to six levels) used in STATCOM applications to voltage regulation, reactive

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energy compensation, and power factor improvement in a power system. A proposed STATCOM mathematical model was analyzed in detail for designing an appropriate control.

Furthermore, PWM control based on phase shifted allowed floating capacitor voltages to be equilibrated, and consequently, enhancing the output voltage signal and minimizing the current THD.

STATCOM model and control were implemented on MATLAB/SIMULINK environment to investigate the steady state and dynamic performance. A conclusion which can be underlined on the basis of the obtained results is that using the proposed control, STATCOM is efficient in supplying or absorbing the reactive energy required for maintaining a stable voltage at the common connection point (PCC) regardless of disturbance type.

The findings also suggest that this approach could be useful to provide a basis for a STATCOM experimental setup validation based on the serial multicellular converter.

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NOMENCLATURE

STATCOM	Static Synchronous Compensator
PS-PWM	Phase Shifted Pulse Width Modulation
SPWM	Sinusoidal pulse width modulation
PCC	Point Common Connection
RMS	Root Mean Square
THD	Total harmonic distortion
FD	Fundamental