

Optimum Parametric Identification of a Stand-Alone Photovoltaic System with Battery Storage and Optimization Controller Using Averaging Approach



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ABSTRACT

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The dimensioning of photovoltaic systems is the major concern of researchers and power industry practitioners. This aims to improve energy efficiency and protect the conversion units by a consistent assessment of power conditioning circuits and interconnections for the PV application. In this context, this paper sets out to fulfill detailed modeling and control steps of a standalone photovoltaic (PV) power system with energy storage, according to practical specifications of the load, PV generation unit, and battery pack. The main goal is to estimate all unknown parameters, as the diode ideality factor and revers saturation current, the controller, and the PV link. The PV link interfacing the PV source circuit to the PV-side converter (PVSC) provides a filtering function to maintain a steady voltage at the link. The charge controller used in the PV-side converter is a DC/DC buck converter. It transfers the PV power to the battery and supplies the load. Using pulse- width modulation (PWM) technical, of which the switching duty cycle is the control-input variable; the PVSC power-conditioning circuit is permanently controlled by the maximum power point tracking (MPPT) algorithm to achieve the maximum energy. The battery pack voltage is properly maintained by the charge controller and specified to match the load voltage rating, to avoid a high ratio of voltage conversion. A method is proposed to integrate both the MPPT function and the battery cycle charge. The PV generator output and the power conditioning circuits, mainly constructed from switching- mode power converters, are nonlinear. An averaged model is then derived for dynamic analysis and controller synthesis, using the state-space averaging and linearization method. A PV array of nine PV modules configured into three strings is used in this application to demonstrate the effectiveness of modeling, design, control, and simulation. Simulation model for the controller and power interface is built and developed in short term, using the fundamental blocks of Matlab Simulink.

1. INTRODUCTION

Photovoltaic (PV) power engineering has attracted significant attention in recent years. This energy is free, clean, and permanently dispersed throughout the world, compared with exhaustible and polarizing fossil fuels [1].

In this context, our study focuses on the design, integration, and simulation of a standalone PV system with battery charging. A successful standalone photovoltaic installation requires first an energy saving approach, then a rigorous design with components that meet the needs and promises. This requires mastering the basic techniques, and concrete steps to design such a system, putting in place the main components namely, the battery, the regulator, and the converter. Taking into account the technical requirements, the system has been designed according to specific steps.

System sizing is based on a clear specification of the load profile which includes the reference nominal load voltage for rating the battery pack and solar generation unit voltages. I–V and P–V curves are commonly used to illustrate the outputs of PV cells, modules, strings, or arrays. Computational models can be developed to represent the PV output characteristics under variations in cell temperature and solar irradiance. These

models are classified according to their equivalent circuits into two main types: single-diode model (SDM) and double-diode model (DDM).

Various modeling approaches have been presented in the literature. For example, a single diode model with both parallel and series resistors was detailed [2]. The same approach was applied in several studies such as [3, 4]. In which, these two internal characteristics are considered very important and require its judicious determination. The models designed by Zerhouni et al. [5, 6] hold only the resistance in series. The parallel resistance is considered very large and can be ignored. To simplify the model, even more, the ideal single diode model (ISDM) is chosen in this paper. Both resistances are removed by assuming that the one in series one is very small, and the one in parallel is very large. Some papers focus on the dimensioning of power converters. A full study [7] is done only, to calculate the component values of the buck converter in the PV system by the method of minimizing losses of switching devices. In our approach, we refer to the manufacturers' data at standard test conditions (STC) to specify the PV link, using the peak-to-peak values and the duty cycle operating at nominal load conditions.

Voltage regulation for the PV and DC links is introduced

and analyzed using dynamic models that are developed based on the state-space averaging, and linearization technique. A simple practical PID regulator is therefore calculated and integrated, unlike those developed by complicated techniques, such as those developed by Precup et al. [8].

Maximum power point tracker techniques differ in several aspects as feasibility, analogical or digital realization, sensor requirements, effectiveness, rate of convergence, hardware embodiment, cost, and other aspects. The most well used are perturb and observe algorithm and incremental conductance algorithm [9, 10]. Other papers introduce artificial intelligence techniques as used in the ref. [11, 12]. The MPPT algorithm introduced in this paper is based on the hill and climbing technique to improve tracking performance. To enhance the practicality of this paper, a specific battery module is used, where real discharge characteristics are given by the product datasheet.

It was given that the majority of papers focus their contributions on control, dimensioning, battery charging, or maximization. We have opted in this article to integrate both aspects at the same time. Simulation model for the controller and power interface is developed in short term to capture the transient details of fast dynamics and demonstrate the effectiveness of modeling, design, and control of the standalone PV power system.

2. MODELING OF STAND-ALONE PV SYSTEM

The equivalent circuit studied for analysis and simulation is illustrated in Figure 1. The buck converter is the charge controller that transfers the PV power to the battery and supplies the load. Battery equivalent circuit is formed by a voltage source V_{OC} in series connection with a resistor R_{bal} , and equivalent capacitance across the battery pack C_{BAT} . All DC loads are simplified to draw current from the battery link, which is denoted as i_{load} .



Figure 1. Equivalent circuit of the PV system

2.1 PV cell output characteristics and mathematical models



Figure 2. Scheme of PV cell with ideal single-diode model

Considering the objectives envisaged from this study, the choice of the solar panel equivalent circuit does not affect the overall behavior of the systems. Indeed, and for reasons of simplification, we chose the simplest ideal single-diode model (ISDM). Based on the p-n junction structure for both PV cell and diode, the model has only a current source in parallel with a diode, as shown in Figure 2.

For standard test conditions (STC) of cell temperature and solar irradiance, the current–voltage characteristics are expressed by Eq. (1):

$$i_{pv} = i_{ph} - \underbrace{i_s \left[e \left(\frac{qv_{pv}}{kT_c A_n} \right) - 1 \right]}_{i_d}$$
(1)

The values of the p-n junction constants, model parameters, and variables are listed in Table 1.

Table 1. PV parameters

Components	Rating values
Irradiance at STC	1000 W/m ²
Boltzmann constant	$K=1.38 \times 10^{-23} \text{ J/K}$
Electron Charge	$1.6 \times 10^{-19} \text{ C}$
PV cell temperature at STC	298 K
Thermal voltage of p-n junction at STC	25.7 mV
PV Cells number per module	60
PV power at MPP and STC	280 W
PV voltage at MPP and STC	31.67V
PV current at MPP and STC	8.84A
PV open-circuit voltage at the STC	38.97V
PV short-circuit current at the STC	9.41A
Temperature coefficient on PV current	0.04%/°C
Temperature coefficient on PV voltage	-0.29%/°C
Irradiance coefficient on PV power	-0.40%/°C

2.1.1 Parameters identification at STC

When the terminal of the equivalent circuit shown in Figure 2 is shorted, the diode current i_d is equal to zero. The value of the photon current i_{ph} is equal to the short-circuit current I_{SCS} which is available from the product datasheet. When the terminal is opened, the PV cell output current i_{pv} is equal to zero. The value of the diode current, i_d , becomes equal to the photon current i_{ph} which is the I_{SCS} at STC. This can be expressed as in Eq. (2), which includes two unknown parameters of I_{SS} and A_n:

$$i_{SCS} = i_{SS} \left[e^{\left(\frac{v_{OCS}}{v_{TCS}A_n}\right)} - 1 \right]$$
(2)

where, I_{ss} is the diode reverse-bias saturation current, and V_{TCS} is the thermal voltage at STC, which is constant and expressed as:

$$v_{TCS} = \frac{kT_{CS}}{q} \tag{3}$$

Using Eq. (1), the I–V characteristics of the maximal power point (MPP) is, therefore, expressed in STC by Eq. (4)

$$i_{MS} = i_{SCS} - i_{ss} \left[e^{(\frac{v_{MS}}{v_{TCS}A_n})} - 1 \right]$$
 (4)

The unknown parameters i_{SS} and A_n can be determined by solving the two nonlinear equations, Eq. (2) and Eq. (4). They can also be combined to form one Eq. (5):

$$\frac{e(\frac{v_{MS}}{v_{TCS}} \frac{1}{A_n}) - 1}{e(\frac{v_{OCS}}{v_{TCS}} \frac{1}{A_n}) - 1} = 1 - \frac{i_{MS}}{i_{SCS}}$$
(5)

We define a new variable A_{inv} as the reciprocal of A_n :

$$A_{inv} = \frac{1}{A_n} \tag{6}$$

The Eq. (5) can be reorganized as Eq. (7) which is a nonlinear equation [13]:

$$f(A_{inv}) = e^{C_1 A_{inv}} - C_3 e^{C_2 A_{inv}} - 1 + C_3 = 0$$
(7)

where, the constants are calculated as:

$$\begin{cases}
C_1 = \frac{v_{MS}}{v_{TCS}} \\
C_2 = \frac{v_{OCS}}{v_{TCS}} \\
C_3 = 1 - \frac{i_{MS}}{i_{SCS}}
\end{cases}$$
(8)

We use the Newton–Raphson method to solve Eq. (7). The derivation of $f(A_{inv})$ is expressed in Eq. (9):

$$f'(A_{inv}) = C_1 A_{inv} e^{C_1 A_{inv}} - C_3 C_2 A_{inv} e^{C_2 A_{inv}}$$
(9)

Since the values of A_n are generally between 1 and 2, A_{inv} can be assigned an initial value of 0.7. The chosen tolerance is $(Err=10^{-6})$. Newton–Raphson numerical iteration is given by Eq. (10).

$$A_{inv}(n+1) = A_{inv}(n) - \frac{f[A_{inv}(n)]}{f'[A_{inv}(n)]}$$
(10)

The software flowchart for parameters identification is illustrated in Figure 3. The value of A_{inv} is continuously updated until the output of $f(A_{inv})$ is close to zero, and the resulting value of A_n represents, therefore, the ISDM ideality factor.

2.1.2 Variation with irradiance and temperature

In reality, The I–V characteristics of the simulation model should respond to variations in both cell temperature and solar irradiance. The PV cell manufacturer usually provides correction coefficients derived from experimental evaluations.

The temperature coefficients for PV output current, voltage, and power are denoted α_T , β_T , and λ_T , respectively.



Figure 3. Flowchart of the Newton-Raphson method

Based on the assumption that the ideality factor A_n is constant regardless of environmental variations, the expression of i_{ph} should be [13]:

$$i_{ph}(G,\Delta T) = \frac{G}{G_{STC}} i_{SCS} (1 + \alpha_T \Delta T)$$
(11)

where, G is the present solar radiation and G_{STC} is the solar radiation at the reference test. ΔT is the difference between the cell temperature T_C and the temperature at STC, T_{CS} , which is 25°C or 298 K. The open-circuit voltage correction for both solar irradiance and cell temperature variations is given by Eq. (12):

$$v_{OC}(G,\Delta T) = v_{OCS}(1 + \beta_T \Delta T)v_T$$
(12)

The value of the diode saturation current $i_s(G, \Delta T)$ can be determined from open-circuit conditions, since the photon current is equal to the diode current:

$$i_{S}(G,\Delta T) = \frac{i_{ph}(G,\Delta T)}{e^{\left[\frac{qv_{oc}(G,\Delta T)}{kT_{c}A_{n}}\right]} - 1}$$
(13)

The (I–V) characteristic equation of a PV cell with cell temperature and solar irradiance variations can be written as:

$$i_{pv} = i_{ph}(G, \Delta T) - i_s(G, \Delta T) \left[e^{(\frac{qv_{pv}}{kT_c A_n})} - 1 \right]$$

$$\underbrace{i_d(G, \Delta T)}_{i_d(G, \Delta T)}$$
(14)

The PV module voltage V_{pvM} and current I_{pvM} are given in term of the cell's voltage V_{pv} and current I_{pv} by the following relations:

$$I_{pvM} = N_S I_{pv} \tag{15}$$

$$V_{pvM} = N_p v_{pv} \tag{16}$$

where, N_S is the number of series cells and N_P is the number of parallel cells. The PV cell parameters can be estimated using the specification of the PV module shown in Table 1. The PV array has a 3 × 3 configuration of nine PV modules. Therefore, the PV array output characteristics are represented by the (I– V) and (P–V) curves shown in Figures 4 and 5 to illustrate the impact of changes in cell temperatures and solar irradiance, respectively. With a balanced operation of nine PV modules, the MPP of the array is indicated as 2525 W and is at (96.66 V, 26.12 A).



Figure 4. PV characteristics by varying temperature



Figure 5. PV characteristics by varying irradiance

2.2 Buck converter dimensioning

In the PV system of Figure 1, the DC-DC buck converter is controlled using pulse width modulation (PWM) technique, in which the switching duty cycle D is the control-input variable. A step-down topology should be considered if the converteroutput voltage is never higher than the PV terminal voltage V_{MPP} , when the normal voltage variation of both sides has been considered. The condition can be expressed as $V_O(\max) \le$ $V_{MPP}(\min)$. The lowest value of the PV terminal voltage at the MPP, $V_{MPP}(\min)$, can be estimated from the highest ambient temperature and the minimum irradiance for the converter to operate. The highest value of the output voltage, $V_O(\max)$, can be determined from the load profile. For battery-charging applications, the battery voltage becomes the converter-output voltage V_o , which varies from the cut-off voltage at 0% state of charge (SOC) to the highest level: the open-circuit voltage at 100% SOC. In this case, the value of $V_o(\text{max})$ is equivalent to the open-circuit voltage of the battery at 100% SOC [13].

2.2.1 Calculation of L and C_{in}

At STC and the predefined switching frequency f_{SW} , the inductor ripple current and ripple voltage at the PV link should be specified by the peak-to-peak values, ΔI_L and ΔV_{PV} , respectively. Steady-state analysis may determine the duty cycle at the nominal load operating condition. At STC, the PV source circuit should be operated at the MPP, which is represented by V_{MPP} and I_{MPP} . The duty cycle can be calculated using Eq. (17):

$$D_0 = \frac{V_{0-NOM}}{V_{MPP}} \tag{17}$$

The nominal output voltage V_{o-NOM} corresponding to the buck converter in continuous conduction mode (CCM) can be specified from the load profile. The value of the inductance, L, and the capacitance, C_{in} , can be calculated from Eq. (18) and Eq. (19), respectively.

$$L = \frac{V_{0-NOM} (1 - D_0)}{\Delta I_L f_{sw}}$$
(18)

$$C_{in} = \frac{I_{MPP}(1 - D_0)}{\Delta V_{pv} f_{sw}}$$
(19)

According to this sizing approach, buck converter parameters are listed in Table 2.

 Table 2. Buck converter parameters

Components	Rating values
Switching frequency	f_{sw} =50 kHz
Inductance	<i>L</i> =470 μH
Input capacitor	Cin=470 μF
Nominal input voltage	95V
Nominal output voltage	48V

Applying the basic Kirchhoff's laws in Figure 1, the system dynamics can be revealed based on CCM operation. When the PV-link voltage is the control variable, the output voltage Vo is assumed to be constant for dynamic modeling. Based on the switching device conduction state Q, we distinguish two cases:

Q on-state Dynamics:

$$L\frac{di_L}{dt} = v_{pv} - v_0 \tag{20}$$

$$C_{in}\frac{dv_{pv}}{dt} = i_{pv} - i_L \tag{21}$$

Q off-state Dynamics:

$$L\frac{di_L}{dt} = -v_0 \tag{22}$$

$$C_{in}\frac{dv_{pv}}{dt} = i_{pv}$$
(23)

2.2.2 Dynamic modeling

The PV output generator and the power conditioning circuits are nonlinear. From the dynamic expression of the switching on/off operation, an averaged model can be derived using the state-space averaging method and linearization technique to derive linear models for dynamic analysis and controller synthesis. The averaging approach requires that switching device frequency is much higher than the system's critical dynamics, which are formed by energy-storage components, such as the capacitor and the inductor. Under this condition, the nonlinear switching dynamics can be neglected for dynamic analysis and controller synthesis. During one switching cycle, the system state-space model can be derived from the on-state and off-state of the power switch. The averaged value of continuous signals can be used to form a dynamic model without the need to represent the switching ripples.

The system dynamics can be expressed in a general form showing the dynamics of the inductor current and the PV-link voltage:

$$\frac{di_L}{dt} = f(i_L, v_{pv}, d)$$
(24)

$$\frac{dv_{pv}}{dt} = g(i_L, v_{pv}, d)$$
(25)

The linear model can be derived by a linearization process:

$$\frac{di_L}{dt} = \frac{\partial f}{\partial v_{pv}} \bigg|_{SS} v_{pv} + \frac{\partial f}{\partial i_L} \bigg|_{SS} i_L + \frac{\partial f}{\partial d} \bigg|_{SS} d$$
(26)

$$\frac{dv_{pv}}{dt} = \frac{\partial g}{\partial v_{pv}} \bigg|_{SS} v_{pv} + \frac{\partial g}{\partial i_L} \bigg|_{SS} i_L + \frac{\partial g}{\partial d} \bigg|_{SS} d$$
(27)

where, $\tilde{\iota}_L$, $\tilde{\nu}_{pv}$, and \tilde{d} represent the small signals of the PV module voltage V_{pv}, the inductor current i_L , and the switching duty cycle d, and SS denotes the steady state.

Averaging the state dynamics of Eqns. (20), (21), (22), and (23), we get:

$$\frac{di_L}{dt} = -\frac{1}{\underline{L}} \left[\frac{dv_{pv} - v_o}{\int (v_{pv}, d, i_L)} \right]$$
(28)

$$\frac{dv_{pv}}{dt} = -\frac{1}{\underbrace{C_{in}}_{g(v_{pv},d,i_L)}} \begin{bmatrix} di_{pv} - di_L \end{bmatrix}$$
(29)

where, d is the switching duty cycle and the control variable. Due to the nonlinear characteristics in (28) and (29), linearization is required to derive the small-signal model at the nominal operating condition. Based on the predefined steady state, the small-signal model can be derived using (26) and (27) and expressed in the state-space form as:

$$\begin{bmatrix} \frac{di_L}{dt} \\ \frac{dv_{pv}}{dt} \end{bmatrix} = \begin{bmatrix} 0 & \frac{D}{L} \\ -\frac{D}{C_{in}} & \frac{1}{R_{pv}C_{in}} \end{bmatrix} \begin{bmatrix} i_L \\ v_{pv} \end{bmatrix} + \begin{bmatrix} \frac{v_{pv}}{L} \\ -\frac{i_L}{C_{in}} \end{bmatrix} d$$
(30)

where, D, V_{pv} , and i_L are considered to be constant in the steady state. The signals $\tilde{\iota}_L$, \tilde{v}_{pv} are the state variables and \tilde{d} represent the control variable, R_{pv} is the PV resistance defined as the ratio between PV terminal voltage and current.

2.3 Battery modeling

The battery can be considered a variable voltage resource, with the steady-state value affected by the state of charge (SOC) and the rate of charge or discharge. A simple battery model can be represented by a Thévenin equivalent circuit, which is formed by a voltage source in series connection with a resistor, as shown in Figure 1. The model can be configured to simulate the steady-state value of the voltage responding to the discharge current variations, as expressed in Eq. (31).

$$v_{bat} = V_{OC} - R_{bat} . i_{bat}$$
(31)

BK-10V10T is a specific battery module used in this paper. These parameters are defined in Table 3. Figure 6 illustrates discharge characteristics given by the product datasheet [14].



Figure 6. Battery voltages versus discharged capacity for model output and product data

Table 3. DC load and battery parameters

Components	Rating values
Battery type	NiMH
Battery model	BK-10V10T
Battery pack voltage rating	55 V
Band of voltage limits	54.45-55.45 V
Battery pack nominal capacity	90 Ah
Nominal voltage of DC load	48 V
Acceptable voltage range of DC load	42-56 V

For a general representation, the discharge capacity, C_{dis} can be converted into the SOC, which is the normal way to represent the battery capacity. For the BK-10V10T module, the polynomial function is derived in Eq. (32), where polynomial constants are listed in Table 4.

 Table 4. Polynomial parameters for modeling battery module

 BK-10V10T

Rating values
42.2942
-98.3961
88.7769
-40.3893
10.2942
11.4802

$$V_{OC} = P_{S5}SOC^{5} + P_{S4}SOC^{4} + P_{S3}SOC^{3} + P_{S2}SOC^{2} + P_{S1}SOC + P_{S0}$$
(32)

In the equivalent circuit of Figure 1, the inductor current of the buck converter is denoted i_L . The circuit dynamics are given by Eqns. (33) and (34).

$$i_L + i_{bat} = C_{BAT} \frac{dv_{bat}}{dt} + i_{load}$$
(33)

$$R_{bat}C_{BAT}\frac{di_{bat}}{dt} + i_{bat} = i_{load} - i_L \tag{34}$$

With the assumption of a constant voltage V_{OC} in the shortterm steady state, the battery-link dynamics can be represented by the transfer function:

$$i_{bat}(s) = \frac{i_{load} - i_L}{R_{bat}C_{BAT}s + 1}$$
(35)

3. VOLTAGE REGULATION IN PV LINK

We derive from Eq. (30) the following transfer function:

$$\frac{v_{pv}(s)}{d(s)} = \frac{-\frac{i_L}{C_{in}}s - \frac{Dv_{pv}}{LC_{in}}}{s^2 - (\frac{1}{R_{pv}C_{in}})s + \frac{D^2}{LC_{in}}}$$
(36)

The transfer function in Eq. (36) represents a second-order system, which can be standardized by Eq. (37):

$$G_0(s) = \frac{k_0(\beta s + 1)}{s^2 + 2\xi\omega_n s + \omega_n^2}$$
(37)

where, the undamped natural frequency and damping factor are expressed as ω_n and ξ respectively. We can then derive the coefficients relating to Eq. (36).

$$\omega_n = \frac{D}{\sqrt{LC_{in}}} \tag{38}$$

$$\xi = -\frac{\sqrt{L}}{2DR_{pv}\sqrt{C_{in}}} \tag{39}$$

$$k_0 = -\frac{Dv_{pv}}{LC_{in}} \tag{40}$$

$$\beta = \frac{Li_L}{Dv_{pv}} \tag{41}$$

The relative degree of the Eq. (36) is one because one minimal phase zero is present. We propose to use a standard PID controller to regulate the PV link voltage. The first-order of the desired closed-loop function transfer F(s) is adopted, with the general form:

$$F(s) = \frac{1}{\alpha s + 1} \tag{42}$$

 α is a constant to be defined. The feedback controller can be derived then as:

$$C(s) = \frac{s^2 + 2\xi\omega_n s + \omega_n^2}{k_0 s(\alpha\beta s + \alpha + \beta)}$$
(43)

The PID controller shown in Eq. (43) can be written in a parallel form as:

$$C(s) = k_p + \frac{k_i}{s} + \frac{k_d}{\tau \cdot s + 1}$$
(44)

PID parameters can be derived from Eq. (43) and Eq. (44) as:

$$\tau = \frac{\alpha.\beta}{\alpha+\beta} \tag{45}$$

$$k_i = \frac{\omega_n^2}{k_0(\alpha + \beta)} \tag{46}$$

$$k_p = \frac{2\zeta\omega_n(\alpha+\beta) - \omega_n^2\alpha\beta}{k_0(\alpha+\beta)^2}$$
(47)

$$k_d = \frac{(\alpha + \beta)^2 - 2\zeta\omega_n(\alpha + \beta)\alpha\beta + \omega_n^2\alpha^2\beta^2}{k_0(\alpha + \beta)^3}$$
(48)

The PID controller for the voltage regulation of the PV link is synthesized and expressed as:

$$C(s) = -0.0017 - \frac{8.4736}{s} - \frac{6.8485 \times 10^{-6}}{2.8745 \times 10^{-4} s + 1}$$
(49)

3.1 Relative stability

To evaluate the relative stability, the phase margin and gain margin are measured by Bode diagram plot of $C(s)G_O(s)$ in the frequency domain as shown in Figure 7. The value of the gain margin is 8.202(dB) at the frequency of 3500rad/s, and the phase margin is calculated as 95.67° at the frequency of 500(rad/s). The closed-loop stability is then proved since both margins are positive.

3.2 Robustness

In a closed-loop system, the sensitivity function is defined and expressed as:

$$S(s) = \frac{1}{1 + C(s)G_0(s)}$$
(50)



Figure 7. Demonstration of relative stability by Bode diagram



Figure 8. Demonstration of sensitivity by Bode diagram

The sensitivity function is illustrated by a Bode diagram, as shown in Figure 8. The function shows high-pass features: the magnitude is high in the high-frequency band, but is low in the low-frequency band. The magnitude of the sensitivity peak should be lower than 2 to achieve robust system control. The sensitivity peak is specified as the maximum magnitude in S(s):

$$S(\max) = \max \left| S(j\omega) \right| \tag{51}$$

The peak gain is measured as 1.5515 and calculated as 3.8153 dB which guarantees robust operation.

4. MAXIMUM POWER POINT TRACKING

In a steady state of cell temperature and solar irradiance, there is a single operating point where the output of the voltage and current results in the maximum power output (MPP). Various techniques have been proposed for the MPPT algorithm. Hill Climbing (HC) technique is one of the most well-known algorithms thanks to its simplicity; even there are oscillations around the MPP in a steady state and a loss of research of the MPP, when climatic conditions change rapidly [15]. This method consists to climb the operating point along with the generator characteristic to a maximum. The output PV power P_{New} is periodically compared to the previous value P_{Old} . Depending on the outcome of the comparison, we increase or decrease the voltage reference, to calculate the corresponding duty cycle d via the PID regulator. Once the PPM point is reached, the system oscillates around it indefinitely. The flowchart of this algorithm is illustrated in Figure 9.



Figure 9. Hill climbing algorithm for maximum power point tracking

4.1 Integrating battery-charge control with MPPT

To stop the PV generation from overloading the charging capacity, the charging cycle should be maintained with an MPPT function. When the voltage and current of the battery reach their charging-cycle limits, MPPT should be stopped to reduce PV power generation. Instead, the control action should shift the operating point of the PV generator in the open-circuit voltage direction or into the voltage-source zone. The rated value and allowable range of voltage variations of the considered battery are given in Table 3.

5. SIMULATION RESULTS

The overarching model of simulation for the designed system is displayed in Figure 10. It includes the PV array, DC/DC buck converter, battery link, and battery pack, with control and MPPT blocs. To validate the model, we subject it to certain conditions of load change and climatic conditions. Simulation results are illustrated in Figure 11.



Figure 10. Simulation model of the PV system



Figure 11. Simulation results

The initial SOC of the battery pack was set to be 80%. A load current of 2.8 was constantly extracted from the battery

link. Before 0.2 s, the irradiance was 400 W/m² and the cell temperature was 25°C. Since the voltage is lower than the limit of 54.45 V, the MPP is tracked at 0.09 s during the start-up period and maintained by the steady-state tracking operation. The voltage ripple caused by the active perturbation of the HC algorithm is noticeable. The SOC gradually increases in response to the charging current. The solar irradiance steps up at the instant 0.2 s from 400 to 1000 W/m² and steps down again to reach the value 800W/m² at the instant 0.4 s. The PV output power significantly increases, causing an increase in the charging current. The increase of the SOC becomes faster than before. Since the SOC is still relatively low, the battery voltage is still below the upper limit. The MPPT operates continuously to inject the highest power into the battery link.

6. CONCLUSIONS

In this paper, a standalone photovoltaic system with energy storage was designed and dimensioned according to the load specifications, the photovoltaic production unit, and the battery pack. Since the PV generator output and the power conditioning circuits are nonlinear, an averaged model was derived for dynamic analysis and controller synthesis, using the state-space averaging and linearization method. Using a specific method, both MPPT function and battery cycle charge were integrated, to achieve the maximum energy and specify the battery voltage to match the load voltage rating. The battery simulation was built using a practical model design. Due to computational constraints, the Simulink model for the controller and power interface is developed only in short term to capture the transient details of fast dynamics, including fast switching in the DC/DC converter. The accuracy of the parameters identified and used in the mathematical models is cheeked by comparing manufacturer's data in tables with simulation results at STC conditions, that show PV power, voltage, and current at MPP, battery voltage regulation, and variation of SOC in response to changes in solar irradiance and cell temperature

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NOMENCLATURE

- G Solar irradiance, W/m²
- I_M Instant MPP current, A
- i_{ph} PV photon current, A
- i_{pv} PV cell output current, A
- I_d Diode current, A
- I_{ss} Diode reverse-bias saturation current, A
- T_C PV cell temperature, K
- V_D Diode voltage, V
- V_M Instant MPP voltage, V
- Voc PV open-circuit voltage, V
- V_{PV} PV cell terminal voltage, V
- V_t Thermal voltage of p–n junction, V
- α_T dimensionless temperature coefficient on PV current
- β_T dimensionless temperature coefficient on PV voltage
- λ_T dimensionless irradiance coefficient on PV power
- A_n dimensionless, diode ideality factor in SDM
- I_{MS} PV current at MPP and STS, A
- I_{ph} PV photon current at STC, A
- *I*_{SCS} PV short-circuit current at the STC, A
- *Is* Diode reverse-bias saturation current, A
- P_{MPP} PV power at MPP and STC, W
- R_s Series resistance, Ω
- R_h Shunt resistance, Ω
- V_{MS} PV voltage at MPP and STC, V
- V_{OCS} PV open-circuit voltage at STC, V