State Space Modeling and Stability Analysis of a VSC-HVDC System for Exchange of Energy

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ABSTRACT

Nowadays, relevant actors are searching for solutions to produce energy with a low impact on the environment. Indeed, transmitting power via large distances with maintaining low losses is one of the main challenges. To improve electricity communication between countries and offshore wind, a new interconnections line must be built. Therefore, Voltage Source Converter High Voltage Direct Current (VSC-HVDC) transmission is incoming as the exceptive technology in order to address the challenges related to the integration of future offshore wind power plants. In spite of its many advantages, VSC-based HVDC transmission systems can experience unexpected instability and interaction phenomena: Small disturbances that occur continually in VSC-HVDC transmission systems due to the complex VSC based interconnections and an important number of components with non-linear nature that may cause failure. Thus, before installing the HVDC system, there is a significant need for studying a hybrid AC-DC system to guarantee the reliable and stable operation. This paper deals with the stability of a VSC-HVDC system by the use of a small signal stability method; such procedure enables to study the stability of a linearized VSC-HVDC system through state-space modeling and eigenvalue-based stability analysis.

1. INTRODUCTION

The environment is facing today a worldwide energy evolution challenge since developed and budding countries require more energy for their economy growth in a structure of limited and poorly distributed energy resources. In the same time, the climate change because of the greenhouse gas emission which induces the change of the energy aspect with more climate-friendly energy resources such as hydro, solar or wind renewable resources [1].

The integration of the offshore wind power will aid many countries to attain their objectives in terms of renewable energy. Since future offshore wind farms are predicted to be built farther away from shore and have larger capacities than today. It shows the way to new challenges concerning grid connection, for large distances longer than 100 km [2]. In the second half of the 20th century, it becomes possible to start projects of DC links for the transmission of power beyond the seas thinks to the development of electronic power components, for example: (Italy-Corsica-Sardinia, France-England or Those in the Baltic Sea) [3] or over very long distances.

For long-distance, HVDC technology allows larger transmission capacity because of the nonexistence of reactive power generated by submarine cables which restricts the transmission capacity in HVAC [4]. Moreover, HVDC network is a reliable technology for connecting asynchronous networks. It enables also the transfer of the power between grid systems which have not the same values of frequencies; this act allows having a wind turbines higher production at the Maximum Power Point Tracking (MPPT). This enhances the economy and stability of each grid by permitting the interchanging of power through diverse networks [4, 5].

In literature, there are two configurations of HVDC: Conventional Line-commutated converter (LCC) which is not appropriated to be connected to weak grids for example offshore wind farms. Voltage Source Converter technology (VSC) [6] which is the most preferred technology to connect islanded grids. The appearance of Voltage Source Converter (VSC) has prolonged the flexibility and operability of HVDC technology [3].

The connection of wind farm could be joined with DC interconnections between AC systems to enhance power transit flexibility between AC systems.

The control principles and the protection scheme ought to be looked at before the achievement of such DC grids. With a view to guarantee that the HVDC grid is always stable even after major events, the stability problems have to be resolved. For example, the variation of the rotor angle of the wind synchronous machines which corresponds to the capacities of a coupled synchronous machine on an alternating three-phase network to maintain the equilibrium between the electromagnetic couple attached to the network and the mechanical couple related to the load [7]. Furthermore, the variations of the electrical frequency of alternative grids which correspond to the balance between the power spend on the network and the power generated from it. An important factor also is the stability of the bus voltage when it is submitted to a major variation: This means that amplitudes of the bus voltage which varies after each power step on the network will be stabilized around a point of equilibrium [8].

Whenever a system is equilibrated at an operator point after a physical disturbance, we can say that this system is stable [9]. Normally, the study of system stability differs according to its type: linear or non-linear.
With the aim of studying the linear system stability, the eigenvalues evaluation method is the more used one [8]. For the non-linear system, the complexity of analysing the VSC-HVDC system stability shows the way to make a linearized model around an operating point for this system.

Small-signal stability analysis (SSSA) with power electronics has been studied while researchers specified the stability problems due to the control of the LCC.

The integration of non-conventional power sources to the grid is enabled by the converters. Owing to the complexity of the power electronics converter, various instability and interaction phenomena have been noted in these types of systems. Indeed, small disturbances occur continually in the case of wind turbines and HVDC systems due to the continuous variation of the loads and generations.

Modern works have investigated SSSA of VSC-HVDC system and have analysed the control strategy of a VSC-HVDC chain connected to weak AC grids [10]. They have investigated control strategies of VSC-HVDC transmission in order to upgrade damping of electromechanical modes of AC systems [9]. In recent years, researchers are focused on studying the stability of power system equipped with power electronics using the eigenvalue method [11-13] or the impedance based method [14, 15]. Indeed, the impedance based method is utilized in the aim of regulating the converters of the VSC-HVDC system that assures the stability of the system and minimizing the interaction phenomena. But, the drawback of this method is the limited observability of some states considering its dependence on the definition of local source-load subsystems which insures the necessity of investigating the stability at different subsystems.

State-space modeling and the eigenvalue study is a well-established method which is used to study the stability of HVDC systems [11, 12].

In the literature, amount of papers treat the primary control strategy of a VSC-HVDC system [16, 17]. However, rare of them which look to the impact of these controls on the stability of the HVDC system [18, 19]. Though, a state space system linearized model of the VSC-HVDC system is analysed in view of evaluating the stability.

In this context, the work presented in this paper attempts to check the effectiveness of the system control used and to analyse the dynamic stability of a point to point VSC-HVDC transmission system by the use of its state space model and the analyse of the root locus. The eigenvalues of the system are retrieved and the root places are analysed after getting linear state equations. This is a simple procedure used for the sake of studying VSC-HVDC system stability.

The three main sections of the rest of this paper are outlined as follows: The architecture of the studied VSC-HVDC system, the control strategy applied, the linearization of the total system and a comparison between the linearized and non-linearized HVDC system are described in Section 2. The state space model of the total chain HVDC is then studied. Finally, in Section 3, the root locus analysis of the total system is detailed and the main conclusions are drawn in Section 4.

2. A BASIC CONTROLLED POINT TO POINT VSC-HVDC SYSTEM FROM AVERAGE TO LINEARIZED MODEL

2.1 Study of two terminals VSC-HVDC controlled system

The converter is outlined as a fundamental part of the VSC-HVDC network. The fundamental ingredient of the converter is a permanent switch which is adapted to turn on or off a current [20]. The asymmetric structure of IGBT (Insulated Gate Bipolar Transistor) used in this VSC-HVDC system, enables lower on-state voltage drop and higher brazen voltage blocking capability at the expense of reverse voltage blocking capability efficiency reduced [21].

The VSC-HVDC scheme is shown in Figure 1. A DC cable is made as connection between the two VSC stations. On the DC side, capacitors permit to conserve the DC voltage and the phase reactors filter injected grid currents. On the AC side, the converter is connected to an AC network or a wind turbine with a transformer in order to insulate the station from the AC source and provides the appropriate AC voltage. The phase reactors associated with the transformer filter injected grid currents. Optional AC filters are exploited to supply the reactive power and also to filter Pulse-Width Modulation (PWM) harmonics.

![Figure 1. VSC-HVDC transmission system](image)

The various control methods developed in view of controlling the VSC-HVDC system can operate suitably in case of a communication failure. Specially the Master-Slave method and the Voltage Droop method [22]. The Voltage Droop method is typically used when more than one converter is controlling the DC voltage at the same time. On the other hand, the Master-Slave method is a simple control method adapted to command the VSC-HVDC system. Indeed, there is only one converter called Master one. It controls the DC bus voltage to keep it in a certain range. While the other converters which are called Slave, control their power by injecting or extracting the power to from the DC grid. For point to point HVDC structure the Master Slave control method is usually sufficient [22].

The dq rotating structure used for the modelling and controlling of an AC system is employed with the aim of attaining stationary electrical equations. To pathway the AC grid voltage frequency and the phase angle, a Phase Lock Loop (PLL) is applied. In such a case, the q-axis voltage is null and the d-axis is synchronized with the grid voltage vector and has similar amplitude as the point of common coupling voltage vector. Thus, the active and reactive powers are separately controlled by current constituent [9].

As shown in Figure 2 which presents a VSC substation controlled in dq synchronous frame, the control system is divided into two parts: the inner control loop is used to control the currents (\(i_{sd}\) and \(i_{sq}\)) by adjusting d and q coordinates of modulated voltages (\(V_{sd}\) and \(V_{sq}\)) in the Park frame, and the outer control loop is used to achieve precise functions: The d-axis current reference (\(i_{sdref}\)) can be generated by an active power or DC voltage controller. The q-axis current reference (\(i_{sqref}\)) can be obtained by a reactive power or AC voltage controller.
where:

- $R_s, L_s$: Resistance, Inductance of the phase reactors associated with the interface transformer filter.
- $C_s$: DC capacitors.
- $L_{sr}$: Smoothing reactors.
- $L_{g} C_g$: Inductance, Capacitance of the AC filter.

$(v_{gi}, i_{gi})$: The couple voltage-current of an AC grid.
$(u_{si}, i_{ci})$: The couple voltage-current of the DC bus.
$(v_{mdi-ref}, v_{mqi-ref})$: Direct and quadrature control voltages of the converter.
$(v_{mi}, i_{mi})$: Couple voltage-current delivered by the converter.

$(P_{gi}, Q_{gi})$: The active and reactive power through the system.
$(U_{si-ref})$: The DC Voltage reference.

$(P_{gi-ref}, Q_{gi-ref})$: The reference active and reactive power delivered by the transformer.

$(P_{mi}, Q_{mi})$: Active and reactive power delivered by the converter.

$i_{si}$: The phase reactor current.
$i_{li}$: The cable current.
$U_{Cci}$: The voltage cable.

The $i$ index $\epsilon \{1, 2\}$, it indicates the station number.

### 2.2 Stability study of the HVDC link via small signal stability method

#### 2.2.1 Linearization of a VSC-HVDC link

The stability of a system is its capability, for an operating point to find equilibrium after a physical disturbance. Traditionally, the system stability is tested when it is submissive to small disturbance. Therefore, a linearized model of the system is created to analyse its stability [7-23]. In this part, we present a technique to model VSC-HVDC converter station for dynamic study.

In this work, the converter is deemed ideal and it is simulated by its average model in view of not taking into account harmonics effects [24], which are not our a matter of interest in this work. Indeed, the average-value model of the converter has the same dynamic performance of the detailed model, it is also more efficient for simulation time steps as explained in the study [25]. In this section, the linearization of the various components of a HVDC liaison is presented.

The general state space of a linearized system is expressed by [26]:

$$\begin{align*}
\Delta \dot{X} &= AX + BU \\
\Delta Y &= CX + DU
\end{align*}$$

where, for each considered subsystem: $\Delta X$ is the state vector, $\Delta U$ is the input vector and the output vector is presented by $\Delta Y$, $A$ is the state matrix, the input matrix is $B$, the output matrix is presented by $C$, and the feed-forward matrix is presented by $D$.

We notice that the linearized block diagram of each subsystem presented in Figures 3, 4, 5 and 6 contains a proportional integral (PI) regulator. Therefore, to facilitate the creation of a global state space model, we choose to change the model defined by Eq. (1) to an increased one by adding a new state auxiliary vector "$\Delta Z$" to the main state vector "$\Delta X$" in order to eliminate the use of the controller [9]. Thus, the new state space system is obtained:

$$\begin{align*}
\Delta \dot{X} &= AX' + B' \Delta U' \\
\Delta Y &= CX' + DU'
\end{align*}$$

with $\Delta X' = \begin{bmatrix} \Delta X \\ \Delta Z \end{bmatrix}$

where, $\Delta X'$ is the new state vector, $\Delta U'$ is the new input vector, the new output vector is presented by $\Delta Y'$, $A'$ is the new state matrix, $B'$ is the new input matrix, new output matrix is given by $C'$ and finally, $D'$ is the new feed-forward matrix. However, when the state matrix $A'$ is null, we fix the controlled state vector $\Delta U'$ as: $\Delta U' = k \Delta X'$. With: $k$ is the gain matrix assuring the stability of the system.

**Figure 2.** Point-to-point controlled VSC-HVDC link
A current controller linear model

The model of the VSC-HVDC system associated to its current control loop as given in Figure 2 is the basic structure for such VSC system connected to grid. Disregarding the switching losses, power at both sides of the converter is the same: Assumptions are made with the aim of simplifying the complexity of the system and making the AC side of the VSC not to impact the DC system. With this assumption and referring to Figure 2, we can assume that:

\[ V_{\text{mod}} I_{\text{mod}} + V_{\text{que}} I_{\text{que}} = U_{\mu} I_{\mu} = P_{\mu} \]  

Eq. (4) is not linear. So, with the aim of evaluating its stability, we should linearize the system around an operating point. Each quantity is prescribed by an operating point denoted by capital letter and the subscript 0, and a small variation denoted by the Greek symbol. After linearization, Eq. (4) becomes:

\[ \Delta V_{\text{mod}} I_{\text{mod}} + \Delta I_{\text{mod}} V_{\text{mod}} + \Delta V_{\text{que}} I_{\text{que}} + \Delta I_{\text{que}} V_{\text{que}} = \Delta U_{\mu} I_{\mu} + \Delta I_{\mu} U_{\mu} \]  

The \( i \) index refers to the station number which is in this case \( \epsilon \{1, 2\} \).

Thus, the linearized current controller of a VSC is presented in Figure 3.

The steady state error between the reference and the output current ought to be avoided by the integration of a PI controller in the current controller. The corrector gain of the current controller PI corrector is defined by:

\[ PI = \frac{K_p + K_i}{p} \]  

With: \( p \) presents Laplace Coefficient. The transfer function (TF) of the current control is the same for the two axes. The expression of the d-axis current control loop transfer function is expressed by:

\[ i_{\text{ad}}(p) = \frac{1 + K_p}{K_i} p + \frac{L_\alpha}{K_i} p^2 \]  

The formulations of \( K_p \) and \( K_i \) deduced from the pole placement method, are:

\[ K_p = 2\zeta \omega_n C_s \] and \( K_i = \frac{L_s}{\omega_n^2} \)  

where: \( \zeta \) is the damping ratio.

\( \frac{3}{\omega_n} \) is the natural frequency [rad/s].

To create the new state space current controller, we choose new state variables \( \Delta Z_1 \) and \( \Delta Z_2 \) such as:

\[ \begin{bmatrix} \Delta Z_{1i} \\ \Delta Z_{2i} \end{bmatrix} = \begin{bmatrix} \frac{-R}{L_s} & 0 & 0 & \frac{1}{L_s} & 0 \\ 0 & \frac{-R}{L_s} & 0 & 0 & \frac{1}{L_s} \\ \frac{1}{C_s U_{\alpha}} & \frac{1}{C_s U_{\alpha}} & \frac{1}{C_s U_{\alpha}} & \frac{1}{C_s U_{\alpha}} & \frac{1}{C_s U_{\alpha}} \\ -1 & 0 & 0 & 0 & 0 \\ 0 & -1 & 0 & 0 & 0 \end{bmatrix} \begin{bmatrix} \Delta V_{\text{gdi}} \\ \Delta V_{\text{gqi}} \\ \Delta V_{\text{mgi}} \\ \Delta V_{\text{mqi}} \\ \Delta i_{\text{dri}} \end{bmatrix} \]  

\[ \frac{d\{\Delta Z_{1i}\}}{dt} = \frac{d\{\Delta Z_{2i}\}}{dt} \]  

The vectors and the following matrices represent the new linearized current controller state space representation according to the system (2).

\[ \Delta X' = \begin{bmatrix} \Delta i_{\text{adi}} \\ \Delta i_{\text{aqi}} \\ \Delta U_{\mu i} \\ \Delta Z_{1i} \\ \Delta Z_{2i} \end{bmatrix} \]  

\[ \Delta U' = \begin{bmatrix} \Delta i_{\text{adi}} \\ \Delta i_{\text{aqi}} \\ \Delta U_{\mu i} \end{bmatrix} ; \Delta Y' = \begin{bmatrix} \Delta i_{\text{adi}} \\ \Delta i_{\text{aqi}} \end{bmatrix} \]  

Figure 3. Linearized block diagram of the current controller VSC.
In order to reach a required response time, we neglect the interaction between converter controllers and the DC grid. Indeed, the conclusions of our analysis will be only due to the phase reactor and the shunt conductance capacitance losses.

\[ \Delta P_{\text{gi}} = \Delta V_{\text{gd}} I_{\text{sd}0} + \Delta i_{\text{di}} V_{\text{gd}0} \]  

(12)

\[ \Delta Q_{\text{gi}} = -(\Delta V_{\text{gd}} I_{\text{iq}0} + \Delta i_{\text{qi}} V_{\text{gd}0}) \]  

(13)

The PI power controller is expressed by:

\[ P_I = \frac{K_p}{p} + \frac{K_i}{p} \]  

(14)

The power control transfer function has the following form:

\[ \frac{P_G}{P_{G-\text{ref}}}(p) = \frac{1 + \frac{K_m}{K_r} p}{1 + \frac{K_m}{K_r} p} \]  

(15)

This TF is assimilated at a first order TF with a time constant \( \Gamma_p = \frac{K_r}{K_m} \). In order to reach a required response time, the parameters of the power controller are:

\[ K_m = 0 \text{ and } K_r = \frac{1}{\Gamma_p} \]  

(16)

According to the active power controller linearized block diagram defined in Figure 4, we define the new state variables “\( \Delta Z_{\text{di}} \)” and “\( \Delta Z_{\text{ri}} \)” respectively related to the active and reactive power controller:

\[ \begin{bmatrix} \frac{d\Delta Z_{w}}{dt} \end{bmatrix} = \begin{bmatrix} \Delta P_{\text{gi}} - \Delta i_{\text{di}} V_{\text{gd}0} - \Delta V_{\text{ph}} I_{\text{sd}0} \\ \Delta Q_{\text{gi}} - \Delta i_{\text{qi}} V_{\text{gd}0} - \Delta V_{\text{ph}} I_{\text{iq}0} \end{bmatrix} \]  

(17)

For the active power controller, we obtain:

\[ \begin{align*}
\Delta X &= \begin{bmatrix} \Delta i_{\text{di}, \text{ref}} \\ \Delta Z_{\text{w}} \end{bmatrix} \\
\Delta U &= \begin{bmatrix} \Delta P_{\text{gi}} \\ \Delta V_{\text{ph}} \end{bmatrix} \\
\Delta Y &= \Delta i_{\text{di}, \text{ref}}
\end{align*} \]  

(18)

\[ \begin{bmatrix} A' \\ B' \\ C' \end{bmatrix} = \begin{bmatrix} 0 & 0 \\ 0 & 1 \\ 0 & -1 \end{bmatrix} \]  

(19)

And for the reactive power controller, we obtain:

\[ \begin{align*}
\Delta X &= \begin{bmatrix} \Delta i_{\text{qi}, \text{ref}} \\ \Delta Z_{\text{w}} \end{bmatrix} \\
\Delta U &= \begin{bmatrix} \Delta Q_{\text{gi}} \\ \Delta V_{\text{ph}} \end{bmatrix} \\
\Delta Y &= \Delta i_{\text{qi}, \text{ref}}
\end{align*} \]  

(20)

\[ \begin{bmatrix} A' \\ B' \\ C' \end{bmatrix} = \begin{bmatrix} 0 & 0 \\ 0 & 1 \\ 0 & -1 \end{bmatrix} \]  

(21)

In this case the gain \( K \) defined in the respective feedback power controller, is fixed assuring the final form of the matrix \( A' = N'K \) [27].

B. DC voltage controller Linear Model

The balance of powers proves that the DC power is converted to real power in the AC part when we neglect the phase reactor and the shunt conductance capacitance losses. Indeed, the conclusions of our analysis will be only due to the interaction between converter controllers and the DC grid. So the relation between AC and DC side can be written as follows:

\[ i_m U_s = P_g = i_{sd} V_{gd} \]  

(22)

Thus, the linearization of the Eq. (22) is developed as follows:

\[ I_{\text{m0}} \Delta U_{\text{ai}} + U_{\text{x0}} \Delta i_{\text{mi}} = I_{\text{sd0}} \Delta V_{\text{gd}0} + \Delta i_{\text{di}, \text{ref}} V_{\text{gd}0} \]  

(23)

The PI used in the DC voltage controller is expressed by:
\[ PI = \frac{K \mu p + K_n}{p} \]  

The linearized model of the DC voltage controller is illustrated in Figure 5.

**Figure 5.** Block diagram of the linearized DC voltage controller

The voltage control loop transfer function is:

\[ \frac{U_{a,ref}}{U_{si,ref}}(p) = \frac{1}{1 + \frac{K_{in}}{K_v} p + \frac{C_i}{K_v} p^2} \]  

The parameters of the controller are deduced by identifying the polynomial characteristic of the voltage TF to the desired second order polynomial.

\[ K_v = C_i w_n^2 \text{ and } K_{in} = 2 \zeta w_n C_i \]  

where, \( \zeta \) is the damping ratio and \( w_n \) is the natural frequency.

In accordance with the linearized block diagram of the voltage controller defined in Figure 5, a new state variable \( \Delta Z_{si} \) ought to be created.

where,

\[ \frac{d \Delta Z_{si}}{dt} = \Delta U_{si,ref} - \Delta U_{si} \]  

Thus, we obtain:

\[ \Delta X' = \begin{bmatrix} \Delta X'_{si,ref} \\ \Delta Z_{si} \end{bmatrix}; \quad \Delta U' = \begin{bmatrix} \Delta U_{si,ref} \\ \Delta U_{i,ref} \end{bmatrix}; \quad \Delta Y' = \Delta i_{adi-ref} \]  

\[ A' = \begin{bmatrix} 0 & 0 \\ 0 & 0 \end{bmatrix}; \quad B' = \begin{bmatrix} 0 & 0 & 0 \\ 1 & 0 & -1 \end{bmatrix}; \quad C' = \begin{bmatrix} 0 & -U_{a,ref} \\ U_{a,ref} \end{bmatrix} \]  

and \( D' = \begin{bmatrix} 0 & -I_{a,ref} & I_{a,ref} & 0 \\ -V_{a,ref} & V_{a,ref} & U_{a,ref} & V_{a,ref} \end{bmatrix} \)

2.2.2 Studied VSC-HVDC model association

To find a liaison between two subsystems which are constituents of a total system \( \Sigma_r \), two systems \( \Sigma A \) and \( \Sigma B \) are designed.

The index 'a' is an auxiliary subscribe identifying \( \Sigma A \). The state space model of \( \Sigma A \) is defined by:

\[ \Sigma A = \begin{bmatrix} \frac{d}{dt} X_{a1} \\ \frac{d}{dt} X_{a2} \\ Y_{a1} \\ Y_{a2} \end{bmatrix} = A_{a} \begin{bmatrix} X_{a1} \\ X_{a2} \\ X_{a1} \\ X_{a2} \end{bmatrix} + B_{a} \begin{bmatrix} U_{a1} \\ U_{a1} \\ U_{a2} \\ U_{a2} \end{bmatrix} \]  

The index 'b' is an auxiliary subscribe identifying \( \Sigma B \). The state space representation of \( \Sigma B \) is defined by:

\[ \Sigma B = \begin{bmatrix} \frac{d}{dt} X_{b1} \\ \frac{d}{dt} X_{b2} \\ Y_{b1} \\ Y_{b2} \end{bmatrix} = A_{b} \begin{bmatrix} X_{b1} \\ X_{b2} \\ X_{b1} \\ X_{b2} \end{bmatrix} + B_{b} \begin{bmatrix} U_{b1} \\ U_{b1} \\ U_{b2} \\ U_{b2} \end{bmatrix} \]  

In the whole system, the transfer matrix \( T_{ab} \) links the inputs of subsystem \( \Sigma A \) to the outputs of subsystem \( \Sigma B \):

\[ \begin{bmatrix} U_{b1} \\ U_{b2} \end{bmatrix} = T_{ab} \begin{bmatrix} Y_{a1} \\ Y_{a2} \end{bmatrix} \]  

In the same manner, the transfer matrix \( T_{ba} \) links the inputs of subsystem \( \Sigma B \) to the outputs of subsystem \( \Sigma A \) in the global system:

\[ \begin{bmatrix} Y_{a1} \\ Y_{a2} \end{bmatrix} = T_{ba} \begin{bmatrix} U_{b1} \\ U_{b2} \end{bmatrix} \]  

Hence, the state space representation of the global system is:

\[ \Sigma = \begin{bmatrix} \frac{d}{dt} X_{a1} \\ \frac{d}{dt} X_{a2} \\ \frac{d}{dt} X_{b1} \\ \frac{d}{dt} X_{b2} \\ Y_{a1} \\ Y_{a2} \\ Y_{b1} \\ Y_{b2} \end{bmatrix} = \begin{bmatrix} A_{a} & 0 & B_{a} & 0 \\ 0 & A_{b} & 0 & B_{b} \\ C_{a} & 0 & D_{a} & 0 \\ 0 & C_{b} & 0 & D_{b} \end{bmatrix} \begin{bmatrix} X_{a1} \\ X_{a2} \\ X_{b1} \\ X_{b2} \end{bmatrix}+ \begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \end{bmatrix} \]

Presuming that the state variables of the total system are a juxtaposition of subsystems state variables in order to build the system \( \Sigma \) based to [28]. As well, a combination of subsystem inputs constitutes the inputs of the total system. To retrieve the state variables of subsystems within the total system, subsystems are arranged in the order in the total system.

The form of the state space matrices \( A, B, C \) and \( D \) of the global system which are resulted from the associations of state space matrices of each subsystem are defined by [28]:

\[ \begin{bmatrix} A & B T & C & D \\ 0 & B T & D \end{bmatrix}; \quad B = \begin{bmatrix} B_{a} & B_{b} \end{bmatrix}; \quad C = \begin{bmatrix} 0 & C_{a} & 0 & C_{b} \end{bmatrix}; \quad D = \begin{bmatrix} 0 & D_{a} & 0 & D_{b} \end{bmatrix} \]  

A. State space model of station 1

In order to find the state space model of station 1, we studied the link between the state space model of the voltage controller and the state space model of the active power controller with
the state space model of the current controller as presented in Figure 6.

First, a development of the state space model of the voltage controller with the state space model of the reactive power controller is done. The reached model is deduced directly from the state space model of each subsystem, so we obtain:

\[
\frac{d}{dt} \begin{bmatrix} \Delta V_{g1,ref} \\ \Delta Z_{g1} \\ \Delta i_{s1,ref} \\ \Delta Z_{s1} \end{bmatrix} = \begin{bmatrix} p_3 & p_6 & 0 & 0 \\ 0 & p_3 & p_4 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 0 & 0 \end{bmatrix} \begin{bmatrix} \Delta V_{g1,ref} \\ \Delta Z_{g1} \\ \Delta i_{s1,ref} \\ \Delta Z_{s1} \end{bmatrix} + \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} V_{g1,ref} \\ I_{g1,ref} \\ U_{s1,ref} \\ Q_{g1,ref} \end{bmatrix}
\]

\[
\frac{d}{dt} \begin{bmatrix} \Delta i_{q1,ref} \\ \Delta U_{s1,ref} \end{bmatrix} = \begin{bmatrix} -\frac{U_{s1}}{V_{g1,ref}} & 0 \\ 0 & -\frac{1}{V_{g1,ref}} \end{bmatrix} \begin{bmatrix} \Delta V_{g1,ref} \\ \Delta Z_{g1} \\ \Delta i_{s1,ref} \\ \Delta Z_{s1} \end{bmatrix} + \begin{bmatrix} 0 & \frac{I_{g1,ref}}{V_{g1,ref}} & \frac{U_{s1}}{V_{g1,ref}} & 0 \\ 0 & 0 & 0 & 0 \end{bmatrix} \begin{bmatrix} I_{g1,ref} \\ V_{g1,ref} \\ \Delta U_{g1,ref} \\ \Delta Q_{g1,ref} \end{bmatrix}
\]

\[
T_{ab1} = \begin{bmatrix} \Delta U_{s1,ref} \\ \Delta i_{s1,ref} \end{bmatrix} = \begin{bmatrix} \Delta U_{g1,ref} \\ \Delta V_{g1,ref} \\ \Delta i_{s1,ref} \\ \Delta Z_{s1} \end{bmatrix}
\]

\[
T_{ba1} = \begin{bmatrix} 0 & 1 \\ -1 & 0 \end{bmatrix} \begin{bmatrix} \Delta U_{s1,ref} \\ \Delta i_{s1,ref} \end{bmatrix}
\]

![Figure 6. Global model of station1](image)

Then, to have the state space model of station1, we determined the transfer matrix \( T_{ab1} \) between the inputs of subsystem obtained by the voltage and reactive power controllers’ models connected together and the output of the current controller model. This matrix is defined as:

\[
\frac{d}{dt} \begin{bmatrix} \Delta i_{g1,ref} \\ \Delta i_{q1,ref} \\ \Delta U_{g1,ref} \\ \Delta V_{g1,ref} \\ \Delta U_{s1,ref} \\ \Delta i_{s1,ref} \\ \Delta Z_{g1} \\ \Delta Z_{s1} \\ \Delta i_{g1} \\ \Delta i_{q1} \\ \Delta V_{g1} \\ \Delta Z_{g1} \\ \Delta U_{g1} \\ \Delta i_{s1} \\ \Delta Z_{s1} \\ \Delta i_{s1} \end{bmatrix} = \begin{bmatrix} \Delta i_{g1,ref} \\ \Delta i_{q1,ref} \\ \Delta U_{g1,ref} \\ \Delta V_{g1,ref} \\ \Delta U_{s1,ref} \end{bmatrix} + \begin{bmatrix} \Delta i_{g1} \\ \Delta i_{q1} \\ \Delta V_{g1} \\ \Delta Z_{g1} \\ \Delta U_{g1} \\ \Delta i_{s1} \\ \Delta Z_{s1} \\ \Delta i_{s1} \end{bmatrix}
\]

\[
T_{ba1} = \begin{bmatrix} \Delta i_{g1,ref} \\ \Delta i_{q1,ref} \end{bmatrix} = \begin{bmatrix} \Delta i_{g1} \\ \Delta i_{q1} \\ \Delta V_{g1} \\ \Delta Z_{g1} \\ \Delta U_{g1} \\ \Delta i_{s1} \\ \Delta Z_{s1} \\ \Delta i_{s1} \end{bmatrix}
\]

So, we deduced the global state space model of station1, as given in system (39):

\[
\begin{align*}
\frac{d}{dt} \begin{bmatrix} \Delta i_{g1,ref} \\ \Delta i_{q1,ref} \\ \Delta U_{g1,ref} \\ \Delta V_{g1,ref} \\ \Delta U_{s1,ref} \end{bmatrix} &= \begin{bmatrix} \Delta i_{g1,ref} \\ \Delta i_{q1,ref} \\ \Delta U_{g1,ref} \\ \Delta V_{g1,ref} \\ \Delta U_{s1,ref} \end{bmatrix} + \begin{bmatrix} \Delta i_{g1} \\ \Delta i_{q1} \\ \Delta V_{g1} \\ \Delta Z_{g1} \\ \Delta U_{g1} \\ \Delta i_{s1} \\ \Delta Z_{s1} \\ \Delta i_{s1} \end{bmatrix} \\
T_{ba1} &= \begin{bmatrix} 0 & 1 \\ -1 & 0 \end{bmatrix} \begin{bmatrix} \Delta i_{g1,ref} \\ \Delta i_{q1,ref} \end{bmatrix}
\end{align*}
\]
B. State Space model of station2

In the same way, we determined the state space model of station2, as presented in Figure 7. This station is controlled by the following controllers: active power, reactive power, and current controller.

![Figure 7. Model of station2](Image)

First, a development of the state space model of the active power controller with the state space model of the reactive power controller is done. The reached model is deduced directly from the state space model of each subsystem, so, we obtain:

\[ p_j; j \in (1,2,3,4) \] is a constant value, representing the elements of the state matrix of the system (40).

Then, in order to get the state model of station2, we should determine the transfer matrix \( T_{b2} \) between the inputs of subsystem obtained by the active and reactive power controllers’ models connected together and the output of the current controller model. This matrix is defined as:

\[
T_{b2} = \begin{bmatrix}
\Delta P_{s2-ref} \\
\Delta i_{a2} \\
\Delta V_{p2} \\
\Delta Q_{r2-ref} \\
\Delta i_{q2-ref}
\end{bmatrix} \begin{bmatrix}
1 & 0 & 0 & 0 \\
0 & 0 & 0 & 1
\end{bmatrix}
\]

(41)

The transfer matrix \( T_{b2} \) given in Eq. (42), links the inputs of the active power controller model with the reactive power controller model and the output of the current controller model.

\[
T_{b2} = \begin{bmatrix}
\Delta P_{s2-ref} \\
\Delta i_{a2} \\
\Delta V_{p2} \\
\Delta Q_{r2-ref} \\
\Delta i_{q2-ref}
\end{bmatrix} = \begin{bmatrix}
\Delta P_{s2-ref} \\
\Delta i_{a2} \\
\Delta V_{p2} \\
\Delta Q_{r2-ref} \\
\Delta i_{q2-ref}
\end{bmatrix}
\]

(42)

So we deduced the state space model of station2:
C. State Space model of system reactor and cable

The connection between the improved cable model and the smoothing reactor system model is illustrated in Figure 8.

![Figure 8](image)

**Figure 8.** Connection of the cable to the smoothing reactor

To determine the state space model of this association, we determined the transfer matrix $T_{a(b)}$ between the inputs of subsystem built from cable model and the output of the smoothing reactor model. This matrix is defined as:

$$
\begin{bmatrix}
\Delta i_{I1} \\
\Delta i_{I2} \\
\Delta U_{c1} \\
\Delta U_{c2} \\
\Delta i_{L1} \\
\Delta i_{L2}
\end{bmatrix} =
\begin{bmatrix}
p_1 & p_8 & \frac{1}{L_r} & 0 & 0 & 0 \\
p_9 & p_{10} & 0 & \frac{1}{L_r} & 0 & 0 \\
-1 & 0 & -G_c & C_r & 0 & -1 \frac{1}{C_r} & 0 \\
0 & -1 & C_r & 0 & -G_c & \frac{1}{C_r} & 0 \\
0 & 0 & L_{c2} & L_{c2} - M_{c12}^2 & \frac{L_{c2} - M_{c12}^2}{L_{c12}} & \frac{L_{c2} - M_{c12}^2}{L_{c12}} & \frac{-R_c L_{c2}}{L_{c12}} - \frac{R_c M_{c12}}{L_{c12}} & \frac{-R_c L_{c2}}{L_{c12}} - \frac{R_c M_{c12}}{L_{c12}} & R_c M_{c12} \\
0 & 0 & L_{c2} & L_{c2} - M_{c12}^2 & \frac{L_{c2} - M_{c12}^2}{L_{c12}} & \frac{L_{c2} - M_{c12}^2}{L_{c12}} & \frac{-R_c L_{c2}}{L_{c12}} - \frac{R_c M_{c12}}{L_{c12}} & \frac{-R_c L_{c2}}{L_{c12}} - \frac{R_c M_{c12}}{L_{c12}} & R_c M_{c12}
\end{bmatrix}
\begin{bmatrix}
\Delta i_{I1} \\
\Delta i_{I2} \\
\Delta U_{c1} \\
\Delta U_{c2} \\
\Delta i_{L1} \\
\Delta i_{L2}
\end{bmatrix}
$$

Therefore, we obtained the state space model of the reactor, cable system association:

$$
T_{a(b)} = \begin{bmatrix}
\Delta U_{a} \\
\Delta U_{a1} \\
\Delta U_{a2} \\
\Delta U_{a3}
\end{bmatrix} = \begin{bmatrix}
0 & 0 \\
1 & 0 \frac{1}{2} \\
0 & 0 \frac{1}{2} \\
0 & 0 \frac{1}{2}
\end{bmatrix}
\begin{bmatrix}
\Delta i_{I1} \\
\Delta i_{I2} \\
\Delta i_{L1} \\
\Delta i_{L2}
\end{bmatrix}
$$

where, $U_{ccl} = \frac{u_i}{2}$.

The transfer matrix $T_{a(b)}$ given in Eq. (45), links the inputs of the cable model and the output of the smoothing reactor model is defined by:

$$
T_{a(b)} = \begin{bmatrix}
\Delta i_{I1} \\
\Delta i_{I2} \\
\Delta U_{c1} \\
\Delta U_{c2} \\
\Delta i_{L1} \\
\Delta i_{L2}
\end{bmatrix} = \begin{bmatrix}
1 & 0 & 0 & 0 \\
0 & 1 & 0 & 0 \\
0 & 0 & 1 & 0 \\
0 & 0 & 0 & 1 \\
0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0
\end{bmatrix}
\begin{bmatrix}
\Delta i_{I1} \\
\Delta i_{I2} \\
\Delta U_{c1} \\
\Delta U_{c2} \\
\Delta i_{L1} \\
\Delta i_{L2}
\end{bmatrix}
$$

D. State space model of the studied VSC-HVDC system

In the preceding parts, each linearized subsystem of the VSC-HVDC liaison is modeled as a linear state space model. With the aim of getting a total closed-loop model of the VSC-HVDC system, inputs and outputs of subsystems are linked together as shown in Figure 9.

After developing the link between the systems composed of station 1, station 2 and the system smoothing reactor with cable, we obtain the final state matrix $A$ of the VSC-HVDC System. As well, the final state vector $\Delta X'$, the final input vector $\Delta U'$ and the final output vector $\Delta Y'$ of the VSC-HVDC system are given as:

$$
X_{VSC-HVDC} = \begin{bmatrix}
\Delta i_{d1-ref} \\
\Delta i_{q1-ref} \\
\Delta Z_r \\
\Delta i_{d1} \\
\Delta i_{q1} \\
\Delta U_{r1} \\
\Delta Z_{11} \\
\Delta Z_{21} \\
\Delta i_{d2-ref} \\
\Delta i_{q2-ref} \\
\Delta Z_{r2} \\
\Delta i_{d2} \\
\Delta i_{q2} \\
\Delta U_{r2} \\
\Delta U_{c1} \\
\Delta i_{L1} \\
\Delta U_{c2} \\
\Delta i_{L2}
\end{bmatrix}^T
$$

$$
Y_{VSC-HVDC} = \begin{bmatrix}
\Delta i_{d1-ref} \\
\Delta i_{q1-ref} \\
\Delta i_{d1} \\
\Delta i_{q1} \\
\Delta U_{r1} \\
\Delta i_{d2-ref} \\
\Delta i_{q2-ref} \\
\Delta i_{d2} \\
\Delta i_{q2} \\
\Delta U_{r2} \\
\Delta U_{i1} \\
\Delta U_{i2}
\end{bmatrix}^T
$$
$$U_{\text{VSC-HVDC}} = \begin{bmatrix} \Delta U_{i1-\text{ref}} & \Delta V_{g11} & \Delta U_{i3} & \Delta \eta_{11} & \Delta \xi_{q1-\text{ref}} & \Delta \xi_{i1-\text{ref}} & \Delta \xi_{q2-\text{ref}} & \Delta \xi_{i2-\text{ref}} & \Delta P_{g1-\text{ref}} & \Delta I_{ad1-\text{ref}} & \Delta V_{m1} & \Delta Q_{g2-\text{ref}} \\ \Delta i_{sq1} & \Delta i_{ad2-\text{ref}} & \Delta i_{sq2-\text{ref}} & \Delta i_{i1} & \Delta i_{i2} & \Delta U_{ec1} & \Delta U_{ec2} \end{bmatrix}^T$$

$$A_{\text{VSC-HVDC}} = \begin{bmatrix} 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \end{bmatrix}$$

Figure 9. Closed-loop linearized model of the controlled point-to-point VSC-HVDC link
where, \( a_1 = \frac{-V_{mdc}i_{lag0}+z'_1}{c_z} \); \( a_2 = \frac{-V_{mga}i_{lag0}+z'_2}{c_z} \); \( a_3 = \frac{i_{ino}}{c_z} + \frac{z'_2}{c_z} \); \( a_4 = \frac{-i_{ino}}{c_z} + \frac{z'_3}{c_z} \); \( a_5 = \frac{-i_{ino}+z'_3}{c_z} \).

### 3. SIMULATION RESULTS

All the elements of the VSC-HVDC system are modeled separately, then they are gathered to generate the desired HVDC system topology. The principle is to place the pre-constructed blocks corresponding to each element of the system (power sources, VSC converters, lines or nodes), to configure the system parameters and to use the various control techniques detailed previously. The simulation of this HVDC system was realized by using the MATLAB Simulink software for 50 MW transferred power and 100 [km] DC transmission cable during 10 [s]. We consider that the first converter performs the role of the master and it adjusts the voltage at 640 [KV] and the second converter is the slave, it controls the power flow with power reference submitted into the DC network equal to 50[MW]. Results reached by «Matlab/Simulink» for the HVDC-VSC state space model and its average model are compared in Figures 10, 11, 12 and 13.

The evolution of the linearized current controller VSC in each station is similar to the evolution of its average model. Therefore, the linearized current controller model in VSC-HVDC link is proved.

The architecture of the point to point VSC-HVDC system studied is composed of 2 converters. All the currents, voltages and powers control loops are implemented in the converters control. The small instability found in the initial period of the average current is caused by the sizing of the proportional gain \( K_p \) of the PI regulators. So, in the start-up, the reverse power of the converter in the network can lead to instability because the converter will sometimes inject power and sometimes take power. This instability was shown during the response of the different controlled variables of the currents, powers, and voltage controls loops. Knowing that, the calculation of the regulators is based on the decoupling and compensation technique used in the control technique of the linear systems. As a solution, we can act on the gain \( K_p \) empirically to improve it in order to have less significant oscillations.

The evolution of the powers transmitted through the two converters of the VSC system is given in Figure 11. In accordance with this figure, the complementarity of the waveforms of powers transmitted through the two stations during time simulation is assured. The DC link was transferring 50 [MW] from Station 2 which controls the power transfer to station 1 which controls the DC bus voltage.

In addition, it is notable that the evolution of the linearized active power in each station is similar to the evolution of the average model. Hence, the linearized active power controller model in VSC-HVDC link is proved.

Whenever station 2 changes its power flow, there is an excess of the current \( i_{l2} \) into the DC cable from the sending station 2 to the receiving station 1. Thus, the negative sign of the current \( i_{l2} \) is clarified. While the current \( i_{l1} \) flows in the opposite path.

There is no remarkable difference between the waveform of the DC bus voltages \( U_{l1} \) and The DC bus voltages \( U_{l2} \), their curve is constant at 640 [KV] during time simulation as presented in Figure 13. In fact, the balance between the power generated by the station 2 and the power absorbed by the station 1 empowers the DC bus voltage to be adjusted to its reference value. Thus, this pace shows the effectiveness of the control loop used.

In addition, it is notable that the evolution of the linearized DC voltage in each station is similar to the evolution of the average model. Hence, the linearized voltage controller model in VSC-HVDC link is proved.
Figure 13. Voltage controller of the VSC-HVDC link state space model validation

Figure 14. Root locus of the VSC-HVDC link

Table 1. Eigenvalues of the VSC-HVDC system

<table>
<thead>
<tr>
<th>Number</th>
<th>Eigenvalues</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>-1.297</td>
</tr>
<tr>
<td>2</td>
<td>-2.838</td>
</tr>
<tr>
<td>3</td>
<td>-2.885</td>
</tr>
<tr>
<td>4</td>
<td>-2.885</td>
</tr>
<tr>
<td>5</td>
<td>-2.885</td>
</tr>
<tr>
<td>6</td>
<td>-7.778</td>
</tr>
<tr>
<td>7</td>
<td>-7.778</td>
</tr>
<tr>
<td>8</td>
<td>-7.778</td>
</tr>
<tr>
<td>9</td>
<td>-7.82</td>
</tr>
<tr>
<td>10</td>
<td>-100.002</td>
</tr>
<tr>
<td>11</td>
<td>-100.002</td>
</tr>
<tr>
<td>12</td>
<td>-100.002</td>
</tr>
<tr>
<td>13</td>
<td>100.125+2038j</td>
</tr>
<tr>
<td>14</td>
<td>-100.125+351j</td>
</tr>
<tr>
<td>15</td>
<td>-217.48+351j</td>
</tr>
<tr>
<td>16</td>
<td>-217.48-351j</td>
</tr>
<tr>
<td>17</td>
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</tr>
<tr>
<td>18</td>
<td>0</td>
</tr>
<tr>
<td>19</td>
<td>-1.39e-43</td>
</tr>
<tr>
<td>20</td>
<td>-3.410e-42</td>
</tr>
<tr>
<td>21</td>
<td>-236.17</td>
</tr>
<tr>
<td>22</td>
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<td>23</td>
<td>-19997.55</td>
</tr>
<tr>
<td>24</td>
<td>-9999999.99</td>
</tr>
</tbody>
</table>

As conclusion, we approved that the global state space model of the VSC-HVDC is validated. After that, the stability of the VSC-HVDC system will be checked by the eigenvalue-based Stability analysis.

Eigenvalues of the state matrix are resumed in Table 1. The number of eigenvalues is equal to the number of states variables: 10 for the current controller, 2 for the active power controller, 4 for the reactive power controller, 2 for the voltage power controller, 2 for the smoothing reactor, and 4 for the DC cable.

The plot of Figure 14 illustrates the roots referred to the total VSC-HVDC system, we notice that all the eigenvalues have a negative real part, the root is clearly oscillating but finally converges until reaching the abscissa axis. Which prove that the VSC-HVDC system is asymptotically stable.

4. CONCLUSION

Small-signal stability analyses (SSSA) becomes essential in the design of HVDC controllers in order to enhance their flexibility to faults, and to boost their aptitude to contribute to power grid operation.

The stability of two terminal VSC-HVDC system using the small signal stability technique for a linearized state space model is studied in this paper. The state space model of each subsystem was developed then a connection between each block was made to create a global linear state space model. The study of eigenvalues associated to the global linearized state space system is automatically used to improve the stability of the VSC-HVDC link. This work will help us to develop a generic method, with a view of providing flexible models to study the stability of a multi-terminal MTDC grid.

REFERENCES


**NOMENCLATURE**

<table>
<thead>
<tr>
<th>Abbreviation</th>
<th>Description</th>
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<tbody>
<tr>
<td>VSC</td>
<td>Voltage Source Converter</td>
</tr>
<tr>
<td>HVDC</td>
<td>High Voltage Direct Current</td>
</tr>
<tr>
<td>AC</td>
<td>Alternating Current</td>
</tr>
<tr>
<td>DC</td>
<td>Direct Current</td>
</tr>
<tr>
<td>LCC</td>
<td>Line-commutated converter</td>
</tr>
<tr>
<td>HVAC</td>
<td>High Voltage Alternating Current</td>
</tr>
<tr>
<td>MPPT</td>
<td>Maximum Power Point Tracking</td>
</tr>
<tr>
<td>PWM</td>
<td>Pulse-Width Modulation</td>
</tr>
<tr>
<td>PLL</td>
<td>Phase Lock Loop</td>
</tr>
<tr>
<td>PI</td>
<td>Proportional Integral</td>
</tr>
<tr>
<td>TF</td>
<td>Transfer Function</td>
</tr>
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</table>
The control loops characteristics are resumed in this table.

Table 2. VSC control loop characteristics

<table>
<thead>
<tr>
<th></th>
<th>Response Time [MS]</th>
<th>The damping ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>Current loop</td>
<td>10</td>
<td>0.7</td>
</tr>
<tr>
<td>Power loop</td>
<td>200</td>
<td>-</td>
</tr>
<tr>
<td>Voltage loop</td>
<td>100</td>
<td>0.7</td>
</tr>
<tr>
<td>PLL</td>
<td>1</td>
<td>0.7</td>
</tr>
</tbody>
</table>

For a DC transmission, two cables buried beneath the ground, are used. One for the positive pole and the other for the negative pole (Table 3).

Table 3. Cable parameters

<table>
<thead>
<tr>
<th></th>
<th>Cable 320 kV 2500 mm²</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cable section [mm²]</td>
<td>2500</td>
</tr>
<tr>
<td>Nominal current [A]</td>
<td>1800-2700</td>
</tr>
<tr>
<td>Re1: Core resistance [mΩ/km]</td>
<td>5.3</td>
</tr>
<tr>
<td>Lc1: Core inductance [mH/km]</td>
<td>3.6</td>
</tr>
<tr>
<td>Lc2: Screen inductance [mH/km]</td>
<td>3.5</td>
</tr>
<tr>
<td>Mc12: Core-screen mutual inductance [mH/km]</td>
<td>3.5</td>
</tr>
<tr>
<td>Re2: Screen resistance [mΩ/km]</td>
<td>60.2</td>
</tr>
<tr>
<td>Mc12: Core Screen mutual inductance [mH/km]</td>
<td>0.15</td>
</tr>
<tr>
<td>Gc1, Gc2: Core-to-ground conductances [μS/km]</td>
<td>0.06</td>
</tr>
<tr>
<td>Cc1, Cc2: Core to ground Capacitances [μF/km]</td>
<td>0.24</td>
</tr>
</tbody>
</table>