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Mitigation of Certain Power Quality Issues in Wind Energy Conversion System Using UPQC and IUPQC Devices

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ABSTRACT

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Keywords:

improved unified power quality conditioner, power quality, wind energy conversion system, voltage sag, voltage swell Due to limited conventional energy sources and to meet the increase of load demand there is a need for utilization of renewable energy sources. Among the all renewable energy sources wind energy is widely used and is highly sustainable as compared to other sources of energy. Many wind energy conversion devices working with doubly fed induction generators and synchronous generators and it is integrated to the grid produces the power quality issues like as voltage sags, swells, harmonics, voltage imbalance and short interruptions etc. Many power electronic based Flexible AC Transmission Systems (FACTS) are designed to solve above problems and facilitate to meet the required power demand. In this paper, UPQC (Unified Power Quality Conditioner) and IUPQC (Improved Unified Power Quality Conditioner) models are designed to mitigate the above power quality issues. In this paper, mainly voltage sags, voltage swells and harmonics are considered as a power quality issues to analyze the UPQC and IUPQC devices. The conventional PID controller is employed in control circuit of both the devices. It also discusses the comparative analysis between UPQC and IUPQC devices. The MATLAB/SIMULINK Software is used for above analysis.

1. INTRODUCTION

To meet the required load demand and decrease of fossil fuels, the conventional energy sources such as wind energy, solar energy, nuclear energy, fuel cell etc. are used for generation of electrical energy at generating stations. Among the all renewable energy sources the wind energy is one of the fastest development energy sources in worldwide. At present the wind energy is one of the major power suppliers to the distribution stations [1]. The total installed capacity of the wind power in India up to March 31st, 2019 was 36.625 GW. India is the fourth largest wind power capacity in the world. It expands across all the regions in India [2, 3]. Many issues are arises when the wind form is integrated to the power grid due to increase of nonlinear loads [4].

These issues are Capabilities of reactive power, Requirement of Power factor, Frequency and voltage ridethrough capabilities, Impacts on base system conditions, Short circuit and stability effects, Voltage instability and Harmonics and flicker effects [5]. Hence, it is very important to mitigate the above issues. There are many power electronic based devices are introduced to reduce the power quality issues. Active Power Filters, Flexible AC Transmission Systems (FACTS) devices and Shunt capacitors etc., are comes under this category [6].

The authors of the paper [7] published the different power quality issues occurred in the distribution system and there corresponding mitigating techniques. In paper [8], the authors explained designing a fuzzy logic control based SSSC device that improves the stability of the system. The mitigation of power quality issues using Static Synchronous Compensator (STATCOM) is discussed in papers [9, 10]. Jadhao et al. [11] and Pei and Chen [12] explained the enhancement of power quality using UPQC device.

In this paper at first, UPQC model is developed for mitigating the different power quality issues. Later, improved version of UPQC known as IUPQC model is developed for mitigating the above power quality issues. Conventional PID controller is used for designing the control circuit for both the devices. MATLAB/SIMULINK Software is used for designing the models and to analyze the performance of devices.

In this paper, the concept of WECS and the proposed test system are discussed in section II, UPQC and IUPQC models and concept of conventional PID (Proportional Integral Derivative) controller are explained in section III, simulation results of with and without FACTS devices are discussed in section IV and finally in section V the conclusions of this paper is described.

2. DYNAMICS OF PROPOSED SYSTEM

The proposed test system is shown in Figure 1. It consists of two parallel transmission lines and they are connected to the grid. For analyzing the system first, voltage sag, voltage swell and nonlinear disturbances are created in the first transmission line by including the capacitor load, creation of three phase fault and non linear load at different time instants with help of circuit breakers [13]. This results the second transmission line also experiences the sag, swell and nonlinear disturbances at different instants, because both transmission lines are connected in parallel. The second line also contain the DFIG based WECS and acted as generating source to the system. To overcome the above problems different Facts are connected to a power system at PCC. In this paper UPQC and IUPQC devices are connected at PCC (point of common coupling) point. The WECS mainly consists of wind turbine, wind generator and converter station. In this paper horizontal axis wind turbine, DFIG generating machines are modeled because of their advantages and the back to back voltage source converter called (AC-DC-AC converter) is used.



Figure 1. Single line diagram of proposed power system

2.1 Wind turbine modelling equations

The wind power developed by the turbine is given by the Eq. (1):

$$P_m = \frac{1}{2} \rho C_p(\lambda, \beta) A_r V_w^3 \tag{1}$$

where,

 ρ indicates the air density (Kgm³)

 V_w is the wind speed (m/s)

 C_p indicates the power coefficient

 λ indicates Tip – Speed Ratio

 β indicates the rotor blade pitch angle

Ar is the wind turbine rotor swept area (m^2) .

The power coefficient C_p can be calculated by using the following equation.

$$C_{p}(\lambda,\beta) = 0.73 \left(\frac{151}{\lambda_{i}} - 0.58\beta - 0.002\beta^{2.14} - 13.2 \right) e^{\frac{18.4}{\lambda_{i}}}$$
(2)

where,

$$\lambda_{i} = \frac{1}{\frac{1}{\lambda - 0.02\beta} - \frac{0.003}{\beta^{3} + 1}}$$
(3)

and

$$TSR(\lambda) = \frac{\omega_r R_r}{V_w}$$
(4)

2.2 Wind generator modelling equations

There are many generators are available in wind energy applications. Among the all Double Feb Induction Generator (DFIG) is chosen because of its advantages. Some of advantages of DFIG machine are controlling the active and reactive powers independently; PF (Power factor) can be controlled by varying rotor voltages, getting maximum energy etc., The equivalent circuit of DFIG machine as shown in Figure 2.



Figure 2. Equivalent circuit of DFIG

Following are the design equations of DFIG machine obtained from equivalent circuit as shown in Figure 2.

$$V_{ds} = R_s i_{ds} + \frac{d\psi_{ds}}{dt} - \omega_s \psi_{qs}$$
⁽⁵⁾

$$V_{qs} = R_s i_{qs} + \frac{d\psi_{qs}}{dt} + \omega_s \psi_{ds}$$
(6)

$$V_{dr} = R_s i_{dr} + \frac{d\psi_{dr}}{dt} - (\omega_s - \omega_r)\psi_{qr}$$
⁽⁷⁾

$$V_{qr} = R_s i_{qr} + \frac{d\psi_{qr}}{dt} + (\omega_s - \omega_r)\psi_{dr}$$
(8)

The ψ stands for flux linkage and express as:

$$\psi_{ds} = L_{ss}i_{ds} + L_m i_{dr} \tag{9}$$

$$\psi_{as} = L_{ss}i_{qs} + L_{m}i_{qr} \tag{10}$$

$$\psi_{dr} = L_{rr}i_{dr} + L_{m}i_{ds} \tag{11}$$

$$\psi_{qr} = L_{rr}i_{qr} + L_{m}i_{qs} \tag{12}$$

and also

$$L_{ss} = L_s + L_m \tag{13}$$

$$L_{rr} = L_r + L_m \tag{14}$$

3. EQUIVALENT CIRCUIT OF UPQC AND IUPQC

3.1 Unified power quality conditioner (UPQC)

The basic circuit diagram of UPQC as shown in Figure 3.

Unified Power Quality Conditioner (UPQC) is a combination of series and shunt compensation devices connected through a DC link capacitor. Voltage sags/swells, voltage unbalances, flickers and voltage harmonics are reduced using series compensation device [14, 15]. Current harmonics are reduced by using shunt compensation device. The intermediate DC link can be designed either with help of capacitor or inductor. The DC link capacitor interconnects the both shunt and series converters and maintains the constant DC bus voltage across it. The main principle of UPQC is to generate 3-phase voltages with the help of series and shunt converters. These converters are designed either voltage source type (VSI – Voltage Source Inverter) or current source type (CSI – Current Source Inverter).



Figure 3. Block diagram of unified power quality conditioner

The controlling of DC link voltage is the main role in order to achieve the desired UPQC performance. At the time of sudden load change conditions the feedback controller in the DC link can react as fast as possible to return the DC-link voltage at set reference value, with minimum delay as well as lower overshoot. The authors in papers [16, 17] developed the proportional–integral (PI)-regulator-based DC-link voltage controller in the UPQC to achieve desired response. To reduce response time of PI-controller-based approach in this paper PID controller is used in place of PI controller.

3.2 Improved Unified Power Quality Conditioner (IUPQC)

The drawbacks of UPQC can be overcome by using IUPQC. The IUPQC is similar to UPQC and contain the shunt and series converters [18]. The main difference between UPQC and IUPQC is in case of UPQC, the series power converter is controlled by non-sinusoidal voltage source and shunt converter is controlled by non-sinusoidal current source but in the case of IUPQC Shunt power converter carried out by sinusoidal voltage source and series power converter carried out by sinusoidal current source [19]. The schematic diagram of IUPQC device as shown in Figure 4. The internal control circuit of IUPQC device as shown in Figure 5.

The novel IUPQC device can perform [20]

(i) As a smart circuit breaker between grid and micro grid

(ii) Provide power flow between grid and micro grid

(iii) Boost up the Reactive power at series converter station(iv) Compensating the voltage/frequency at shunt converter station

(v) Harmonic voltage and current isolation between series and shunt converter stations.

(vi) It is compensating the voltage and current Imbalance.



Figure 4. Schematic diagram of IUPQC device



Figure 5. Control circuit for IUPQC device

3.3 Conventional PID controller

Many control structures and procedures are available for selecting a suitable controller for industrial application. In this paper conventional PID controller is used to control the UPQC and IUPQC devices [21]. This controller computes the error value which is the difference between a measured value and desired value and it minimizes the error by adjusting the process control inputs [22]. The schematic diagram of conventional PID controller as shown in Figure 6. In this, the proportional controller determines the reaction to the present error, the integral value determines the reaction based on the sum of recent errors, and the derivative value determines the reaction based on the rate at which the error has been changing.

The mathematical form of PID controller has

$$u(t) = K_{p}e(t) + K_{I}e(t) + K_{D}\frac{de(t)}{dt}$$
(15)

The desired closed loop response can be obtained by tuning three constant parameters K_P , K_D , and K_I values. Many methods are available for tuning the PID controller parameters. In this paper with help of Ziegler – Nichols method the PID controller parameters are tuned. By using this method the desired tuning parameters obtained for this system are

K_P=2.19277, K_I=3.07272 and K_D=0.172.



Figure 6. Schematic diagram of conventional PID controller

4. SIMULATION RESULTS

In this paper the performance of UPQC and IUPQC devices are discussed. These models are developed by using MATLAB/SIMULINK software. The conventional PID controller is used for determining the performance of these devices. At first, simulation results of UPQC with conventional PID controller is discussed, later simulation results of IUPQC device with conventional PID controlled is discussed. The MATLAB/SIMULINK model of test system as shown in Figure 7.

In this system at B3 point considered as a Point of Common Coupling (PCC) point. At that point UPQC and IUPQC devices are connected in order to reduce the different power quality issues occur at B3 point.

4.1 Simulation results of UPQC with conventional PID controller

The Simulink model of UPQC device operating with conventional PID controller is connected at PCC point in the second transmission line as shown in Figure 8.



Figure 7. MATLAB/SIMULINK model of test system



Figure 8. MATLAB/SIMULINK model for UPQC device connected at PCC



Figure 9. Voltage waveform measured at point B3



Figure 10. Current waveform measured at point B3



Figure 11. RMS voltage waveform measured at point B3



Figure 12. %THD measured in load voltage at t=0.3sec (during voltage sag)

Voltage waveform measured at load point B3 after connecting UPQC device with conventional PID controller as shown in Figure 9. Current waveform measured at load point B3 after connecting UPQC device with conventional PID controller as shown in Figure 10. Figure 11 shows RMS voltage waveform measured at point B3. A voltage sag is caused during the time period t=0.3sec to 0.5sec due to which the voltage decreased initial voltage 316V to 285V. Similarly, a voltage swell is caused during the time period t=0.7sec to 0.9sec due to which voltage is increased to 347V from an initial value of 316V. Also voltage waveform is distorted due to harmonics during the time period 1.1sec to 1.3sec. The decrease and increase of voltages are clearly observed in the RMS voltage wave form as shown in Figure 11.

Figure 12, Figure 13 and Figure 14 shows the harmonic spectra of load voltage (B3 point) for different time instants. %THD at t=0.3sec (Voltage sag harmonic) is 6.38%, at t=0.7 sec (Voltage swell harmonic) is 6.56%. and at t=1.1 sec (harmonics due to nonlinear load) is 6.07% respectively.



Figure 13. %THD measured load voltage at t=0.7sec (during voltage swell)



Figure 14. %THD measured in load voltage at t=1.1sec (during harmonics)

4.2 Simulation Results of IUPQC with Conventional PID Controller

The Simulink model of IUPQC device is same as that of UPQC device, only the internal control circuit is of IUPQC is differed from the UPQC device. The Simulink circuit of IPQC device connected at PCC point in the test system as shown in Figure 15. Simulink model of Internal control circuit of IUPQC device as shown in Figure 16.

Figure 17 shows voltage waveform measured at load point B3. Figure 18 shows the current waveform measured at load

point B3. By observing these figures, it is noticed that when IUPQC is controlled by conventional PID controller distortions in voltage and current waveforms have been reduced compared to UPQC device. Figure 19 shows RMS voltage waveform at B3 point. It can be noticed that voltage

drops to 295V during time period 0.3sec to 0.5sec due to voltage sag and voltage rises to 342V from an initial value of 316V. Also voltage waveform is distorted due to harmonics during the time period 1.1sec to 1.3sec.



Figure 15. MATLAB/SIMULINK Model for IUPQC device connected at PCC



Figure 16. Internal control circuit of IUPQC



Figure 17. Voltage waveform measured at point B3







Figure 19. RMS Voltage measured at point B3



Figure 20. % THD in load voltage waveform measured at t=0.3sec (during sag)

Figure 20, Figure 21 and Figure 22 represent %THD measured at point B3 in load voltage waveform for voltage sag, voltage swell and harmonic conditions respectively. It is observed that %THD measured during voltage sag at t = 0.3 sec is nearly 4.29%. %THD measured during voltage swell at t = 0.7 sec is nearly 4.93%. %THD measured at t = 1.1sec (when harmonics occur due to non-linear load conditions) is nearly 3.41%. Table 1 and Table 2 show the %THD and RMS

voltages of UPQC and IUPQC devices employing conventional PID controller.



Figure 21. %THD in load voltage waveform measured at t=0.7sec (during swell)



Figure 22. %THD in load voltage waveform measured at t=1.1sec (when harmonics occur due to non-linear load)

 Table 1. %THD comparative analysis of conventional PID controller with UPQC and IUPQC devices

% THD For V _{load} at	UPQC	IUPQC
t=0.3 sec (Voltage Sag)	6.38	4.29
t=0.7 sec (Voltage Swell)	6.56	4.93
t=1.1 sec (Harmonics)	6.07	3.41

 Table 2. RMS voltage comparative analysis of conventional

 PID controller with UPQC and IUPQC devices

	UPQC	RMS Voltage (Volts)
Reference Voltage	Reference Voltage	
316V decreased to 295V	316V decreased to 285V	SAG
Reference Voltage 316V increased to	Reference Voltage 316V increased to	SWELL
	316V increased to 347V	SWELL

5. CONCLUSIONS

In this paper the performance of UPQC and IUPQC devices are discussed by employing conventional PID controller. The different power quality issues are created at different time instants to analyse the performance of the system. Mainly voltage sag, voltage swell and harmonics are treated as a different power quality issue. MATLAB/SIMULINK software is used for designing the models and analyzing the systems. The UPQC and IUPQC devices with conventional PID controller are employed at the load point to mitigate the above power quality issues. Comparative simulation results depict that IUPQC device with conventional PID controller improves the performance of DFIG based WECS by offering better compensation for power quality issues and by providing required active and reactive powers compare to UPQC device.

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NOMENCLATURE

- *Pm* Mechanical Power developed by Wind Turbine
- *Cp* power coefficient
- λ Tip Speed Ratio
- β Pitch Angle
- ρ Air density
- Ar Rotor Area in m²
- V_w Velocity of wind m/sec
- ω_r Angular Velocity of rotor