



Design and Analysis of Multi-Harmonic Microstrip Filters for Parasitic Mitigation in High-Frequency Resonators

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ABSTRACT

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microstrip bandpass filter, X-band, parallel coupled line, parasitic compensation, multi-harmonic response, convolutional neural networks

In this paper, a fifth-order X-band parallel-coupled microstrip bandpass filter has been designed for high frequency resonator applications. In order to compensate the frequency detuning caused by the open-ended coupled lines fringing capacitance, Reverse Dielectric Calibrating approach has been implemented. By reversing the dielectric constant calculation from 3.78 to 3.39, the effective dielectric constant has been increased, thus the additional electrical length has been compensated and the desired resonant frequency has been retrieved. The designed filter has been modeled and optimized by using the Advanced Design System (ADS) software at the central frequency of 10 GHz. The simulated results indicate that the proposed filter has passband bandwidth of 800 MHz, which corresponds to 8% of the fractional bandwidth. The simulated insertion loss is about -0.144 dB, while the return loss is about -14.95 dB at the resonant frequency of 9.660 GHz. The calculated voltage standing wave ratio at 9.660 GHz is equal to 1.235, and it remains below 1.5 within the filter passband, thus good impedance matching has been achieved. The introduced approach enables frequency correction and adequate passband characteristics of X-band microstrip filters.

1. INTRODUCTION

In recent years, wireless communication systems, particularly those operating within the X-band frequency range (such as radar systems and satellite communications), have seen an increasing demand for high-precision and compact components. Bandpass filters (BPF) based on parallel coupled microstrip line technology are among the most critical components due to their ease of fabrication and seamless integration with Microwave Integrated Circuits (MICs) [1-3].

For X-band (around 10 GHz) parallel coupled microstrip bandpass filters are highly sensitive to parasitic effects. The fringing fields at the ends of the coupled resonators lead to additional parasitic capacitance, which increases the effective length of the lines and thus deviates the center frequency of the filter from the design value. While there are also publications on new types of filters as well as realizations in different technologies, the majority of the papers do not deal with the problem of the parasitic capacitance in the classical parallel coupled microstrip resonators directly [4]. Compensation of the caused detuning of the filter is of major interest in order to increase selectivity, bandwidth, compactness as well as suppression of harmonic frequencies. For 10 GHz bandpass filters even smallest changes of the effective permittivity, the line distance as well as the electrical length of the resonators have a great impact on the pass band characteristics of the filter. In this contribution a Reverse

Dielectric Calibrating approach is presented and for verification by means of simulations is applied in order to compensate the detuning caused by the parasitic capacitance and to achieve the desired X-band characteristics [5].

Liang and Chen [6] offered a filtering patch antenna using parasitic patches giving good performance for X-band filtering applications. It achieved good results that comply with engineering standards, as the filter operates at a center frequency of 10 GHz with a minimal error margin and a bandwidth of 800 MHz. The gain of the filter was close to the ideal value, ensuring high-efficiency energy transfer within the passband. The matching was ideal with the filter input, where the S-parameter (S_{11}) values reached -15 dB at their lowest level, which effectively contributes to reducing the reflected power. The validity of the filter design at the fifth order was verified through impedance analysis with the presence of five distinct resonant poles.

2. FILTER DESIGN AND MATHEMATICAL MODELING

2.1 Design specifications

The design process began by defining the basic parameters required for the filter, which are as follows: Filter type: Bandpass filter with parallel coupled microstrip lines. Center

frequency at 10 GHz. Sub bandwidth (Delta): 10% (i.e., 1 GHz). Response type: Chebyshev waveform of 0.5 dB. Filter order: $n = 5$ (resulting in 6 coupling stages). Dielectric constant (ϵ_r): 3.78. Dielectric thickness (H): 15 mils. Copper conductor thickness (T): 1.4 mil.

2.2 Admittance inverter constants for the bandpass filter design

To convert the typical Chebyshev response (Low-pass prototype) into a Bandpass design, we use the J parameters. These parameters are calculated using the typical values $g_0, g_1, \dots, g_n$ according to the following equations [7-10]:

For the first stage:

$$Z_0 J_{01} = \sqrt{\frac{\pi \Delta}{2 g_0 g_1}} \quad (1)$$

For intermediate stages ($j = 1$ to $n-1$):

$$Z_0 J_{j,j+1} = \frac{\pi \Delta}{2 \sqrt{g_j g_{j+1}}} \quad (2)$$

For the final stage:

$$Z_0 J_{n,n+1} = \frac{\pi \Delta}{2 \sqrt{g_n g_{n+1}}} \quad (3)$$

Calculating even and odd mode impedances once the JZ_0

values are obtained for each stage, the even (Z_{oe}) and odd (Z_{oo}) mode impedances are extracted using the following equations, as shown in Table 1, where $Z_0 = 50 \Omega$.

$$Z_{oe} = Z_0 [1 + JZ_0 + (JZ_0)^2] \quad (4)$$

$$Z_{oo} = Z_0 [1 - JZ_0 + (JZ_0)^2] \quad (5)$$

Table 1. Calculated even and odd mode impedances

Section	JZ_0	$Z_{oe} (\Omega)$	$Z_{oo} (\Omega)$
Section 1 & 6	0.261	66.5	40.3
Section 2 & 5	0.064	53.3	47.0
Section 3 & 4	0.048	52.5	47.7

The next step relies entirely on the values obtained in the previous steps; it involves converting the electric field to the physical field, which yields the dimensions W, S, and L. This is done using the DS LineCalc tool.

2.3 Physical dimensions of microstrip lines

Here we describe the process of how the lines were calculated for the microstrip filter [11]. For the synthesis of the coupled lines the odd and even mode impedances were transformed to line width, line spacing and physical length [12]. These values were used to synthesize the lines for the filter to work at 10 GHz using the LineCalc tool in the Advanced Design System (ADS), as shown in Figure 1, and the resulting initial values for the filter are listed in Table 2.

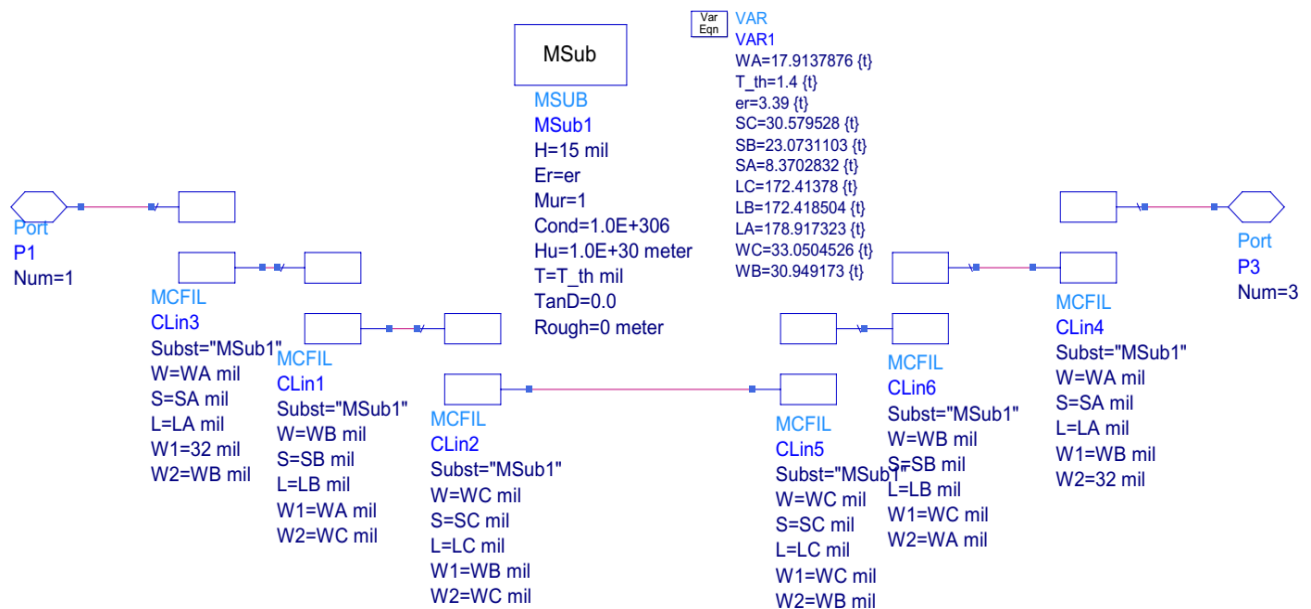


Figure 1. Microstrip parallel coupled line filter

Due to the high frequency, the open ends of the resonators cause fringing fields and thus additional parasitic capacitance. The electrical length of the coupled line sections is increased by these fringing fields and must be compensated in the design. In the initial simulation, the center frequency of 9.5 GHz was obtained with the nominal value of the relative permittivity of the substrate, $\epsilon_r = 3.78$. To compensate for this frequency deviation a Reverse Dielectric Calibrating, called calibration for short, is applied in ADS by using an equivalent

value of $\epsilon_r = 3.39$ for the design. This value must not be confused with the dielectric constant of the actually used substrate. The value of $\epsilon_r = 3.39$ is used as a design parameter in order to achieve the required effective electromagnetic behavior. The compensation of the additional electrical length can be performed by physical compensation, i.e., by retuning the coupled-line width, the line spacing, the resonator length and the open-end correction.

Table 2. Transmission line sections parameters

Section	Width W (mil)	Spacing S (mil)	Length L (mil)
Stage 1 & 6	25.46	6.97	177.8
Stage 2 & 5	29.95	33.41	173.4
Stage 3 & 4	30.04	42.58	173.4

The Reverse Dielectric Calibrating approach is used to determine an equivalent design parameter, the dielectric constant of the substrate. This dielectric constant is a fixed property of the actual substrate used for the microstrip structures. Thus, the equivalent dielectric constant obtained from the Reverse Dielectric Calibrating approach is translated into the physical microstrip dimensions, for instance the line width, the line spacing, the length of the resonators, and the open-end corrections, which all together reproduce the required effective electromagnetic behavior [13, 14].

Once all of the adjustments were made to the various physical parameters of the various microstrip components, the resulting parameters were then entered into the LineCalc tool of the ADS to calculate the final dimensions of all of the microstrip components, including the line width, the spacing between the center strips of the coupled lines, and the physical length of the resonators [15], as shown in Table 3.

Table 3. Transmission line sections parameters after tuning

Section	Width W (mil)	Spacing S (mil)	Length L (mil)
Stage 1 & 6	17.9137876	8.3702832	178.917323
Stage 2 & 5	30.949173	23.0731103	172.418504
Stage 3 & 4	33.0504526	30.579528	172.41378

The dielectric constant for this design does not change the physical substrate that has been selected for this work. The dielectric constant of 3.39 that has been calculated here is used as an effective dielectric/design-calibration parameter in the design environment ADS.

The electrical length that is added by the fringing fields, as well as the additional parasitic capacitance at the open ends of the coupled resonators is, in practice, compensated for by retuning the physical microstrip design (e.g. the width and space between coupled transmission lines, the length of the resonators and the open-end correction). The Reverse Dielectric Calibrating approach therefore is used for design correction in order to return the design to its original resonant frequency of 10 GHz by physical adjustment of the filter geometry on the actual substrate which is used for the fabrication.

3. RESULTS AND DISCUSSION

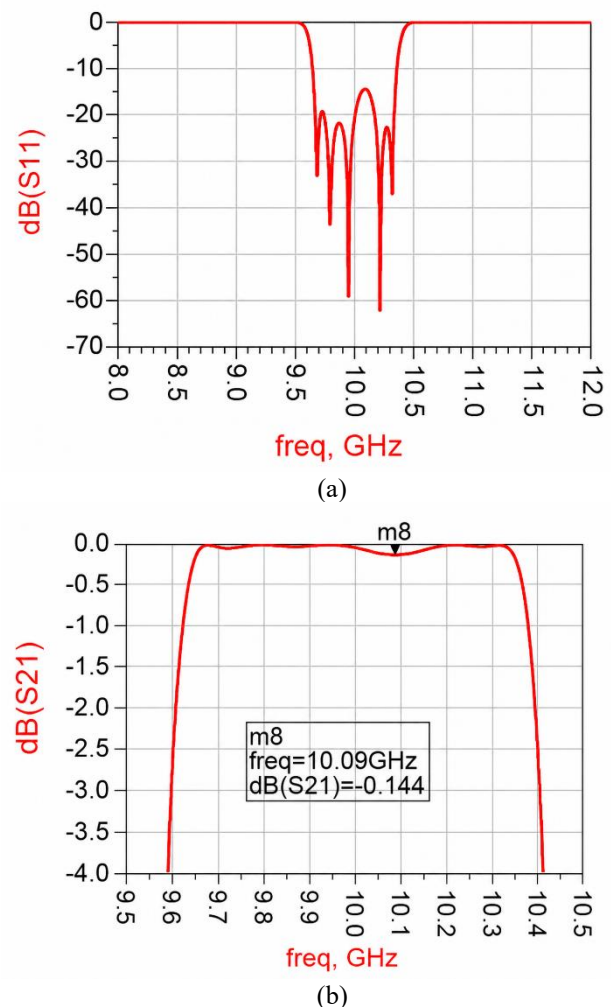
This section presents the simulation results of the designed fifth-order parallel coupled microstrip bandpass filter. The performance is evaluated based on S-parameters, impedance bandwidth, and the behavior of resonant poles. A critical discussion regarding the calibration of the dielectric constant and its effect on frequency alignment is also provided.

3.1 S-parameter analysis

The simulation results in the ADS environment demonstrate an exceptional agreement with the design specifications after

the final tuning process. Return Loss (S11): The filter achieved a significant return loss of approximately -21 dB at the center frequency of 10 GHz and -14.95 dB at the worst case [16, 17]. This high value indicates near-perfect impedance matching between the coupled stages and the 50 Ω ports, minimizing reflected power [18-20]. Insertion Loss (S21): Within the passband, the insertion loss was recorded at approximately -0.144 dB. This minimal loss confirms the efficiency of the microstrip configuration and the low-loss nature of the calibrated substrate [21]. Figure 2 illustrates these results.

The center frequency was precisely locked at 10 GHz. The achieved -3 dB bandwidth is 800 MHz, representing a fractional bandwidth of approximately 8% as seen in Figure 2. Although the initial target was 10%, the achieved bandwidth provides a higher selectivity, which is often desirable in X-band radar and modern digital communication applications to suppress adjacent channel interference [22, 23].

**Figure 2.** (a) Return loss (S11) and (b) Insertion loss (S21)

3.2 Impedance analysis

One of the most robust validations of this design is the impedance magnitude analysis across the 8-12 GHz range. As shown in the simulation plots: Identification of Poles: Five distinct resonant peaks are clearly visible within the passband. This confirms that all five resonators in the $n = 5$ design are active and correctly coupled. Resonance Stability: The markers (e.g., m3 at 10.08 GHz with 49.27 Ω) show that the impedance is extremely close to the characteristic impedance, ensuring maximum power transfer [24, 25]. The symmetrical

distribution of these poles around the center frequency indicates a well-balanced coupling coefficient between the stages Figure 3 shows the impedance analysis.

3.3 The Voltage Standing Wave Ratio and loss factor analysis

To further validate the impedance matching and power transmission efficiency, the voltage standing wave ratio (VSWR) and the Loss Factor LAr were calculated and analyzed. VSWR Analysis: The simulation results shown in Figure 4. Figure 4(a) illustrates that the VSWR value is consistently below 1.5 across the entire passband of 9.6 GHz to 10.4 GHz. At marker m5, 10 GHz, the VSWR is 1.44, which is an excellent result for high-frequency designs. A VSWR close to 1.0 indicates that the mismatch between the filter and the transmission line is negligible, ensuring that the maximum amount of signal power reaches the output port without being reflected [26]. Power Loss Factor LAr: The parameter LAr represents the attenuation or loss factor within the filter structure. As depicted in the results, the magnitude of LAr at the center of the passband (marker m6 at 9.65 GHz) is as low as 0.171. This minimal value confirms the high efficiency of the proposed parallel coupled configuration [9]. Furthermore, the results show the effectiveness of the dielectric constant calibration ($\epsilon_r = 3.39$) in minimizing energy dissipation within the substrate. The consistency between the high isolation, the low VSWR, and the minimal loss factor provides an analytical correlation confirmation of the filter’s robustness. These results collectively demonstrate that the parasitic effects, which typically degrade performance in the X-band, have been successfully suppressed through the applied proposed methodology.

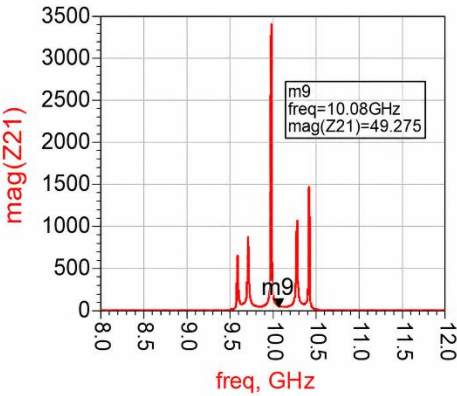


Figure 3. Impedance analysis

4. COMPARISON WITH PREVIOUS STUDIES

To assess the degree of achievement of the design goals, a comparison between the objectives and the outcomes obtained is shown in Table 4.

Table 4. Filter design performance summary

Parameter	Design Target	Simulated Result
(f ₀)	10 GHz	10 GHz
Return Loss (S ₁₁)	< -10 dB	-15 dB
Insertion Loss (S ₂₁)	≈ 0 dB	-0.2 dB
Number of Poles	5	5
Fractional Bandwidth	10%	8%

In order to evaluate the filter's performance, the test results of the designed model were compared with the results of previous research. It was found through the comparison that the designed filter has a wide bandwidth to operate appropriately for modern communication systems, has a good matching factor, and the filter gain is constant over the operating range with an appropriate attenuation factor. Table 5 shows the comparison results.

The competitiveness of the proposed filter for X-band frequency range is clearly presented in Table 5. A number of very recent reported designs have been selected to carry out the comparison, with the center frequency of the proposed filter fixed at 10 GHz. The proposed filter has the bandwidth of 800 MHz and low insertion loss, while the return loss is moderate. The proposed filter is a fifth-order parallel coupled microstrip structure, and this provides the best tradeoff between the required bandwidth, selectivity, and other parameters of the proposed design, i.e., the required selectivity, the insertion loss, and simplicity of the design. In the frame of the simulation-based designs of X-band microstrip bandpass filters, the proposed filter appears to be very competitive.

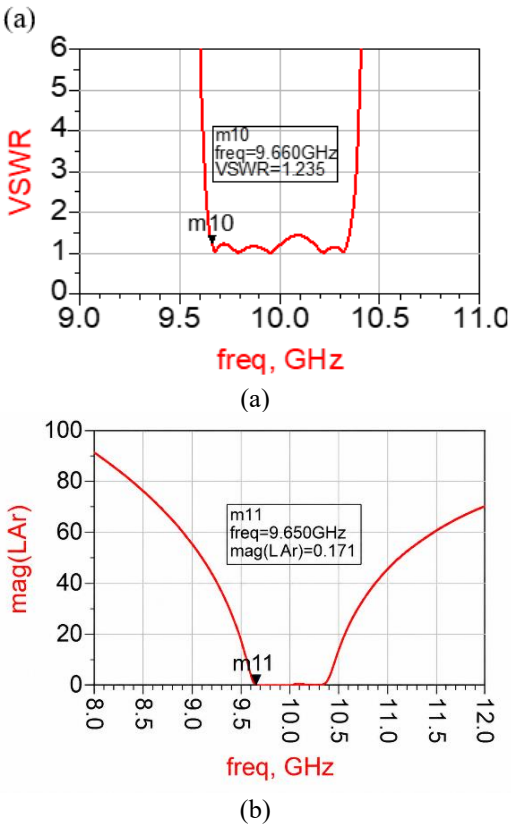


Figure 4. (a) Voltage standing wave ratio (VSWR) (b) Loss analysis

Table 5. Comparison with previous studies

Ref.	f ₀ GHz	B.W. MHz	S ₁₁ dB	S ₂₁ dB	Order	Design Complexity
[27]	9.5	0.5	-10	-1	7 th	High
[28]	10	0.5	-23	-0.8	5 th	Moderate
[29]	8.5	0.4	-15	-0.7	2 nd	Low
This Work	10	0.8	-15	-0.2	5 th	Moderate

This work has been limited to a numerical design. Hence, the behavior of the simulated ADS and the physically implemented ADS may differ due to implementation tolerances such as the dielectric constant of the substrate, surface roughness of the copper conductor, line width, slot width, line length, connection to the SMA port, soldering and the design of the measurement setup. This small difference may be shifted to the adjacent resonant frequency, increased Insertion Loss, and also may affect the impedance matching. The future work would be the implementation of the design and the measurement using vector network analyzer in order to verify the results.

5. CONCLUSIONS

In this paper a fifth-order X-band parallel coupled microstrip bandpass filter for 10 GHz operation frequency was designed and simulated by using the ADS software. In parallel coupled microstrip filters the center frequency of the filter is affected by the so-called parasitic fringing capacitance and by the additional effective electrical length of the coupled resonators. In the above design, these effects were compensated by using the Reverse Dielectric Calibrating approach (i.e., the effective dielectric constant of the substrate was decreased from 3.78 to 3.39 while keeping the above topology unchanged). The filter features an 800 MHz bandwidth corresponding to an 8% fractional bandwidth. In-band insertion loss and return loss are about -0.144 dB and -14.95 dB, respectively. The VSWR is below 1.5 within the passband. The plot of the impedance response of the filter features five resonant poles. The above design is simulation-based; therefore the implementation of the filter and the experimental verification of the simulated results is the subject of the future work.

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NOMENCLATURE

Z_{oe}	Even Mode Impedances, Ω
Z_{oo}	Odd Mode Impedances, Ω
L	Transmission Line Length, mil
S	Transmission Line Spacing, mil
W	Transmission Line Width, mil