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Design and Control of a Laser Cooling System for Wavelength Stabilization of a Monochromatic Light Source



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ABSTRACT

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Keywords:

temperature controller, laser diode stability, PID controller, fibre optical sensor, interferometric system, thermoelectric cooler Most laser diodes experience fluctuations in intensity, phase, and wavelength, predominantly due to heating from energy conversion to thermal energy and changing ambient temperatures. This study primarily focuses on countering the effects of temperature variations through the design and implementation of a laser diode temperature controller (LDTC) for the fiber optical interferometer system. Additionally, there are other contributing factors, including the injected current, noises, and acoustic disturbances, that play significant roles in these fluctuations. Despite these additional factors, our findings emphasize that temperature remains the principal contributor to these fluctuations. The LDTC developed in this work is cost-effective and rapidly responsive to temperature changes, ensuring precise control over the laser diode's temperature. The findings show the ability of this controller to maintain the temperature of a laser diode at a constant value with a precision (steady-state error) of 0.0013°C (0.0396°C without the laser diode) and an average fluctuation of 0.0567°C (0.0742°C without the laser diode). Furthermore, the study establishes a relationship between the length of the measurement object and temperature. This laser diode controller was investigated to measure the wavelengthdependent temperature changes at the interferometric point sensor.

1. INTRODUCTION

In recent years, fiber-optic interferometer systems have been widely investigated in scientific and industrial fields for the measurement of small displacements, refractive index changes, and surface irregularities. Incorporating laser diodes sources into fiber-optic interferometer systems is gaining particular significance. These sources include single-mode or narrow-band spectrum, high-coherence laser devices especially (Distributed Feedback (DFB) lasers), commonly employed in these systems due to their enhanced reliability, power, and wavelength coverage [1, 2].

In most cases, the wavelength of these laser diodes is influenced by the injected current and temperature changes. Energy conversion to heat or variations in ambient temperature may cause these temperature fluctuations. This heating leads to instability in the laser's intensity, phase, and wavelength, as shown in Figure 1. As a result, the diode becomes unsuitable for certain applications. The wavelength can vary by 0.1–0.5 nm per 1°C, necessitating temperature control for stability [3, 4].

Effective temperature control is crucial for maintaining a laser diode's constant wavelength. Common methods including those developed by Lehmann et al. [5] and Schulz et al. [6] for temperature control have employed passive or active heat sinks to address this challenge. Lehmann et al. [5]'s approach offered an affordable temperature controller which achieving an accuracy of 0.5°C. This level of precision does

not meet the requirements for the interferometer. Conversely, Schulz et al. [6] focused on a high-precision controller with an accuracy of 0.001 at 30°C, but this came with increased costs due to the use of specially designed thermoelectric coolers. The excessive cost and limited range are the main obstacles preventing the use of these approaches in the system.



Figure 1. The effects of temperature on center wavelength [4]

This paper proposes a novel temperature control system designed to overcome the limitations of these previous methods. The primary objectives of this design are to prioritize simplicity, cost-effectiveness, fast response, and ability to effectively tackle temperature-related concerns linked to the fiber-optic interferometer. This work aims to develop a userfriendly laser diode temperature controller with minimal effort. Our works are different from the approaches of Lehmann et al. [5] and Schulz et al. [6] by incorporating cost-effective components for regulating the temperature of the laser diode. The key challenge is to maintain the laser diode's temperature with a standard deviation below 0.1°C. This precise temperature control is crucial for consistent wavelength stabilization and enhances the system's precision and stability. The key contributions of this paper are outlined as follows:

- 1. We design and implement an efficient, high-precision laser diode temperature controller. This controller is specifically tailored for fiber-optic interferometer to maintain the laser diode's temperature with a standard deviation below 0.1°C.
- 2. The study establishes a direct relationship between the laser diode's wavelength and the vertical displacement in a micro-optical probe within the interferometer. This provides a more comprehensive understanding of the system's performance.
- 3. The design integrates a microcontroller for real-time monitoring and adjustment of the sensor temperature. This feature enables maintenance of the laser diode's operating temperature within a narrow and precise range.
- 4. A detailed analysis of each component of our temperature controller is conducted and discussed their functions, characteristics, and technical specifications. This analysis illustrates how these components contribute to overcoming the limitations of previous designs.

By addressing the specific temperature control issues associated with fiber-optic interferometer systems, our study aims to present a solution. This solution is not only technically advanced but also cost-effective and simpler compared to existing methods.

The remainder of the paper is organized as follows: Section 2 explains fibre optical interferometer system. Section 3 describes the technical temperature controller requirements and constraints. Section 4 describes the architecture of the laser diode temperature controller. Section 5 explains the hardware and software setup and evaluates them. Also, the advantages of our temperature controller are quantified using experimental results in this section. Finally, Section 6 concludes the work.

2. FIBRE OPTICAL INTERFEROMETER SYSTEM

The interferometer has been widely investigated in scientific and industrial fields for the measurement of small displacements, refractive index changes, and surface irregularities. A Laser interferometry system employs a highly stabilized light source and precise optics to measure distances with great accuracy. The system accuracy depends on environmental stability (such as temperature, air pressure, noises and humidity). Regular calibration using known standards is necessary to maintain precision over time. Repeatability measurements are needed to understand the behavior of the oscillating sensor. These involved recording multiple signals as the optical probe oscillated at a fixed distance from the object being measured [6].

These factors are essential for the system's high precision. Employing interferometric principles, these devices enable micro- or nanometer-scale surface analysis. They operate by splitting the source light into two paths using a beam splitter, which involves a reference and an object surface. These systems can function through either linear or sinusoidal phase shifting methods [1, 2]. The fundamental principle of a laser interferometry system is its ability to detect and measure the changes in phase of the laser light waves. These waves are analyzed for phase differences between the emitted and returning waves after the wave reflected off an object and returned. This difference correlates directly with the distance traveled by the light, enabling precise distance measurements.

In general, the key components of a fibre optical interferometer system work simultaneously for precise object measurement. The laser diode emits a coherent and stable light beam and the beam divides by a splitter into two beams. one is guided using Mirrors/Reflectors towards the measurement object and another to a reference path. these beams. Finally, the detector captures the returning beams and measures the interference pattern formed by the overlapping of the reference and measurement beams. Each component plays a critical role in maintaining the system's accuracy.

The fibre optical interferometer system is constructed from fibre-, micro-optical and electronic components and the important part in the system is called fibre-coupled interferometric sensor [1, 2]. The fibre-coupled interferometric sensor or the whole part for holding the sensor is called micro-optical probe holder, which is, constructed as follows [5, 6].



Figure 2. Schematic of (a) micro-optical probe [1] and (b) the fibre optical interferometer [7]

• The micro-optical probe is mounted on a bending beam which is driven by a piezoelectrical actuator, and the bending beam is mounted on an xyz-stage but in this work x and y axes are fixed and the probe can only be moved in zdirection as shown in Figure 2(a).

• The micro-optical probe head is connected to a single mode fiber which splits through Y-coupler to connect the laser diode (DFB laser 1310nm) on one side and a photodiode (PD) on the other side as shown Figure 2(b).

• The piezoelectrical actuator (crystal) is mounted in a frame which is a part of a bending beam. The actuator appears partly above the middle bending beam to bend micro-optical probe when it is expanded by applying a voltage. This actuator is worked to modulate or change the distance between the head probe surface and measuring object. The distance modulation occurs through periodically expanding and contracting of the actuator when a voltage signal such as a sinusoidal signal is applied. This modulation is a mechanical modulation which is called optical path length modulation (OPL) as shown in Figure 2(a).

The optical path difference (OPD) is the difference in the traveling paths of the two beams (the reference beam and the reflected beam from the measured object). Because the head probe oscillates, this OPD will change periodically between constructive and destructive interference, and therefore it modulates the interference signal. The OPL modulation of these partially sinusoidal interference is generated. A height change happening additionally to the OPL modulation causes a phase shift in this basic interference signal that can be precisely evaluated by proper data analysis [1, 2, 7, 8]. The OPL modulation is generated by applying a sinusoidal voltage to the piezoelectric crystal, as mentioned before, which modulates the distance between the object and the reference plane. The mathematical equation of the OPL modulation is expressed by the phase-modulated voltage signal:

$$V(t) = V_o \cos\left(\frac{4\pi}{\lambda} \{ \tilde{z} \sin(2\pi f_o t) + z(t) \} \right) \tag{1}$$

where, V_o is the amplitude of the photodiode voltage. \tilde{z} is the maximum changed height of actuator, z(t) is the height of the surface changed with time.

3. DESIGN REQUIREMENTS AND CONSTRAINTS

The design of a laser diode temperature controller (LDTC) encompasses two phases: hardware and software. In the hardware phase, components such as a heat sink, temperature sensor, thermoelectric cooler (TEC), housing box, PWM driver, bridge circuit, amplifier, and microcontroller are selected and integrated following detailed calculations. The software phase involves configuring the PID controller and other operational programs. Subsequent subsections will detail the design specifications of the LDTC, focusing on stabilizing the laser diode's temperature to maintain wavelength consistency. Key user-centric features of an effective LDTC include cost-effectiveness, simplicity, low power consumption, high accuracy, reliability, sensitivity, strong wavelength stability, and a temperature controller resolution of $\leq 0.1^{\circ}C$.

3.1 Heat sink

The characteristics of the heat sink, essential for dissipating heat from the laser diode to the ambient air, are integral to the combined function of the heat sink and Thermoelectric Cooler (TEC) in temperature regulation. The selection of an appropriate heat sink is interdependent with the specification of the TEC. This determination utilizes Fourier's Law, which governs heat conduction. Specifically, in the context of one-dimensional steady-state conduction through a homogenous body, Fourier's Law simplifies to this principle:

$$q = \frac{KA\Delta T}{H} \tag{2}$$

where, q is the heat flow (total heat-transfer rate) Watt, K is the thermal conductivity Watt/(m °C), A is the area normal to the direction of heat flow m^2 , ΔT is the temperature difference (across height H) °C, H is the height in direction of heat flow.



Figure 3. Structure of (a) Heat sink which tradeoffs between number of fine fins and spacing between fins, and (b)TEC element constructs of p and n elements is connected in series which transfers the temperature from code side (object) to hot side (heatsink) [9]

To enhance heat sink design and selection while minimizing material and manufacturing costs, several key factors must be considered [10-13]:

- 1. A balance between fin density and size is crucial for effective heat dissipation, as depicted in Figure 3(a). Heat sinks with specific fin shapes/profiles are more efficient in heat dissipation.
- 2. Optimal thermal resistance reduction and conductivity enhancement require perfect contact between the heat sink and the heat source, achieved through various mounting methods and ensuring smooth surfaces.
- 3. The choice of heat sink material, such as gold, silver, copper, or aluminum, is influenced by their electrical and thermal conductivities and associated costs.
- 4. Heat sinks with profiled fins are notably more effective in heat dissipation.

5. Effective air circulation through fine fins can be facilitated by a powerful fan, as shown in Figure 3(a).

6. For low-wattage applications, a large aluminum block suffices, but higher wattage demands adherence to the aforementioned requirements.

The heat sink employed in this work is passive and profile in nature, with characteristics such as tall and broad fins, large spaces between fins, and a thick base. Since the use of a fan would introduce unwanted noise into the fibre-optic interferometer setup, we have opted against employing such a method.

3.2 Aluminum mounting box

The aluminum box, a critical component in our design, serves to contact and mount a laser diode to a TEC Peltier (cold side). In the design of the Aluminum Mounting Box, particular attention was given to its dimensions and functionality to ensure optimal performance. The box is precisely crafted with dimensions of 30mm in length, 30mm in breadth, and 10mm in height. To accommodate the essential components, the box is drilled with two specific holes: one with a smooth inner surface, with a diameter of 6.35mm to house the laser diode and closer to the Peltier cooler (cold side), and another with a diameter of 1.8mm for the temperature sensor, as depicted in Figure 4. The temperature sensor hole also extends to a depth of 20mm. These dimensions and adaptations were meticulously calculated to ensure a snug and effective fit for both the laser diode and the temperature sensor, thereby optimizing the thermal management and overall performance of the system.

Plastic screws secure the box to the TEC, enabling adjustable compressive stress while minimizing heat transfer from the heat sink. This approach stabilizes temperature, ensuring laser diode reliability and reducing mounting and maintenance costs compared to thermal glue.



Figure 4. The proposed architecture of a laser temperature controller system

3.3 Thermoelectric cooler

The thermoelectric cooler (TEC) is a solid-state heat pump that uses the Peltier effect to transfer heat from one side (the cold side) to the other (the hot side) through dissimilar semiconductors when direct current (DC) power is applied. Figure 3(b) shows the structure of a thermoelectric cooler. The requirements for choosing TEC are explained as follows [14-18]:

- The TEC cooling capacity (Qc) depends on the number and shape of P- and N-type pellets. Larger, shorter pellets increase Qc but also raise power usage. Thus, TEC choice relies on power supply (current and voltage) and the object's thermal properties.
- The cooling capacity (Qc) of a TEC, like the QC 35-1.4-6.0 M chosen for cooling a laser diode, depends on current, voltage, and the temperature difference (ΔT). Optimal efficiency, assessed by the Coefficient of Performance

(COP) which is the ratio of the cooling capacity to the consumed electric power. The COP decreases as the power input increases. In this work, Optimal efficiency is achieved when TEC is operating below 40% of its maximum voltage and current.

- Secure mounting of the TEC with the heat sink and object using high-quality thermal material is recommended to minimize thermal resistance, with a suggested gap under 0.001 inches.
- Driving the TEC is performed by pulse width modulation (PWM), which is obtained through an integrated H-bridge PWM switch-mode driver (e.g., MAX1968) or by designing a TEC driver circuit with a microcontroller like STM32F4xx, which later generates low current PWM.
- Prior to TEC selection, determining the cold surface temperature (T_c) , hot surface temperature (T_h) , and required heat absorption capacity (Q_c) is essential.

3.4 Temperature sensor

In temperature control systems, temperature sensors are crucial for measuring and safeguarding against temperatureinduced distortions. Common types include thermocouples, resistive temperature devices (RTDs), thermistors, and integrated silicon sensors. Selection criteria encompass accuracy, temperature range, response speed, thermal coupling, environmental factors, and cost. In defining sensor requirements for a standard laser cooling system, the selection of an appropriate sensor necessitates the following considerations [19-21]:

- Very high robustness and ability of a sensor to repeat a measurement when going back to the same environment.
- The operating wide-temperature range depends on the laser specification (e.g., -40 to 70°C).
- Extremely high sensitivity is needed to detect small changes in the laser diode temperature.
- A high speed of response is required to maintain the temperature constant while controlling it.
- Small size and linearity characteristics.
- Some sensors request external power, which causes inaccuracy due to self-heating and can be avoided by limiting the level of electrical current.

Given the specified criteria, the RTD Pt1000, a platinumchip temperature sensor, is employed due to its advantages including long-term stability in air, reduced noise sensitivity with current excitation, and the ability to generate voltage output with minimal current (<1 mA). This sensor is characterized by its robustness, compactness, linearity, and high sensitivity ($\pm 0.01^{\circ}$ C).

3.5 Signal conditioning circuits

This section describes the enhancement of transducer outputs into suitable signals for digital conversion, further processing, control, or display. It specifically employs a bridge circuit, notably the Wheatstone bridge, to measure temperature-induced resistance changes in an RTD sensor. Bridges are effective for detecting slight resistance variations. The Wheatstone bridge, sensitive to minor electrical resistance shifts, particularly in temperature, is commonly used. Figure 4 shows a standard Wheatstone bridge configuration with four resistors, powered by voltage or current. The bridge includes ratio arms (resistances R1 and R2), a standard arm (known resistance R3), and an unknown resistance R4. A DC power source connects between points C and D, with points A and B used for balancing [22, 23]. In a balanced Wheatstone bridge, where the ratio of resistances R1 to R4 equals that of R2 to R3, the output voltage measures 0V, as depicted in Figure 4. Criteria for selecting and designing the Wheatstone Bridge include:

- Choosing the same value of resistances R1 and R2 and R3 with a tolerance ratio of 0.1%.
- Connecting the variable resistance Rx with the temperature sensor RTD (e.g., RTD R=1000 Ω at 0°C).
- Choosing the values of resistances depends on the resistance value and allowable current value of the RTD and the voltage of the power supply.
- Changing the Rx value to get a balanced or minimum change in voltage that is nearly zero between A and B.
- The stability of the excitation voltage or current directly affects the overall accuracy of the bridge output.

• The output voltage polarity (A and B) in the middle of the Wheatstone bridge depends on the position of the RTD in the bridge and the temperature range operating (T > 0) or (T < 0).

This study focuses on the use of differential amplifiers in signal conditioning circuits, primarily for amplification purposes. Amplification is critical for two main reasons: firstly, it enhances signal measurement resolution, which is contingent on the A/D converter's bit count and the ADC's maximum analog input voltage range. Secondly, it improves the signal-to-noise ratio (SNR), a function of the amplifiers' noise rejection capabilities and resistor precision. Additionally, as recorded voltage levels decrease, system vulnerability to noise escalates, necessitating increased gain in the conditioning circuits.

The differential amplifier, essentially an operational amplifier subtractor, facilitates the subtraction of two voltages while rejecting common mode signals. Ideally, its common mode gain (Gcm) is zero, but practically, it is minimal, influenced by the operational amplifier and the matching of external resistors. The effectiveness of an operational amplifier in discarding unwanted signals is best assessed by the differential to common mode gain ratio. The basic configuration of a differential amplifier, as depicted in Figure 4, though rudimentary, is quite effective. However, its limitations include low and unequal impedances at the inverting and non-inverting inputs, adversely affecting the circuit's common-mode rejection ratio (CMRR). Enhancements, such as incorporating high input impedance buffers, are made to this simple design to provide matched high-impedance inputs, thereby minimizing the impact of input source impedances on the circuit's CMRR.

3.6 Thermoelectric cooler driver

A thermoelectric cooler (TEC), specifically the TEC QC 35-1.4-6.0 M, is a solid-state, current-controlled device that produces a heat flux dependent on the current, voltage, and operational duration. We chose the TEC QC 35-1.4-6.0 M for its efficiency and suitability for our system, despite its polarity reversal susceptibility. This model is particularly susceptible to polarity reversal, leading to rapid heating and potential damage within one second, necessitating careful polarity management. The driving criteria for the TEC are outlined in this study [17, 21, 24]:

- To operate a Thermoelectric Cooler (TEC), one can either directly employ a low-voltage source (around 2 volts) or use approximately 40% of the maximum voltage with a high-wattage resistor, considering the TEC's internal resistance of 0.6Ω.
- To counter polarity reversal susceptibility, we incorporated a high-watt diode, effectively preventing any potential damage from polarity reversal.
- The TEC requires a substantial current (approximately 3A), regulated through either an on-off pulse train or pulse width modulation (PWM), implemented using a fast-switching transistor like the Power MOSFET Transistor IRLR3717, coupled with a high-speed power MOSFET driver (e.g., MCP14E11) to enhance the low-powered PWM signals from a microcontroller.
- For precision applications, such as in laser diodes, the PWM signal's duty cycle must continuously vary between 10% and 90%, controlled by algorithms like PID, to accurately adjust the TEC's temperature. Additionally, a

high PWM signal frequency (around 1 kHz) is necessary to minimize temperature fluctuations during the TEC's on-off cycles.

3.7 Microcontroller and programming software

A microcontroller, tasked with controlling external devices, operates on digital data converted from electrical signals via an ADC. Selection criteria for microcontrollers include:

- A high-frequency floating-point unit and efficient singlecycle instructions.
- Substantial flash and RAM memory.
- High-resolution ADC and PWM timers.
- Various communication interfaces (USART, USB).
- Energy efficiency and cost-effectiveness.

This study utilizes the STM32F407 microcontroller, equipped with three 12-bit ADC converters and a conversion rate of 1 MHz. The ADC, powered independently between 3.3 and 5.0 volts, allows internal or external reference connections and programmable conversion times across eight discrete ranges (1.5 to 239.5 cycles) [25]. It features 18 multiplexed channels, including 16 for external signals, one linked to an internal temperature sensor, and another to a reference voltage. We chose the STM32F407 microcontroller for the LDTC for its fast processing and ample memory, essential for real-time monitoring and complex algorithms. Its ADC operates in DMA mode, enabling efficient data handling without MCU intervention. Coupled with extensive I/O capabilities, these features ensure precise temperature control and system reliability, meeting the LDTC's specific needs for laser diode stabilization. Various C/C++ integrated development environments (IDEs) are available for efficient software development; however, this research exclusively employs the German SiSv environment, tailored for ARM microcontroller programming in embedded system design, offering extensive support and resources.

3.8 Control system

Control systems are essential due to the complexity or absence of plant models in the design process. PID (Proportional-Integral-Derivative) controllers, effective for nearly linear plants, are prevalent in industry, solving 95% of control problems, with many systems primarily using PI control. These controllers, often integrated with logic and function blocks, manage complex automation tasks in heating, cooling, fluid level, flow, and pressure control. Modern PID controllers, typically microprocessor-based, feature automatic tuning, gain scheduling, and continuous adaptation. The Operational guidelines for PID controllers can be summarized as follows [26, 27]:

- 1. Set Point Specification: Establishing a target value based on inputs such as temperature, pressure, or flow rate.
- 2. **Controller Output Calculation:** Generating an output (e.g., heater power, valve position) to align the system's output with the set point.
- 3. **PID Controller Configurations:** Utilizing various methods like gain-scheduled PID, PID auto-tuning, error-squared PID, and others for controller composition.
- 4. **Proportional Gain Adjustment:** Balancing steady-state error and oscillation tendencies by modifying proportional gain.

- 5. **Integral Action Enhancement:** Employing integral action to eliminate steady-state error and control oscillation.
- 6. **Derivative Action Utilization:** Using derivative action to dampen oscillations caused by proportional and integral gains.
- 7. Anti-Windup Methods: Implementing anti-windup techniques to reduce nonlinear behavior in integral action.
- 8. **PID Tuning for Setpoint Tracking:** Applying diverse tuning methods, including Ziegler-Nichols and Kitamori techniques, for effective setpoint tracking.
- 9. **Output Signal Calculation:** Deriving the PID controller's output from the sum of weighted proportional, integral, and derivative terms of the error signal, which is the difference between the plant output and the set point, fine-tuned iteratively.



Figure 5. Block diagram of proposed Ziegler-Nichols tuning PID integrated by anti-windup

Figure 5 depicts the PID's overall structure. The three constants Kp, Ki, and Kd, also known as the proportional, integral, and derivative gains, are the PID design parameters. The PID controller permits inputs from the present, the past, and the future. The optimum equation for a digital PID controller is as follows:

$$u(t) = K_p(t)e(t) + K_i(t)T_s(t)\sum_{\substack{k=0\\k=0}}^{t} e_k + K_d \frac{e(t) - e(t-1)}{T_s}$$
(3)

where, e(t) is the error of the system response in the t instant; $e = y_{sp}$ -y, y is the measured process variable, T_s is the signal sampling period, and u is the control signal (PWM). The reference variable y_{sp} is often called the set point.

4. PROPOSED ARCHITECTURES

This study focuses on developing a laser diode temperature controller for a fiber-optic interferometer, aiming to create an affordable and reliable temperature regulation system for a laser diode. The laser diode temperature controller is composed of four principal elements: an optical unit (including a DFB laser diode and a fiber-optic interferometer), a thermoelectric cooler (TEC) based transduction and mechanical unit with a heat sink, RTD sensor, and laser diode housing for temperature control, a signal conditioning and TEC driver unit, and a control and processing unit. This last unit employs an STM32F407 microcontroller with a Cortex-M4 core for control signal generation, analog-to-digital conversion, and PID algorithm-based temperature data processing. Figure 4 in our documentation details the system's hardware layout, specifically showing how the heat sink connects to the TEC and aluminum box, and illustrating the integration of sensor and circuit with the microcontroller.

In this study, a 5V power supply is utilized for the entire system, with the TEC's selection influenced by the laser diode's type (InGaAsP DFB laser, PL13N0021FAA-0-0-01) and power constraints. This diode, emitting at a central wavelength of 1310 nm, has a maximum output of 2 mW and operates at a maximum current of 150 mA for the laser and 0.8 mA for the photodiode. Given its low dissipated power (approximately 26 mW), the TEC QC 35-1.4-6.0 M is chosen for efficient cooling, operating below 40% of its maximum voltage and current. The TEC's current and voltage are constrained by a 1 Ω resistance and 25 watts power.

We selected the TEC QC 35-1.4-6.0 M for its optimal balance of cooling efficiency, power use, and size, excelling in rapid temperature adjustment and stability, crucial for our precise temperature control needs. Its design, featuring low-height and/or larger cross-section pellets, offers a higher cooling capacity compared to other modules, aligning with the specific cooling capacity requirements of our system.

For temperature sensing, the RTD Pt1000 is selected for its high accuracy, linearity, and simple integration into circuits like the Wheatstone bridge, despite its slower response compared to thermistors. This choice considers factors such as accuracy, response time, temperature range, environmental suitability, ease of use, and cost. The sensor's resistivity demonstrates a linear increase with temperature, as described by the following formula:

$$R_T = R_o(1 + \alpha T) \tag{4}$$

where, *R* is the resistance of the sensor at *T*, R_o is the absolute resistance 1000 Ω of the sensor at T = 0°C, and α is the RTD sensor temperature coefficient 3.85×10^{-30} °C⁻¹ between 0 and 100°C. The RTD sensor temperature coefficient is small, the operating current is recommended to be 0.1mA and the maximum current is 1mA. If the sensor is operated with the maximum current, this causes the sensor to self-heating Δt which is expressed as

$$\Delta t = I^2 R E \tag{5}$$

In air, the self-heating coefficient (E) is 0.2 °C/mW. At a current of 0.1 mA, self-heating is minimal, but it becomes significant at 1 mA. The chosen operating current for the sensor in this study is 0.5 mA, resulting in a self-heating temperature of approximately 10 μ °C.

Upon selecting the sensor, laser diode, and thermoelectric cooler (TEC), the TEC's geometrical compatibility is dictated by its need for direct physical contact with the laser diode. Optimal contact is achieved through a custom-designed aluminum housing (Al-box) for the laser diode, tailored to minimize thermal resistance between the TEC and the diode. This Al-box, matching the TEC's length (~30 mm), incorporates a 6.35mm diameter hole for the laser diode and a 1.8mm, 20mm long hole for the temperature sensor. The design ensures the RTD sensor fits snugly in the smaller hole, while the larger hole fully encompasses the laser diode.

Attachment of the box to the TEC is facilitated by plastic screws, which regulate compressive stress on the TEC and prevent heat transfer from the heat sink to the box via the screws, thereby maintaining diode temperature stability. This screw-based method offers a cost-effective alternative to thermal glue for mounting and part replacement, as illustrated in Figure 4.

The power of the laser diode PL13N0021FAA-0-0-01 is calculated by multiplying the DC applied voltage and current, depending on the datasheet, as shown below. A laser diode's dissipated power is $P = I \times V - P_{out} = 150 \times 10^{-3} \times 1.5 - 2 \times 10^{-3} = 0.223$ watt. The laser diode is cooled using a TEC QC 35-1.4-6.0 M, whose resistance is determined to be 0.6 Ω when coupled serially with a measured resistance of 1.2 Ω and with the MOSFET switch transistor. Using a 5-volt power supply and an expected MOSFET voltage of 0.25 volts, we can calculate its current as follows:

$$I = \frac{V_{Supply} - V_{MOSFET}}{sum of resistances} = \frac{5 - 0.25}{0.6 + 1.2} = 2.6389 A$$
$$V_{TEC} = V_{Supply} - V_{MOSFET} - V_{Resistance}$$
$$= 5 - 0.25 - I \times 1.2 = 1.58332 \text{ volt}$$
$$V * I = 2.638 \times 1.58332$$

 $P_{TEC} = \frac{r_{TT}}{2} = \frac{2.000 \times 1.00332}{2} = 2.177 \text{ watt}$ The total power $P_{tot} = P_{TEC} + P_{laser} = 2.323$ watt The mperature of hot side T_h is calculated as $T_h = T_{room} + R_{th} \times 10^{-10}$

temperature of hot side T_h is calculated as $T_h = T_{room} + R_{th} \times$ P_{tot} , where T_{room} is the temperature of room 25°C, R_{th} is the thermal resistance (°C/watt). Assumed temperature of cold side is 10°C, assumed R_{th} laser =0.1°C/watt, and R_{th} box =0.1°C/watt. The thermal resistance of TEC is found by using curves in the datasheet with the intersection of the operating current and voltage, or power. Thermal impedance represents the resistance to the transfer of thermal energy; therefore, lower numerical thermal impedance ratings mean more efficient heat transfer. The temperature change is calculated $\Delta T = T_{hot} - T_{cold} = 46^{\circ}$ C, from the curve $T_{hot} = 56^{\circ}$ C and R_{thTEC} $= \Delta T / Q_{TEC} = 46/2.177 = 21.13$ °C/watt. The thermal resistance $R_{Box-TEC}$ presents between the box and TEC can be calculated as $R_{Box-TEC} = 0.1 + 0.1 + 21.13 = 21.313$ °C/watt. The hot side temperature without heat sink is T_h = 21+21.313 × 2.323 = 70.51°C. The area of the heat sink is calculated by using the equation below as:

$$Q_{eq} = kA\Delta T \tag{6}$$

where, k is the thermal conductivity for Aluminum, 5W/m²°C, Q_{eq} is the total power of the laser and the TEC. Then $\Delta T = T_{hot(TEC)} - T_{room} = 70.51 - 21 = 49.51$ °C and the cross section of heatsink $A = Q_{eq}/k\Delta T = 2.323/5 \times 49.51 = 0.009384$ m² =93.84 cm². The selection of an appropriate heat sink surface area is crucial for maintaining the desired operating temperature of the laser diode. For this purpose, a heat sink with a surface area exceeding 93.84 cm² is recommended. In this study, a heat sink with a surface area of 160 cm², measuring 16 cm in length and 10 cm in width, was utilized.

The subsequent section details a signal conditioning unit comprising a Wheatstone bridge and differential amplifier (forward part) and a PWM driver (feedback part) for TEC operation. Circuit limitations include a 0.5 mA current from the RTD sensor. Resistances R1, R2, and R3 are set at $5k\Omega$ (±0.1%), with a $4k\Omega$ variable resistance Rx linked to the RTD sensor (RRTD=1K Ω at 0°C). This variable resistance defines the maximum desired temperature point (Max-DTP), set at 26°C, influencing the amplifier's gain and the ADC's maximum voltage level. The ADC on the STM microcontroller has a maximum voltage of 3.3 volts, with calculations proceeding in reverse order.

The temperature range (0-26 °C) enables determination of variable resistance, enhancing sensor accuracy due to reduced Max-DTP. However, the microcontroller STM's ADC range (0 to 3.3 volts) limits temperature controller performance. To mitigate the issue of low differential voltage V_{ba} , a buffered differential amplifier with high impedance and effective CMRR is used, powered by a +5V DC source. The LM324 operational amplifier is chosen for its functional benefits and cost-efficiency, ensuring the amplified output is digitally compatible. This design differs from typical differential circuits by using buffers for isolation from the Wheatstone bridge, achieving high impedance and less non-linearity, thus reducing signal distortion. The amplifier, comprising three operational amplifiers, offers a gain of 100 (see Figure 4). Calculations were performed to derive a formula for converting RTD sensor voltage changes into temperature readings for microcontroller use, as follows:

$$T = \frac{2R}{\alpha R_o} \frac{1}{\left(\frac{V_{cc}}{2V_{ba}} - 1\right)} \tag{7}$$

where, *R* is a resistance $=R1 = R2 = R3 = R4 + R_{sensor}$, $R_{sensor} = R_o + \Delta R$, ΔR is the sensor resistance change which is $\Delta R = \alpha \Delta T R_o$, R_o is the absolute resistance 1000 Ω of the sensor at T = 0°C, and α is the RTD sensor temperature coefficient 3.85×10^{-3} °C⁻¹ between 0 and 100°C, and *Vcc* is a voltage supply. The difference voltage (*V*_{ba}) is a voltage between two branches of the Wheatstone bridge as shown in Figure 4. The ADC converts the amplified difference voltage *V*_{ba} to the integer value of ADC voltage level *V*_{ADC} which depends on the resolution of ADC (2^{No. of bits} = 2¹²).

Thermoelectric coolers (TECs) are controlled via a PWM signal with a 10%–90% adjustable duty cycle, functioning as an on-off switch for cooling and heating operations. A microcontroller generates a low-current PWM, insufficient for the high-current needs of the TEC. Consequently, a MOSFET driver, specifically the MCP14E11, is employed to amplify the current. This driver, capable of delivering a 3 A peak current, operates on a 5V supply and requires minimal input current (1 mA active, $300 \ \mu A$ inactive), aligning with the microcontroller's output capacity. The circuit utilizes an IRLR3717 MOSFET transistor to drive the TEC QC 35-1.4-6.0M with a high-current PWM of approximately 3 A through a $1\Omega/25$ watt resistance. The MCP14E11 driver thus converts the microcontroller's low-power PWM to a high-power signal, as depicted in the circuit diagram (Figure 4), powered externally at 5 volts and 4.5 A.

In this study, the SiSy software is employed for developing C language applications to configure the STM32F407 microcontroller, operating at 168 MHz with 32-bit floating-point capability and a 3.3-volt power supply. Current consumption varies from 50 mA (without peripherals) to 100 mA (with peripherals), depending on clock speed and peripheral usage. GPIO port C is used for configuring pins, with pin 5 designated for ADC1's analog input and pin 6 for PWM signal output in alternate function mode. The ADC, providing temperature data for computer analysis, interfaces

via Port B as a serial port, with USART1's transmitter and receiver on pins 6 and 7, supporting up to 84 MHz I/O speed. ADC1 features a 0-3.3volt range, 1 µsec conversion time, and 1 kHz sampling frequency. PWM output is set at a low 1 kHz frequency to avoid resistance heating. Timers TIM1, TIM2, TIM3, TIM4, TIM5, or TIM8 can trigger ADCs and generate PWM outputs, with the timers also managing interrupts for ADC and PWM in this configuration. The overall functioning of digital and software system components starts when an analog signal enters the ADC and continues until PWM is generated. The following steps describe all the microcontroller's internal operations. The operational procedures are as follows:

- Step 1. Initialization and configuration of system components: ADC, timer, PWM, and GPIO.
- Step 2. Amplified temperature signal acquisition from the sensor via pin PC5, followed by sampling through ADC1, controlled by timers TIM2 (1 kHz) and TIM4 (200 kHz).
- Step 3. Transfer of the sampled signal to DMA2.
- Step 4. Iterative process: if the sample count is ≤ 10 , repeat Step 2; otherwise, proceed.
- Step 5. The process involves calculating the average from 10 samples, converting this average from voltage to temperature using Eq. (8), and transmitting the data via pins PB6 and PB7 to USART1 for processing in MATLAB.
- Step 6. Input of the temperature value into a PID algorithm.
- Step 7. The PID algorithm calculates the duty cycle for PWM/TIM3 generation, and then the microcontroller provides 1 kHz PWM through pin PC6 to the TEC.

This study presents a nonlinear laser temperature control system enhanced by a PID controller. The PID controller, devoid of traditional PID elements, effectively manages nonlinear systems. It integrates three components: proportional, integral, and derivative, based on the error signal, which is the difference between actual and desired temperatures. Optimal temperature regulation is achieved by automatically adjusting PID gains using the Ziegler and Nichols method, with integral term gains modified via a gain table to suit varying temperature setpoints, enhancing stability and performance. This PID system, programmed in C on an STM32F407 ARM microcontroller using SiSy software, processes inputs like actual and target temperatures and outputs a duty cycle for PWM signal generation. The PID operates synchronously with the sampling period, facilitated by timer 2, and its tuning algorithm is detailed in Figure 5. Upon integrating the optical and physical components, the RTD sensor and TEC are linked to the signal conditioning and TEC driver unit. The TEC's anode and cathode connect to the positive power supply and one side of a $1\Omega/25$ watts resistor, respectively. The signal conditioning unit comprises solely a differential amplifier, whose output feeds into the analogueto-digital converter of the STM32F4 microcontroller.

The TEC driver includes the MOSFET IRLU3717 and the MCP14E11 transistor driver, with the MOSFET's drain pin attached to the resistor's other side and its gate pin to the transistor driver's output. The transistor driver receives input from the microcontroller's PWM pin. This microcontroller reads the temperature-corresponding voltage from the differential amplifier and generates a low-power PWM signal

to operate the transistor driver circuit. The complete setup is detailed in Figure 6.



Figure 6. Test the connection of the fibre optical interferometer system and laser diode temperature controller inside the laboratory

5. EXPERIMENTAL EVALUATIONS AND RESULTS

The system's performance, including individual components and overall functionality, was assessed through a series of experiments and tests. These evaluations were crucial for verifying assumptions, optimizing operational parameters, and ensuring compliance with specifications. The testing process involved detailed experimental analysis and individual assessment of each component within the cooling system. The evaluation of the optical and physical units involved several tests, summarized as follows:

- Heat Sink Efficiency: In our design, we use a 16×10 cm² heat sink, effectively dissipating heat from the hot side to the ambient air at 24°C. A reduction in heat sink size to 10×5 cm² correspondingly decreases heat dissipation, confirming our assumptions and analysis regarding heat sink sizing as detailed in section four.
- 2. Aluminum Housing Box: The aluminum housing box, chosen for its low thermal resistance, effectively maintains contact with the laser diode and sensor, insulating them from ambient temperature fluctuations. This alignment keeps the diode temperature consistent with the TEC's cold side. Complete coverage of the diode and sensor within the box's holes is crucial, as partial exposure results in increased temperature fluctuations and control challenges.
- 3. RTD Sensor Limitations: The sensor's driving current was limited to 1 mA to minimize response delays to temperature changes. Tests with varying currents (0.1, 0.5, 1 mA) showed that 0.5 mA was optimal for balancing response time and minimizing heating, which was offset by the TEC.
- 4. TEC Challenges: Two issues were identified with the TEC. First, the 25-watt resistor used for current and voltage control overheated, addressed either by adding a small heat sink or modifying the TEC driver to eliminate the resistor. Second, condensation formed on the housing box due to ambient temperature variations, posing a risk to the TEC.

In the initial experimental test, the laser diode cooling system's robustness and the PID algorithm were evaluated without the optical unit. The testing procedure involved setting a desired temperature in the program, optimizing proportional and derivative gains, tuning the integral gain to reduce steady state error and overshoot while damping oscillations, and documenting the optimal integral gain in the program. This process was repeated across a temperature range of 0 to 26°C.

Ziegler-Nichols tuning PID algorithm integrated by antiwindup is as follows:

- Step 1. Initialize a duty cycle, desired temperature, PID gains, and other parameters of the PID.
- Step 2. Read a temperature (*Treal Tdesired*), modified duty cycle (DuC), and modified PID gains Ki, Kp, Kd.
- Step 3. Choose integral gain Ki dependence on the desired temperature from the integral gain table.
- Step 4. Calculate the Error e(t) = Treal Tdesired.
- Step 5. If /e(t)/ between 0.2 and 0.1, go to the next step, else go to after the next step.
- Step 6. Calculate $\mu = \mu 1$, then skip the next step.
- Step 7. If /e(t)/<0.1, calculate $\mu = \mu 2$, else $\mu = 0$.
- Step 8. Modify the PID gains, $Kp = Kp(1 + \mu * e(t))$, $Ki = Ki(1 + \mu * e(t))$, $Kd = Kd(1 + \mu * e(t))$.
- Step 9. Calculate the PID gains, P(t) = Kp * e(t), K(t)= Ki * e(t) + K(t-1), D(t) = Kd * (e(t) - e(t - 1)).
- Step 10. Calculate the Duty Cycle, DuC=P(t)+K(t)+D(t).
- Step 11. If DuC >0.9, calculate the integral gain K(t) = K(t-1) + (0.9 DuC) and DuC = 0.9 and then skip the next step; if not, go to the next step.
- Step 12. If DuC <0.1, Calculate the integral gain K(t) = K(t-1)+(0.1-DuC) and DuC = 0.1; otherwise, go to the next step.
- Step 13. Generate PWM using the duty cycle value.
- Step 14. Return the PID gains and errors to the beginning of the PID program in Step 2.

Table 1. Controller test parameters without the laser diode

Desired Temp	. Averaged Temp.	Steady State	Fluctuation
(°C)	(°C)	Error (°C)	(°C)
10	10.3561	-0.3561	0.1006
11	11.0396	-0.0396	0.0742
12	12.0087	-0.0087	0.0722
13	12.9899	0.0101	0.0730
14	13.9776	0.0224	0.0630
15	14.9529	0.0471	0.0653
16	15.9406	0.0594	0.0543
17	16.9423	0.0577	0.0667
18	17.9600	0.0400	0.0325
19	18.9456	0.0544	0.0256
20	19.8995	0.1005	0.0532

In our study, the temperature control system was designed for a wide 0 to 26°C range, but testing focused on the 10 to 20°C range, aligning with the laser diode's optimal operating temperatures. This approach ensured thorough evaluation of the system's performance under conditions most critical for the diode's functionality. Tests using a yellow hot lamp and human body temperature demonstrated the system's rapid response in returning to setpoints, attributed to the effective integral PID gain. This gain minimizes the overshoot, dampens oscillations below 0.1°C, and maintains a low steady-state error. Figure 7 illustrates these tests at 20°C, presenting data on ADC sampling values, voltage difference on the Wheatstone bridge, and the RTD sensor's temperature readings. At 20°C, temperature fluctuations were 0.0256 (standard deviation) and steady-state error was 0.0544 (average temperature deviation from setpoint), with the red line in Figure 7 representing the average temperature. These tests also emphasized the importance of time-dependent temperature stability.

The system's time to reach steady state from room temperature varies between 1-2 minutes, faster than the reverse process. Stability was confirmed over 60 minutes, with fluctuations and steady state errors under 0.1, as detailed in Table 1. The controller's performance was less optimal at a 10°C range, with a resolution of 0.1 and a steady state error of 0.3561. Figure 8 shows another example of the desired temperature of 16°C that is read by the controller. Table 1 presents user-selected temperatures and corresponding averaged measured temperatures for error and fluctuation analysis. System testing without a laser diode met all

objectives, as shown in Table 1. A DFB laser diode was then tested, placed in the housing box and connected to an interferometer system via optical fiber, with insulated wiring to minimize electromagnetic interference. The interferometer system, using a natural method, indirectly measures wavelength stability of the laser diode. The system setup involves a common path interferometer (Figure 9) and uses a 1310 nm wavelength. Measurements were taken on a plane mirror, averaging height values over 2000 readings, and repeated ten times for reliability, as shown in Table 2. Temperature-induced variations were observed, as demonstrated in Figure 10.



Figure 7. The desired temperature 20°C reads by controller



Figure 8. The desired temperature 16°C reads by controller



Figure 9. Sketch of the common path interferometer

Eq. (8) defines the absolute height $z (\varphi, \lambda)$ relative to the reference plane, addressing the phase ambiguity issue as initially derived in 1.

$$z(\varphi,\lambda) = \frac{\varphi\lambda}{4\pi} \tag{8}$$

To calculate wavelength change $(\Delta\lambda)$, the absolute height equation is analyzed, and the absolute height measured. Measurement suitability is assessed using the law of propagation of uncertainty, with the general absolute error defined accordingly.

$$\Delta y = \frac{\partial y}{\partial x_1} \Delta x_1 + \frac{\partial y}{\partial x_2} \Delta x_2 + \cdots$$
(9)

Applying Eq. (8) under the assumption of a constant phase to Eq. (9) yields the following result:

$$\Delta z(\phi,\lambda) = \frac{\phi \Delta \lambda}{4\pi} \tag{10}$$

The substitution of Eq. (8) into Eq. (10) eliminates the phase, yielding a general equation employed in this study.

$$\Delta z(\phi, \lambda) = \frac{\Delta \lambda}{\lambda} z(\phi, \lambda) \tag{11}$$

The altered absolute heights were assessed at two distinct temperatures, T1 and T2, facilitating the estimation of $\Delta\lambda$ and subsequent reorganization of Eq. (11).

$$\Delta \lambda = \frac{\lambda_1}{z_{T1}(\varphi, \lambda)} \left(z_{T1}(\varphi, \lambda_1) - z_{T2}(\varphi, \lambda_2) \right)$$
(12)

Eq. (12) involves the calculation of the change in wavelength ($\Delta\lambda$) by measuring the height difference (Δz) of a micro-optical probe in an interferometer system. At a fixed position called the zero position, the physical height of the micro-optical probe (z) remains constant at 10 µm (is assumed as $zT \ 1 (\varphi, \lambda)$), assumed to be the same at all temperatures. The interferometer system measures the virtual height between the probe and the object, enabling the determination of the laser diode's wavelength, which is 1310 nm. To calculate the wavelength change, the parameters mentioned above are used in Eq. (12).

$$\Delta \lambda = 131 \times 10^{-3} \Delta z \tag{13}$$



Figure 10. Variations in object measurement corresponding to a 1°C change in temperature



Figure 11. Effects of temperature change 1°C on the wavelength of the laser diode



Figure 12. Temperature results from the controller using the DFB laser diode (15°C and 19°C)

The cooling system's efficacy is evaluated via a laser diode, measuring temperature variations at distinct points. Measurements start at 20°C, decreasing in 1°C intervals every 10 seconds. Figure 11 shows the temperature's effect on the laser diode's wavelength, with a maximum and minimum wavelength shift of 0.1262 nm and 0.092 nm. Our results validate a 0.1 nm per °C wavelength change in the DBF laser diode, aligning with datasheet specifications. Notably, minimal wavelength variation occurs at 10°C, indicating negligible impact of 1 °C change at lower temperatures.

In the final testing phase, the laser diode cooling system's performance is assessed by monitoring temperature changes during operation. Figure 12 presents temperature data from 10°C to 20°C, processed by Matlab. Table 3 shows the standard deviation and steady-state error, revealing optimal precision at 11°C (\mp 0.0013°C), followed by 10°C (\mp 0.0042°C). Contrasting with a previous study (6) that used a costlier DFB laser diode with two built-in thermoelectric coolers, our more affordable laser diode, lacking these coolers, yielded surprising and superior results.

The second experiment, utilizing a laser diode, surpasses the initial test due to two key factors: complete enclosure of the laser diode by an aluminum housing and the inherently low power of most DFB laser diodes, as previously discussed.

Table 2. Measurement of object height at a constanttemperature of $T=22.4^{\circ}C$

Height Difference Optical	Height Difference Piezo	
(nm)	(nm)	
94575.4324	94913.5325	
94599.7348	94910.1466	
94638.8753	94905.7947	

Table 3 Controller test parameters the laser diode

Desired Temp. (°C)	Averaged Temp. (°C)	Steady State Error (°C)	Fluctuation (°C)
10	9.9958	0.0042	0.0473
11	10.9987	0.0013	0.0567
12	11.9765	0.0235	0.0594
13	12.9890	0.0104	0.0358
14	13.9891	0.0108	0.0441
15	14.9779	0.0221	0.0331
16	15.9719	0.0281	0.0287
17	16.9633	0.0366	0.0266
18	17.9641	0.0248	0.0359
19	18.9713	0.0287	0.0334

6. CONCLUSION

This work presents a comprehensive study on the design and implementation of a temperature controller for a laser diode in a fiber-optic interferometer system. The technology's benefits are primarily its simplicity, cost-effectiveness, fast response, and ability to address specific temperature issues associated with the fiber-optic interferometer. This work aims to develop a user-friendly laser diode temperature controller with minimal effort. It will ensure a consistent temperature for the diode by employing two key principles: cost-effective components and straightforward circuits, and precise temperature measurements. The design of the temperature controller is simple and this simplicity reduces the risk of system overhead and potential noise, which could negatively impact the performance of the interferometer.

This research describes a method for controlling temperature within a program. It encompasses manually setting the target temperature, fine-tuning both the proportional and derivative gains, and adjusting the PID's integral gain to reduce both steady-state error and overshoot. The optimal integral gain is then tabulated. A simple PID controller, used with an integral gains table and adapted gains, mitigates nonlinear behavior and saturation. The system, enclosed in an aluminum box to minimize noise and air impact, employs a low-power DFB laser diode known for high wavelength stability. A variable resistance can adjust the maximum temperature, balanced against the sensor's temperature resolution. Furthermore, the study established a correlation between the shift in wavelength due to temperature variations and the virtual distance between the probe and the object.

Significantly, the study has demonstrated quantifiable improvements in the performance of the fiber-optic interferometer system through experimental evaluations. The temperature controller consistently maintained the diode temperature, as evidenced by a range of steady-state errors spanning from a low of 0.0087°C to a high of 0.3561°C, with corresponding temperature resolution improvements. The conducted experiments, performed both with and without the utilization of a laser, substantiated the efficiency of the proposed design. The observed range of steady-state errors spanned from 0.3561°C at 10°C (in the worst-case scenario) to 0.0087°C at 12°C (in the best-case scenario). Similarly, the temperature resolution varied from 0.1006°C (worst case) to 0.0331°C (best case), thereby corroborating the success of the research. The study effectively meets the initial research questions and presents a practical solution to a major challenge in fiber-optic interferometry. The innovative and effective design of the LDTC establishes a new benchmark, leading to more reliable and precise optical measurement systems.

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