Self Tuning Filter Based Fuzzy Logic Controller for Active Power Filter

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1. INTRODUCTION

In recent years, the world has experienced a sharp increase in electrical disturbances due to the increasing use of non-linear loads. These absorb non-sinusoidal currents and therefore produce harmonic pollution current in the grid electrical causing damage to other devices; hence a custom power device should be used to mitigate this pollution harmonic [1-6]. Among the traditional solutions, the passive power filter (PPF). It eliminates current harmonics and improves reactive energy compensation, but it has many disadvantages such as the series and parallel resonance with the system impedance which can step-up the harmonic voltage and current at specified locations [7]. The active power filter (APF) that can be either a FACTS-based device is a well-developed solution to ensure reactive power compensation and harmonic mitigation [8-10].

The performance of the APF depends on the identification strategy of the reference currents and their control. Several control strategies for the extraction of reference currents have been proposed in the literature, especially the widely used p-q theory and the id-iq theory [11]. However, the performance of APF is poor under unbalanced and distorted source voltage conditions for both strategies [11].

In this paper, a control strategy based on self-tuning filter (STF) is used for extraction of the reference harmonic currents. The major advantages of the STF are cited in the paper [12].

The STF filter extracts the fundamental component directly from the load currents and the source voltages in α-β reference frame while fixing two important parameters, namely, the pulsation \( \omega \) and the proportional gain \( K \), however, no technique existed in the literature for identification of the proportional parameter [13]. For this purpose, a fuzzy regulator has been associated with the STF filter in order to adjust in real time its proportional parameter \( K \) to obtain a perfect extraction of the reference harmonic currents.

The control of the reference currents is accomplished by a hysteresis current controller. The hysteresis control is a simple implemented classical method, it ensures a satisfactory control of the current without requiring a thorough knowledge of the model of the system to be controlled [13, 14]. The purpose of this study is to adjust the \( K \) parameter of the STF filter by a FLC controller to obtain a better improvement of the quality of the electric currents in a transmission line feeding a nonlinear load.

2. SYSTEM CONFIGURATION

The general structure of the three-phase APF is presented in Figure 1.

Figure 1. General structure of the active power filter
The studied system is a transmission line composed of a three-phase source supplying a non-linear load. The parallel active filter is inserted between the power source and the non-linear load.

The nonlinear load is composed of a three full bridge rectifier supplying an inductive load RL. Figure 2 shows the system developed under Matlab/Simulink® environment.

### 3. CONTROL STRATEGY

The control strategy of the APF can be divided into two steps: The first step is the extraction of the reference currents by the STF filter equipped by the FLC controller. The second one is the control of the reference currents by the hysteresis strategy.

#### 3.1 Harmonic currents identification

In order to have a good performance of the APF, the control strategy for the extraction of the reference current harmonics is based on the STF filter. The STF filter extracts the fundamental component directly from the load currents.

The transformation of three-phase voltages and currents in the α-β frame is given by the following expressions:

\[
\begin{align*}
\left[ \begin{array}{c}
 v_a \\
 v_b \\
 v_c 
\end{array} \right] &= \frac{2}{3} \left[ \begin{array}{ccc}
 1 & 0 & 0 \\
 -\frac{1}{2} & \frac{1}{2} & \sqrt{3} \\
 -\frac{1}{2} & \frac{1}{2} & -\sqrt{3} 
\end{array} \right]
\left[ \begin{array}{c}
 v_a \\
 v_b \\
 v_c 
\end{array} \right] \\
\left[ \begin{array}{c}
 i_a \\
 i_b \\
 i_c 
\end{array} \right] &= \frac{2}{3} \left[ \begin{array}{ccc}
 1 & 0 & 0 \\
 -\frac{1}{2} & \frac{1}{2} & \sqrt{3} \\
 -\frac{1}{2} & \frac{1}{2} & -\sqrt{3} 
\end{array} \right]
\left[ \begin{array}{c}
 i_a \\
 i_b \\
 i_c 
\end{array} \right] 
\end{align*}
\]

The instantaneous active power \( p \) and the instantaneous reactive power \( q \) are defined by [16]:

\[
\begin{align*}
 p &= \frac{1}{2} \left[ v_a i_a + v_b i_b + v_c i_c \right] \\
 q &= \frac{1}{2} \left[ v_a i_b - v_b i_a \right] 
\end{align*}
\]

The reference harmonic currents denoted \( i_{\alpha-ref} \) and \( i_{\beta-ref} \) are expressed in the α-β reference frame by:

\[
\begin{align*}
\left[ \begin{array}{c}
 i_{\alpha-ref} \\
 i_{\beta-ref} 
\end{array} \right] &= \frac{1}{v_a^2 + v_b^2 + v_c^2} \left[ \begin{array}{ccc}
 v_a & -v_b & \sqrt{3} v_c \\
 1 & 0 & 0 \\
 v_a & v_b & 0 
\end{array} \right]
\left[ \begin{array}{c}
 \bar{p} \\
 \bar{q} 
\end{array} \right]
\end{align*}
\]
The reference harmonic currents in the three phase reference frame can be determined using the inverse Concordia transformation as follows:

\[
\begin{bmatrix}
i_{a-ref} \\
i_{b-ref} \\
i_{c-ref}
\end{bmatrix} = \begin{bmatrix}
\frac{2}{\sqrt{3}} & -\frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\
-\frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & 0 \\
\frac{1}{\sqrt{3}} & \frac{1}{\sqrt{3}} & \frac{1}{\sqrt{3}}
\end{bmatrix}
\begin{bmatrix}
i_{a} \\
i_{b} \\
i_{c}
\end{bmatrix}
\] 

(10)

The principle of the STF consists of extracting the fundamental components \(i_{a0}\) at the pulsation \(\omega_c\) of the currents \(i_{abc}\). The harmonic components are calculated by subtracting the STF outputs from the corresponding input signals (Figure 4).

![Figure 3. Extraction of reference current harmonics with STF filter](image)

The authors [13] presented the expressions of the fundamental components \(\hat{X}_{a0}\) according to the real quantities \(X_{a0}\), \((X\) can be a current or a voltage):

\[
\begin{align*}
\hat{X}_a(s) &= \frac{K}{s} \left( X_a(s) - \hat{X}_a(s) \right) + \frac{\omega_c}{s} \hat{X}_p(s) \\
\hat{X}_p(s) &= \frac{K}{s} \left( X_p(s) - \hat{X}_p(s) \right) + \frac{\omega_c}{s} \hat{X}_a(s)
\end{align*}
\]

(11)

In this system of equations the \(K\) parameter plays an important role for the extraction of the fundamental quantities \(\hat{X}_{a0}\), however there is no method which allows its dimensioning. Indeed, a value too low or too important of \(K\) does not allow the filter to effectively eliminate all the harmonics. In this study, a fuzzy controller was associated with the STF filter to adjust the value of the \(K\) parameter based on the operating point of the system. The new FLC-STF filter thus obtained makes it possible to extract the fundamental quantities efficiently and independently of the initial value given to the \(K\) parameter.

3.2 Fuzzy logic controller

This type of controller does not require any knowledge of the mathematical model of the system. It is composed of four main blocks (Figure 5): the knowledge base, the inference system, the fuzzification interface and the defuzzification interface [17-20].

![Figure 5. Synoptic diagram of the fuzzy controller](image)

3.2.1 FLC-STFController configuration

The purpose of the FLC-STF filter developed in this study (Figure 6) is to extract the fundamental quantities \(\hat{i}_{a0}\) necessary in the identification phase of the reference harmonic currents. The role of the FLC regulator is to adjust in real time the parameter \(K\) of the STF filter to extract the harmonics of currents as much as possible.

A study was conducted to determine the effect of the \(K\) parameter of the STF filter on low order current harmonics (Figure 7). This study consists to varying the \(K\) parameter and calculating, for each value of \(K\), the amplitudes of the 3rd order harmonic (h3), the 5th order harmonic (h5) and the 7th order harmonic (h7), and the corresponding THD while exploiting the potential of the MATLAB/Simulink® software. Figure 7 and Figure 8 show, respectively, the variations of the THD and the magnitudes of some low order harmonics of current as functions of \(K\) parameter of the STF filter for a non linear load composed of a three phase full bridge rectifier with an RL connection.
This study showed that $K$ parameter can affect three parameters, which are: the THD, $h_5$, and $h_7$. The 3rd harmonic is not affected by the variations applied to $K$ parameter.

For this purpose, the designed FLC controller has three input variables are: THD, $h_5$ and $h_7$, then the output variable of the FLC is the $K$ parameter of the STF filter.

The fuzzy controller is characterized by:
- Three membership functions for each variable (inputs and output) as can be seen in Figure 9.
- Fuzzification based on min-max operator.
- Defuzzification based on center gravity method

The inference system consists of five rules as follows:
- If THD is small AND $h_5$ is medium AND $h_7$ is medium THEN $K$ is large.
- If $h_5$ is small AND $h_7$ is small THEN $K$ is large.
- If $h_5$ is large AND $h_7$ is large THEN $K$ is large.

In the hysteresis current controller presented by Figure 10, the current produced by the filter $i_f$ is forced to follow reference current $i_{ref}$ within a hysteresis band by the switching action of the inverter.

The hysteresis controller is fed by the error signal, which is the difference between the reference current $i_{ref}$ and the current produced by the filter $i_f$. This error is compared with a fixed tolerance band in order to driving the switching signals [21-23].

The control of the DC-link capacitor voltage is necessary to improve the compensation performance of the APF. In fact, the DC-link capacitor voltage control maintains the voltage $V_{dc}$ at constant value [11]. This voltage is compared with its reference $V_{dc-ref}$ and then the error voltage is applied to the input of a standard type PI corrector as shown in Figure 11. According to the error between the reference voltage and the DC capacitor voltage, the PI controller generates a signal proportional to the power absorbed or supplied by the DC link capacitor [24-26]. Then it is added to the harmonic component of active power.
4. SIMULATION RESULTS

The performance of the FLC-STF scheme for APF filter has been tested and evaluated by using SimPowerSystems toolbox in MATLAB/Simulink® software.

The APF system studied is presented in Figure 2. The circuit parameters are given in Table 2. The nonlinear load is consisting of a three-phase diode bridge rectifier with RL connection.

The following figures show the simulation results of the system illustrated in Figure 2.

Figure 12 shows the waveform of the line current absorbed by the nonlinear load consisting of three-phase rectifier with RL connection. In Figure 13, the THD spectrum is presented. It reaches a value of 22.03%.

Figure 14 shows the waveform of the source current and its THD spectrum respectively, when the mitigation of harmonic currents is ensured by using the FLC-STF scheme for the active power filter for the same nonlinear load. The THD of the source current is computed as 0.86%.

The APF generates a harmonic current illustrated in Figure 16. This harmonic current is injected in the transmission line to ensuring the demand of the nonlinear load for harmonic current; thereby the source generates only a sinusoidal current.

In Figure 17, it is presented the variation of STF proportional parameter obtained by the FLC controller when the initial value of $K$ parameter is set at 30. It is clear that the FLC controller adjusts, in real time, its output and therefore the $K$ parameter in order to improve the THD of the current.

It should be noted that the THD is a function of the initial value of the $K$ parameter. For this purpose, it is presented in Figure 18 three cases correspond to three initial values of $K$ for the same nonlinear load. It is clear from this figure that the
THD can be affected by the initial value of $K$. In other hand, it was observed that a very small value of $K$ can generate a critical transient behavior characterized by a very large current peak. However, the THD obtained with FLC-STF scheme for any initial value of $K$ is greater than that obtained with the STF filter alone.

The curve of the DC link capacitor voltage is presented in Figure 19. The capacitor voltage is controlled at 700 volts with a PI type controller as shown in Figure 11. It is clear from this figure that the DC link capacitor voltage follows the reference voltage $V_{dcref}$ efficiently.

In order to show the efficiency of the proposed scheme, it is illustrated in Table 1 the values of THD parameter and magnitudes of the 5th, 7th and 11th order harmonics for three cases; the first one corresponds to the case in which no filter is used to mitigate the harmonic currents. In the second one, the identification method for the APF filter is based on a STF alone and in the last one the identification method is based on FLC-STF scheme, in this case the initial value of the $K$ parameter was set at 60 and the fundamental component of the current reached an amplitude of 78.52 A.

From these results, it can be seen that the control scheme proposed in this paper improves the mitigation of harmonic currents and therefore it enhances the compensation performance of the APF.

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Values</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply voltage</td>
<td>$V_s = 220$ V, $f = 50$ Hz</td>
</tr>
<tr>
<td>The source side line parameters</td>
<td>$L_s = 0.02$ mH, $R_s = 3.5$ mΩ</td>
</tr>
<tr>
<td>Load parameters</td>
<td>$L_f = 0.2$ mH, $R_f = 3$ mΩ</td>
</tr>
<tr>
<td>The load side line parameters</td>
<td>$L_r = 0.1$ mH, $R_r = 3$ mΩ</td>
</tr>
<tr>
<td>The APF: Interfacing bloc</td>
<td>$V_{dc} = 700$ V, $C_{dc} = 2.2$ mF</td>
</tr>
<tr>
<td>DC bus</td>
<td>$H_c = ±0.1$ A</td>
</tr>
<tr>
<td>Hysteresis band</td>
<td></td>
</tr>
</tbody>
</table>

5. CONCLUSION

The work presented in this paper has proposed an improved method for identifying reference harmonic currents based on the use of an STF filter in place of conventional filters (low pass or high pass) used in the instantaneous power theory. The STF filter was endowed with an FLC controller to adjust, in real time, its proportional parameter. The main objective was to generate the reference harmonic currents with a minimum of error to compensate the disturbances caused by the nonlinear loads. For current control, the classic hysteresis control was used. The FLC-STF scheme has allowed obtaining a perfect and efficient extraction of harmonic currents.

The simulation results demonstrated and enhanced the efficiency of using the FLC-STF scheme and the hysteresis current controller in the APF filter control.

REFERENCES

filter (APF) topology. IEEE Transactions on Power Electronics, 17(1): 48-54. https://doi.org/10.1109/63.988669


